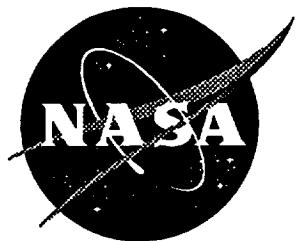


NASA Contractor Report 198313



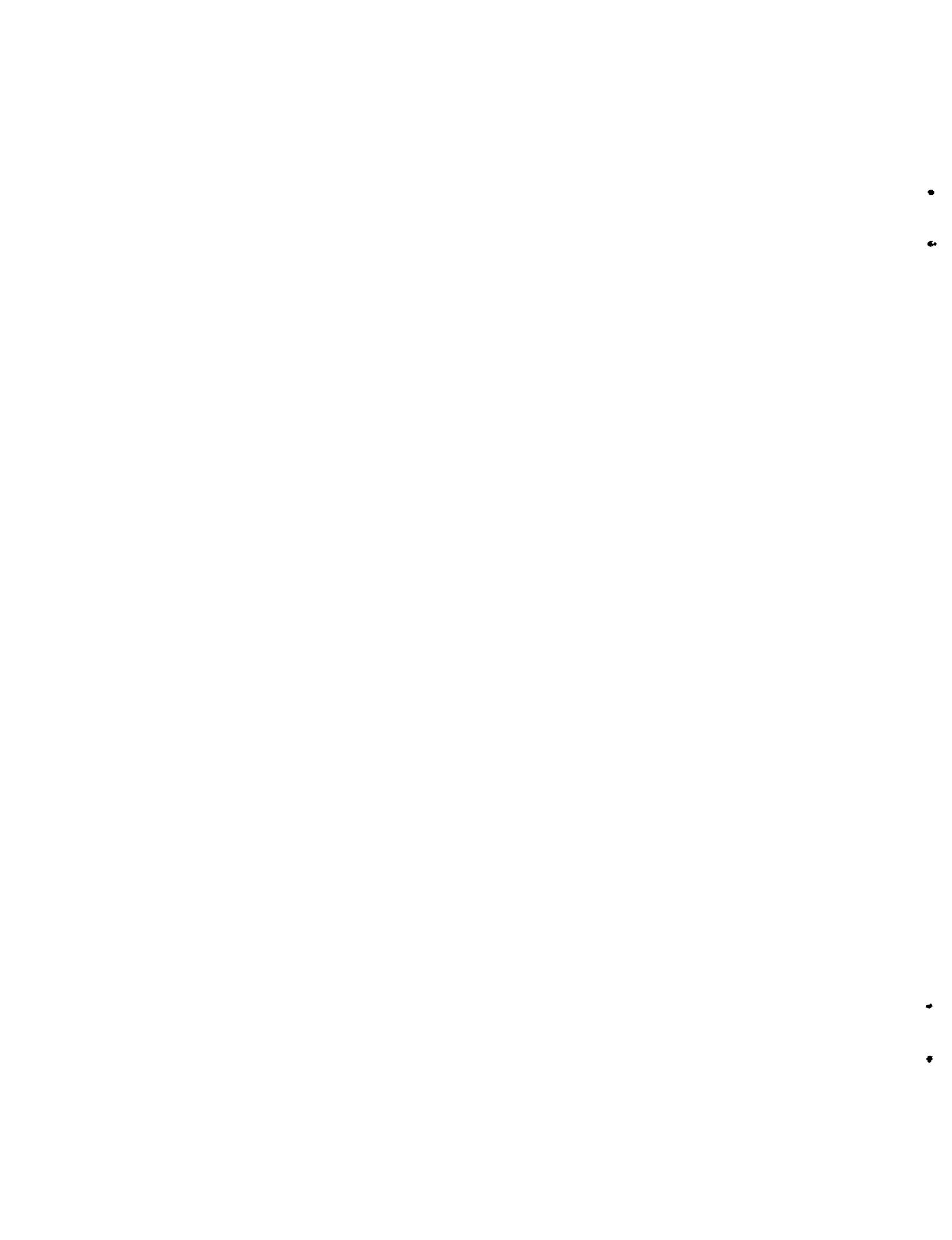
Performance Validation of Version 154.0 ANSER Control Laws for the F-18 HARV

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Contract NAS1-19000

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National Aeronautics and
Space Administration
Langley Research Center
Hampton, Virginia 23681-0001



Overview

A new software release designated version 154.0 was developed and tested for use on the F/A-18 High Angle-of-Attack Research Vehicle (HARV). The software release incorporates all software changes to the Actuated Nose Strakes for Enhanced Rolling (ANSER) control laws through version 153.0 with the following exceptions. The first exception is a modification of the On-Board Excitation System (OBES), which replaces the OBES load in version 153.0 with 17 new maneuvers. The second exception is the modification to change the symmetric strake deployment schedule back to the original schedule in ANSER control law version 151.1. The final modification replaces two instrumentation parameters. The three modifications make up the new software release for use in the Research Flight Control System (RFCS) flight control computer.

Reasons for Change

A brief description of the reasons for changes to the control laws follows:

1. The symmetric strake deployment schedule was changed to make the closed-loop parameter identification results obtained with OBES maneuvers in version 154.0 compatible with previous flight test results.
2. Additional closed loop Parameter Identification (PID) and frequency sweep inputs were included to complete the PID test matrix (OBES maneuver 1-7). OBES maneuvers 8-15 were added specifically for flow visualization flight tests. The impulsive Sine and Schroeder Wave inputs in OBES maneuvers 16 and 17 were implemented for the first time in a RFCS software release. A summary of all OBES maneuvers for version 154.0 can be found in Appendix A.
3. A request was made to add two new instrumentation parameters for post flight analysis. Those parameters are lat_pilot_cmd and dir_pilot_cmd, which are internal lateral and directional control law commands, and replace qcfilter1 and qcfilter2, the filtered-impact-pressure parameters.

Testing Performed

All checkcases were run using the Dryden batch simulation release 15a with FORTRAN-coded control laws corresponding to RFCS version 154.0. See Reference 1 for ANSER control law specification. The Hardware-In-the-Loop (HIL) simulator, version 16, was run using the Ada flight code for RFCS version 154.0.

Table 1 contains the weights and inertia's used for the performance validation testing.

Table 1
Aircraft Configuration for Validation

GW lbs	C.G.,fs in	C.G.,bl in	C.G.,wl in	Ixx slug-ft ²	Iyy slug-ft ²	Izz slug-ft ²	Ixz slug-ft ²
35764.6	456.30	0.0	105.4	22632.6	174246.3	189336.4	-2131.8

A subset of twelve checkcases from the ANSER performance-validation checkcases documented in Reference 2 were run to test version 154.0. These checkcases are listed in Table 2.

Plots containing the comparisons between batch and HIL simulations are contained in Appendix B.

Table 2
Closed-Loop Checkcases for V154.0 Performance Validation

Check case	RFCS Mode	Altitude (ft)	Mach Number	AOA (deg)	Load Factor (g's)	Thrust	Inputs
1	TV	25000	0.35	trim	1.0	trim	+3/-2.5 in. ramped long. stick doublet.
4	TV	35000	trim	30.0	1.0	trim	+3/-2.5 in. ramped long. stick doublet.
7	TV	16000	trim	50.0	trim	MAX A/B	+3/-2.5 in. ramped long. stick doublet.
20	S	25000	0.35	trim	1.0	trim	2 in. ramped lateral stick doublet.
22	S	35000	trim	30.0	1.0	trim	+3/-2.5 in. ramped long. stick doublet.
23	S	35000	trim	30.0	1.0	trim	2 in. ramped lateral stick doublet.
26	S	16000	trim	50.0	trim	MAX A/B	2 in. ramped lateral stick doublet.
27	S	16000	trim	50.0	trim	MAX A/B	50 lb ramped pedal doublet.
30	S	40000	0.68	trim	1.0	trim	PLA ramped input to MAX and IDLE. +5 in. (aft) ramped longitudinal stick pulse. +1.5 in. (rt) ramped lateral stick pulse. -50 lb (lt) ramped pedal pulse.
33	S	25000	0.3	trim	1.0	trim	PLA ramped input to MAX and IDLE. +5 in. (aft) ramped longitudinal stick pulse. -3 in. (lt) ramped lateral stick pulse. 50 lb (rt) ramped pedal pulse.
41	TV/1	25000	0.35	trim	1.0	trim	2 in. ramped long. stick doublet.
46	TV/1	25000	0.35	trim	1.0	trim	2 in. ramped long. stick doublet.

Note: TV - Thrust Vector Only Mode, S - Strake Only Mode, TV/1 - Thrust-Vector Only Mode/Dial-a-Gain Option 1.

OBES Maneuvers

All OBES maneuvers were run in the batch simulation, using flight Ada code to check the OBES generated input signals against the OBES specification (References 4, 5, and 6). Closed-loop dynamics were checked to ensure desired aircraft response characteristics in order to achieve acceptable research data. All OBES maneuvers

coded in this software release matched their specification and are acceptable for flight test.

Dial-A-Gain

The Dial-A-Gain options for version 154.0 have not changed from the options available in version 153.0. A summary table of selectable gain sets for all software loads used during Phase III testing are shown below in Table 3.

Table 3
Dial-A-Gain Options for Version 154.0

RFCS Software Loads	DDI Gain Selection 0	DDI Gain Selection 1	DDI Gain Selection 2
Version 150.0	Feedback Gain set 0 (low)	Feedback Gain set 1 (medium)	Feedback Gain set 2 (high)
Version 151.0	Feedback Gain set 0	Feedback Gain set 1	Feedback Gain set 2
Version 151.1	Feedback Gain set 1	Feedback Gain set 0	Feedback Gain set 2
Version 152.0	Feedback Gain set 1	Feedback Gain set 0	Feedback Gain set 2
Version 153.0	Feedback Gain set 1	Not Selectable	Not Selectable
Version 154.0	Feedback Gain set 1	Not Selectable	Not Selectable

Note: DDI - Digital Display Indicator

Summary and Conclusions

The Ada implementation of the ANSER control laws was tested in the HIL simulation and compared with the FORTRAN control-law implementation in batch simulation. There are differences between the HIL and batch simulation results, but the significant differences can be accounted for by known differences in the simulations. (Reference 2, 3, 7, and 8) When the differences are taken into account, the time-history results between simulations are acceptable. The Ada modifications to the ANSER Control Laws perform satisfactorily and are ready for Phase III flight test.

REFERENCES

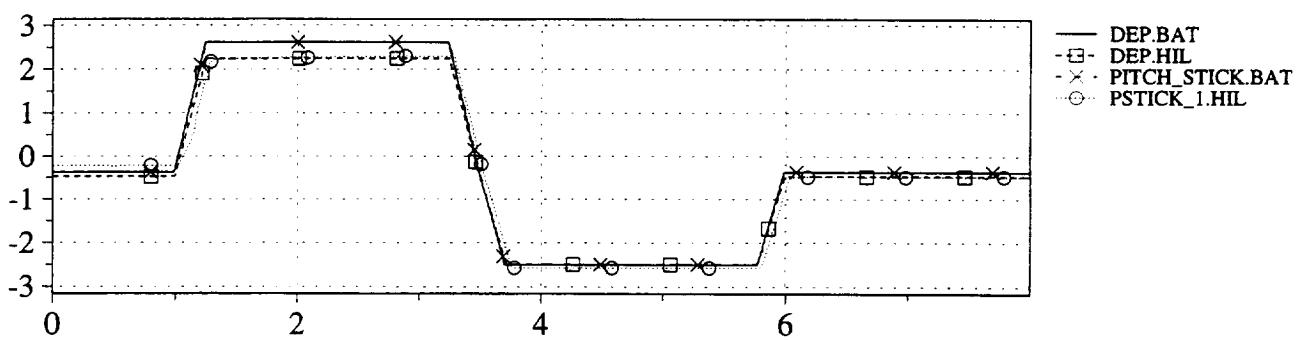
1. HARV Control Law Design Team: *Design Specification for a Thrust-Vectoring, Actuated-Nose-Strake Flight Control Law for the High-Alpha Research Vehicle*, NASA TM-110217, May 1996.
2. Messina, M. D.; *Performance Validation of the ANSER Control Laws for the F-18 HARV*, NASA Contractor Report 198196, August 1995.
3. *Performance Validation of the NASA-1A Control Laws for the F/A-18 HARV Final Report*, Internal High-Alpha Technology Program Document, 21 March 1994.
4. Morelli, E. A.; *Parameter Identification Flight Test Maneuvers for Closed Loop Modeling of the F-18 High Alpha Research Vehicle (HARV)*, NASA Contractor Report 198269, May 1996.
5. Murphy, P. C.; Gallo, B. D.; *ANSER Control Law Validation Inputs: OBES Sine Sweeps for Closed-Loop Identification*, Internal High-Alpha Technology Program Document, 17 August 95.
6. Murri, D. G.; *OBES Requirements - ANSER Aerodynamic Measurements and Flow Visualization*, Internal High-Alpha Technology Program Document, 24 March 94.
7. Messina, M. D.; *Performance Validation of Version 152.0 ANSER Control Laws for the F-18 HARV*, NASA Contractor Report 198250. January 1996.
8. Connelly, P. J.; *Performance Validation of Version 153.0 ANSER Control Laws for the F-18 HARV Test Plan/Final Report*, SPARTA Inc., January 30, 1996.

APPENDIX B

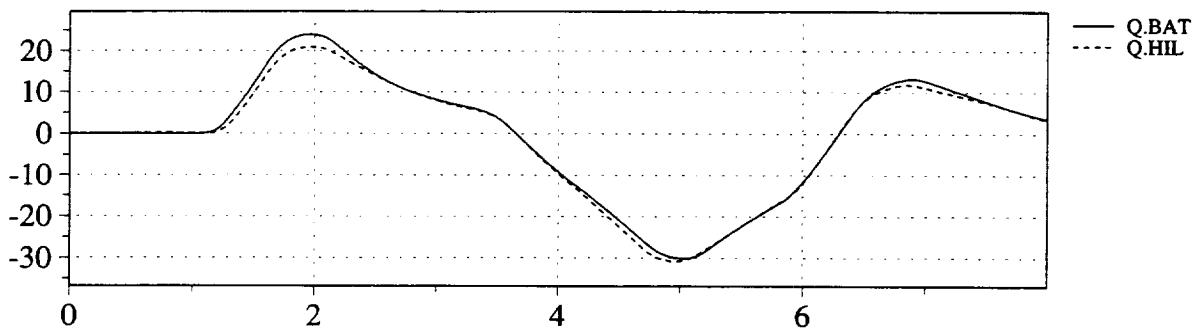
Batch versus Hardware-In-the-Loop Time Histories

Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 1/12

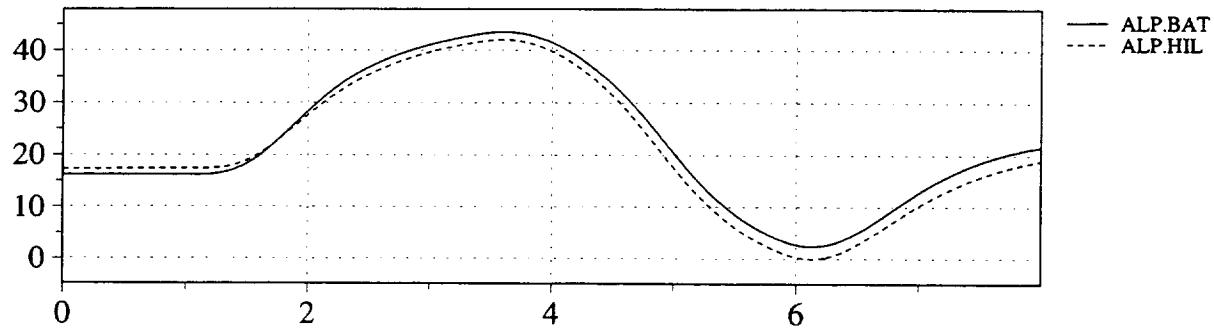
total pit stk cmd, in



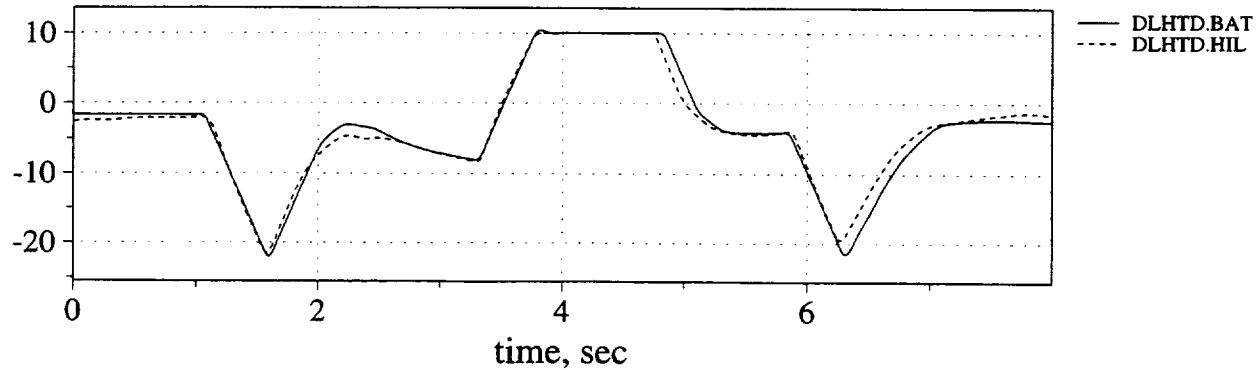
pitch rate, d/s



Angle-of-attack, dg

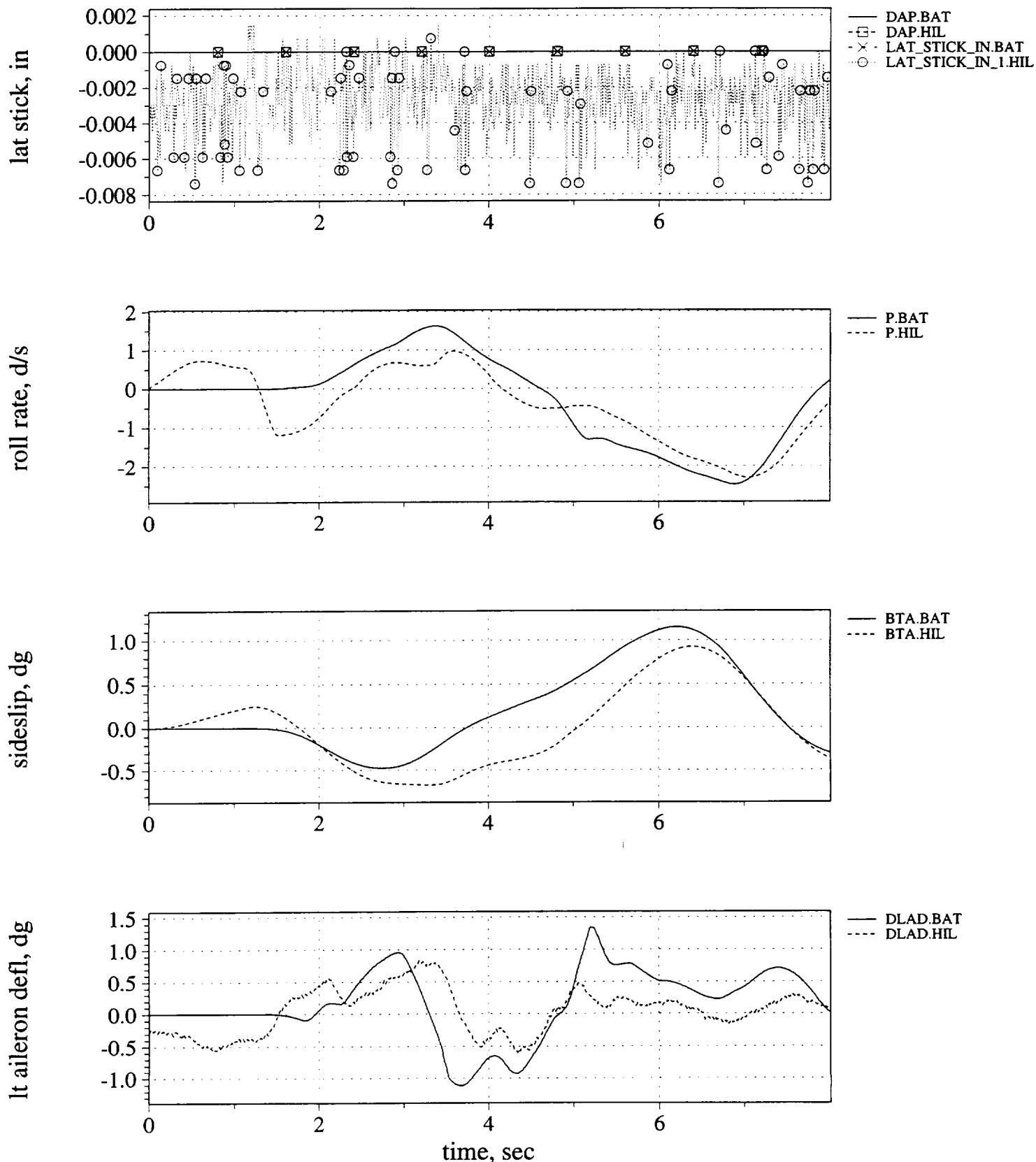


It stab defl, dg



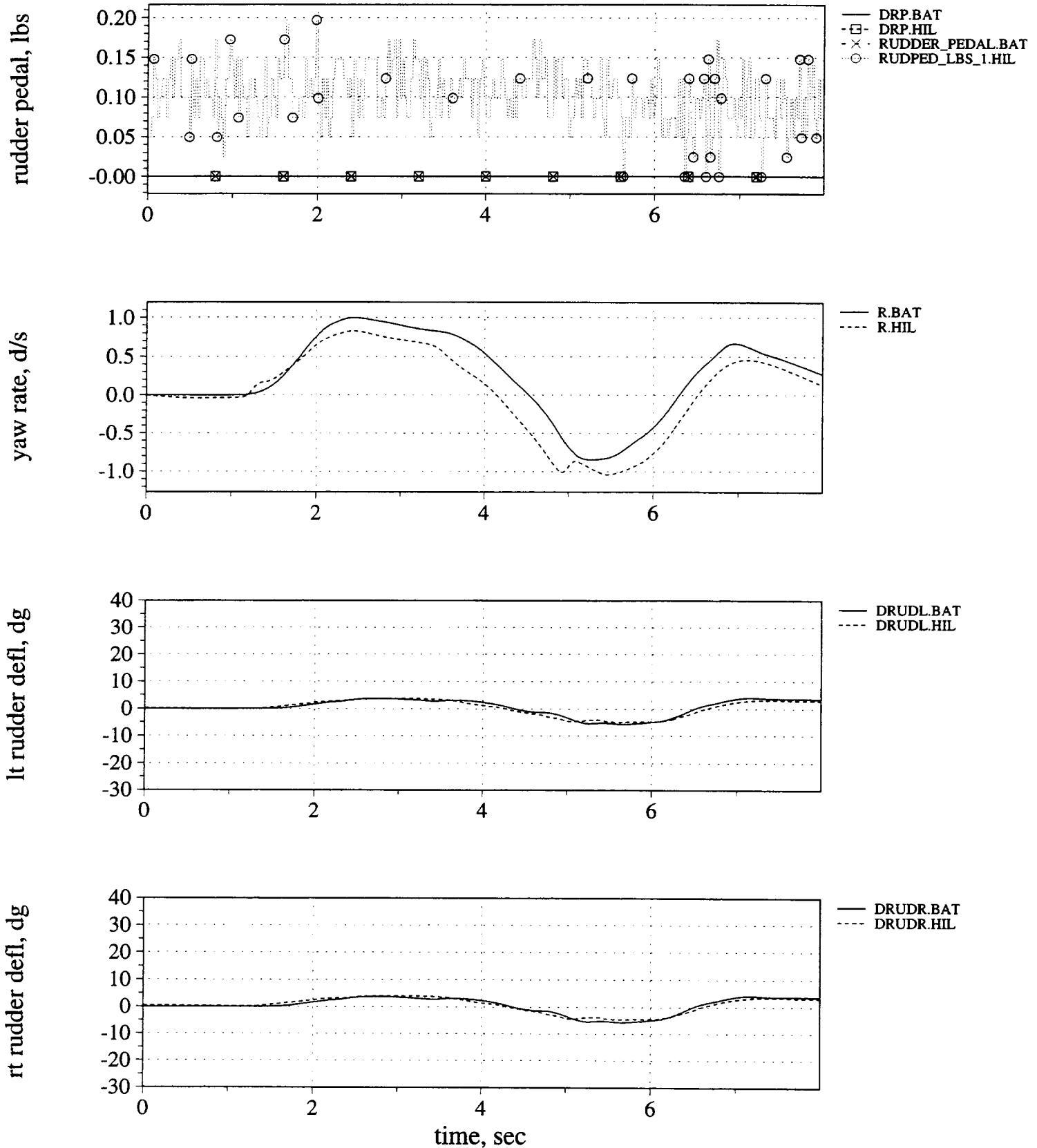
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 2/12



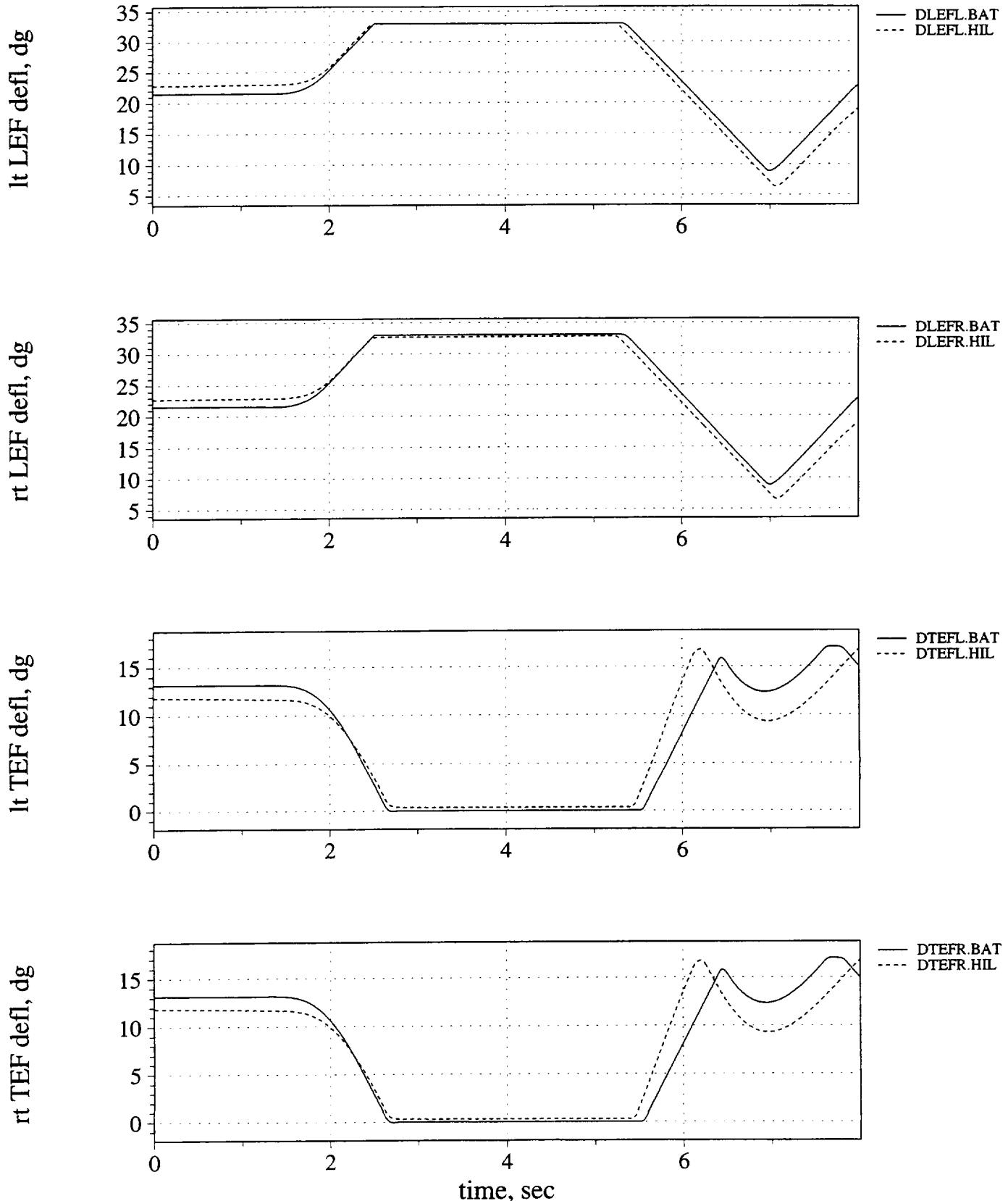
Performance Validation Checkcases

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CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 3/12



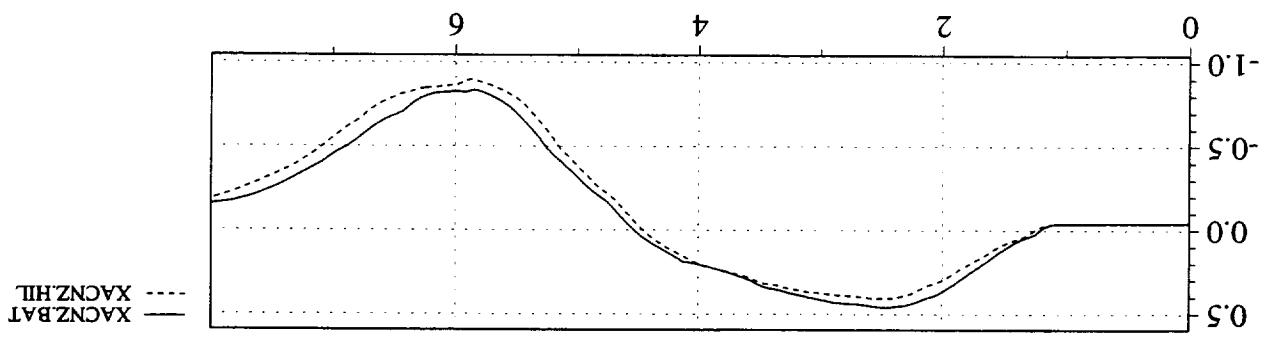
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 4/12

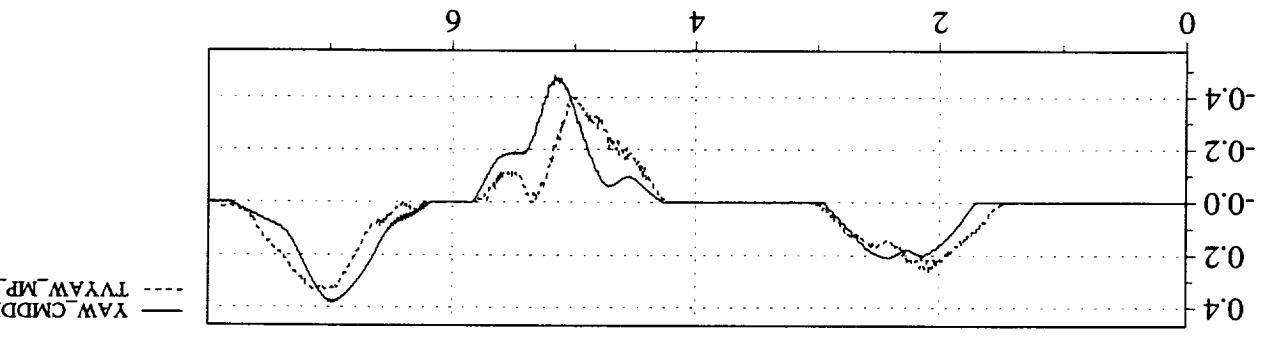


Performance Validation Checkcases

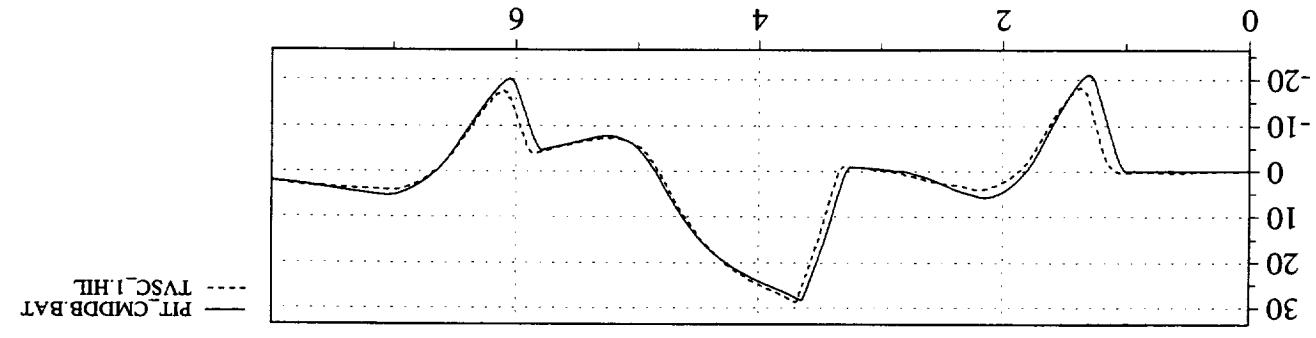
lateral accel, g



normal accel, g



RFCS yaw jet cmd, dg

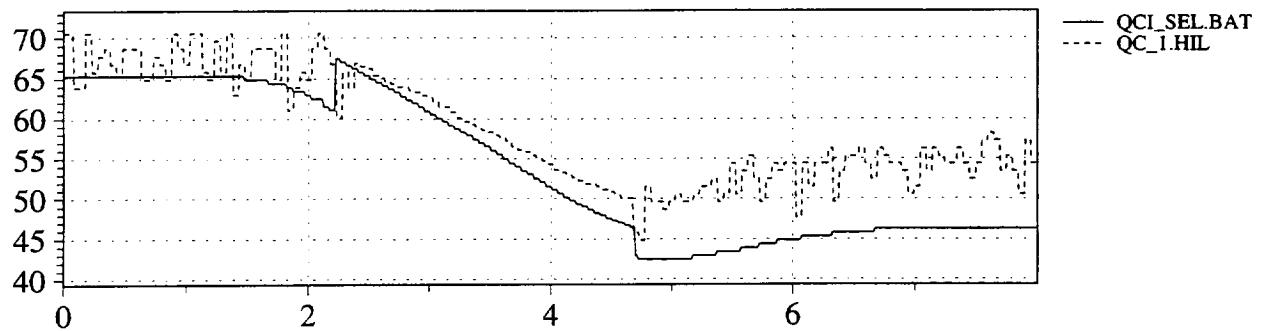


RFCS pit jet cmd, dg

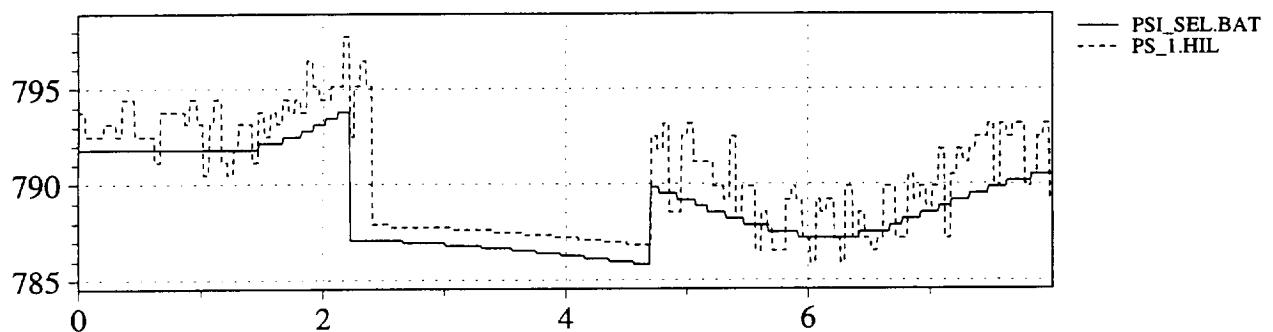
Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_35-25k_trim_AOA_3_-2.5_in_ramp_dep page 5/12

Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 6/12

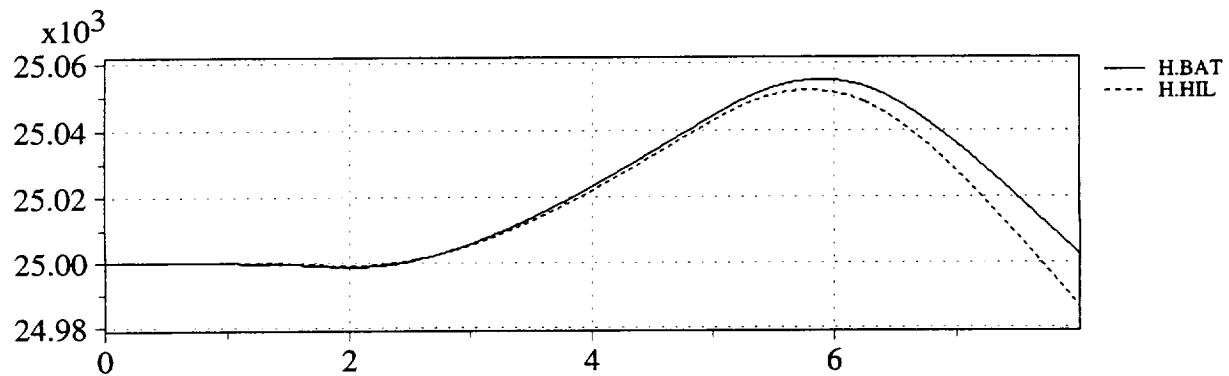
RFCS impact pres, psf



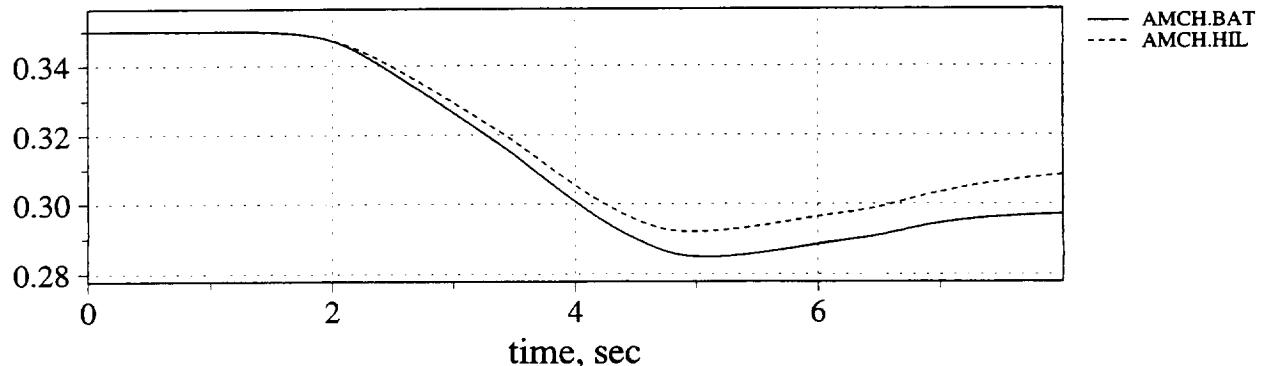
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altitude, ft



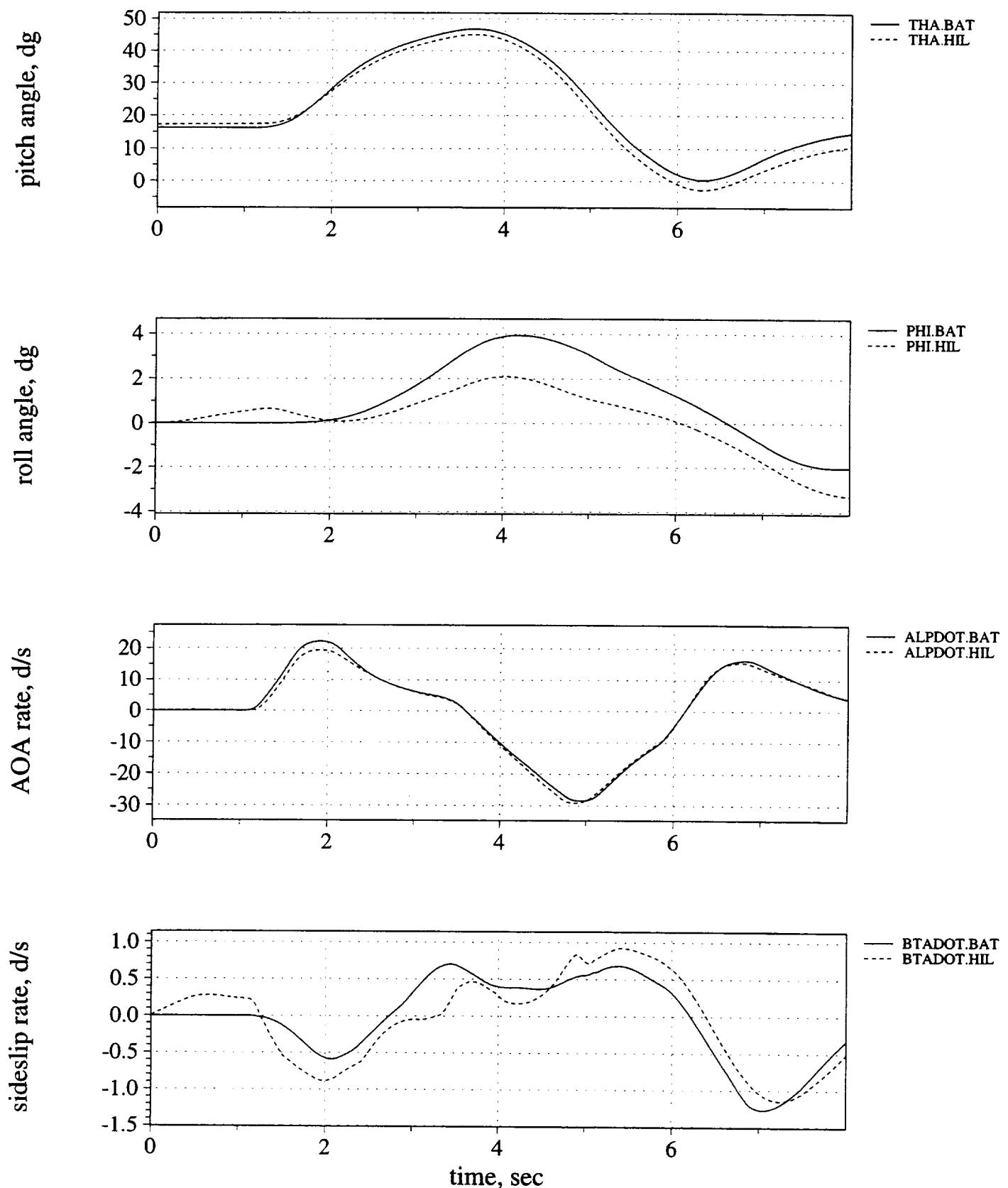
Mach number, nd



Performance Validation Checkcases

page 13

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CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 7/12

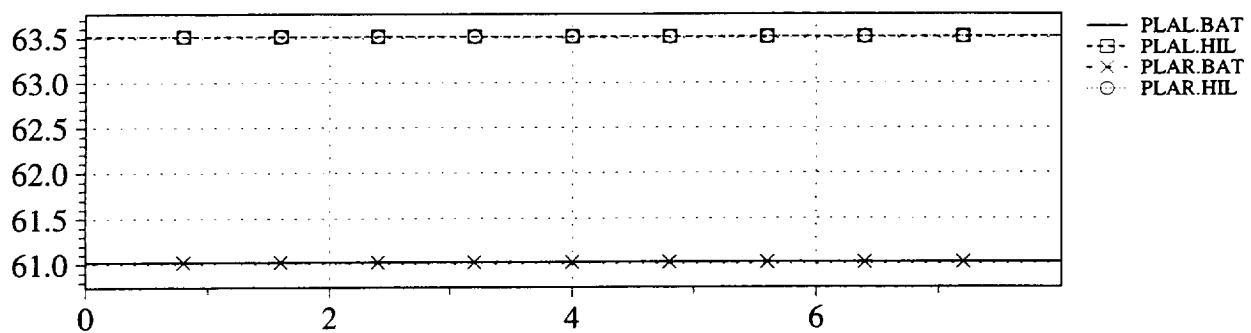


Performance Validation Checkcases

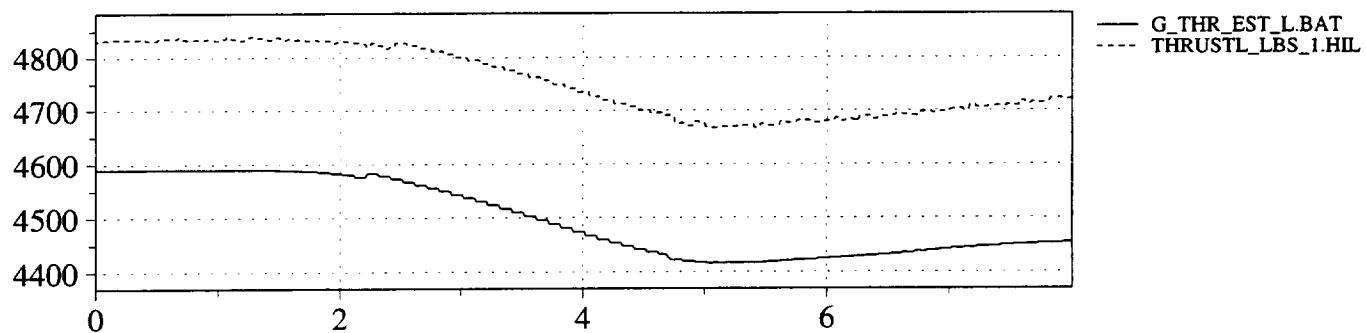
page 14

Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 8/12

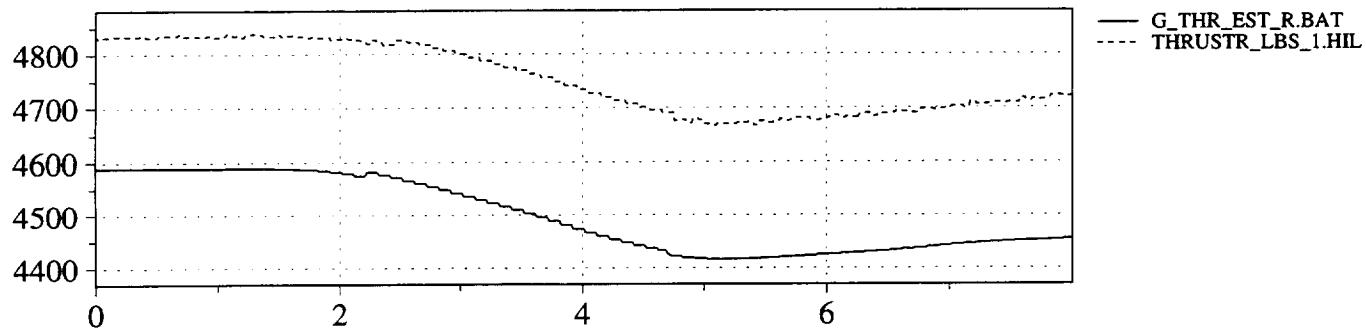
lt/rt power lever ang



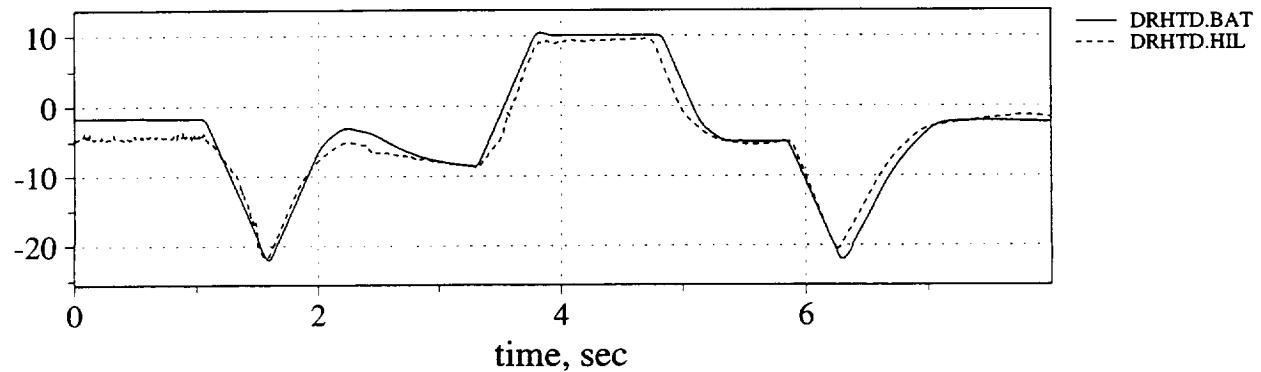
lt gross thr est, lbs



rt gross thr est, lbs



rt stab defl, dg



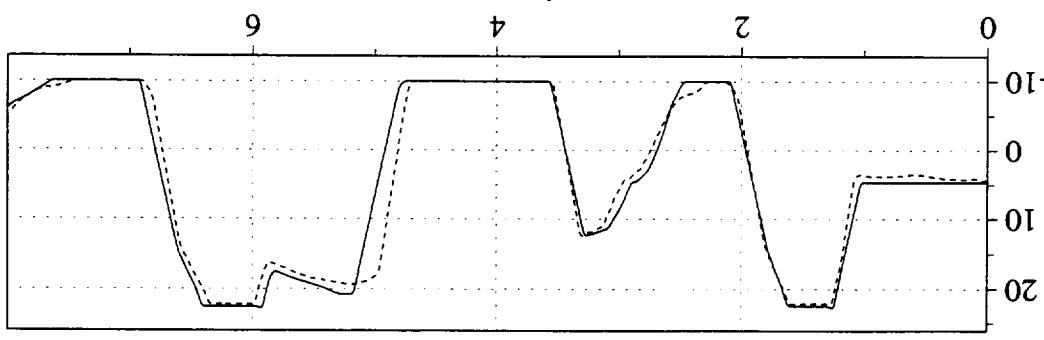
Performance Validation Checkcases

Performance Validation Cases

It outbd vane defl, dg

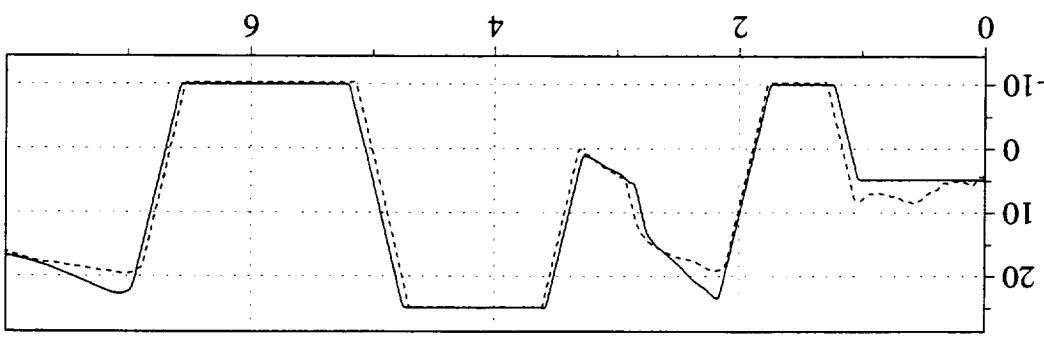
— VANE2,BAT

time, sec



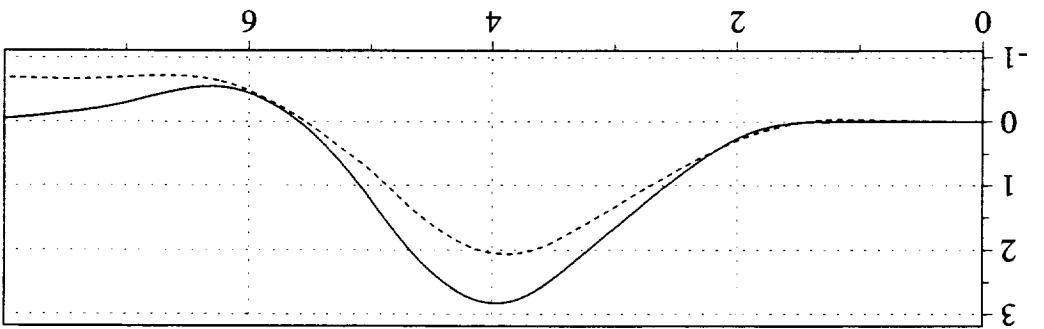
It top vane defl, dg

— VANE1,BAT



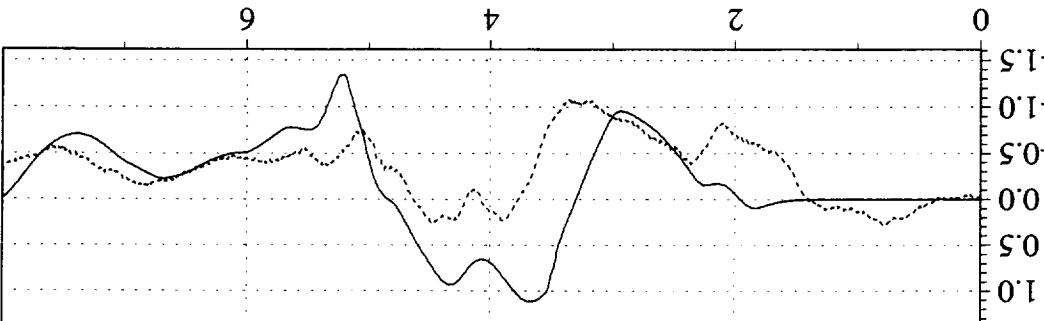
yaw angle, dg

— PS1,BAT



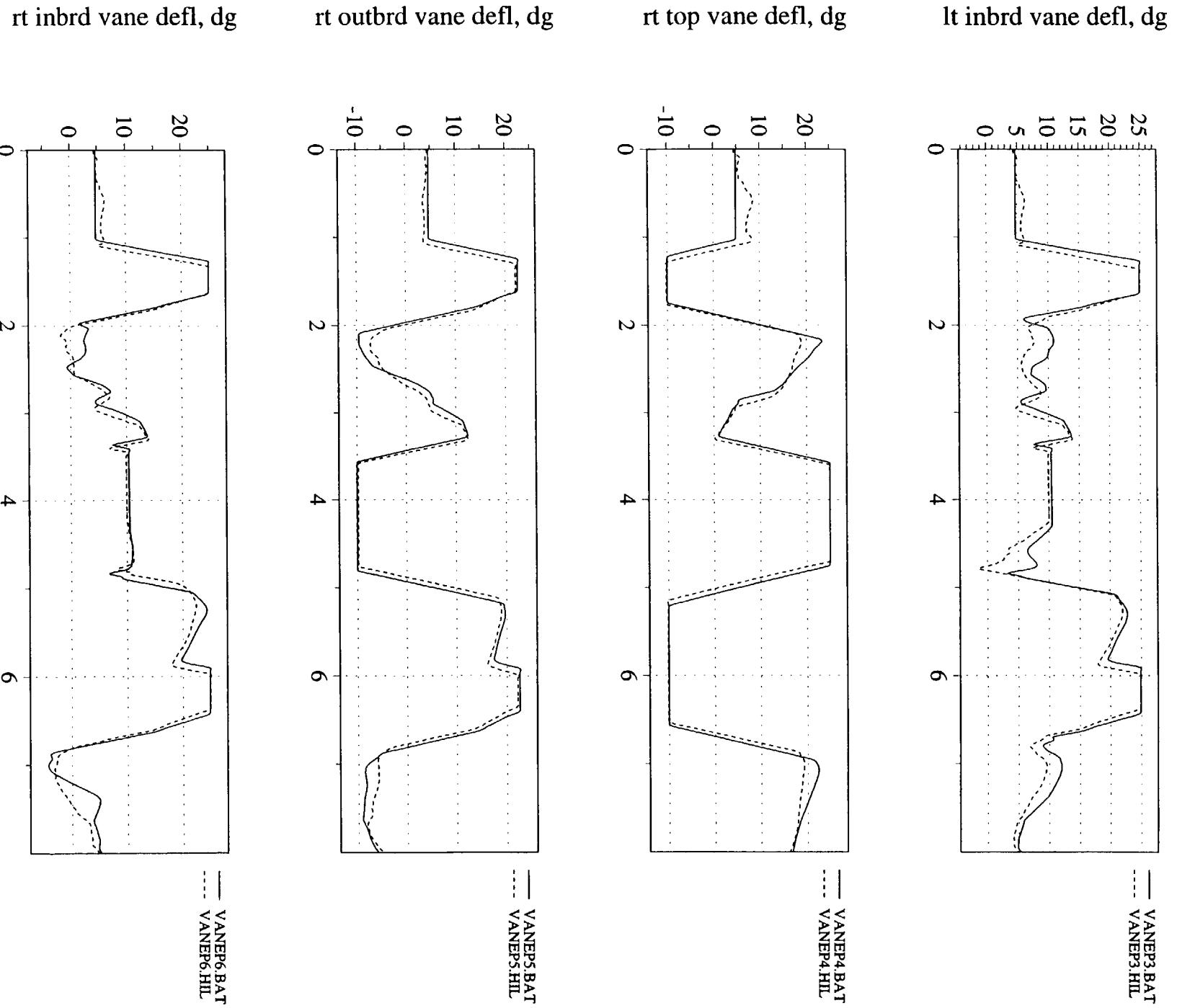
rt aileron defl, dg

— DRAAD,BAT

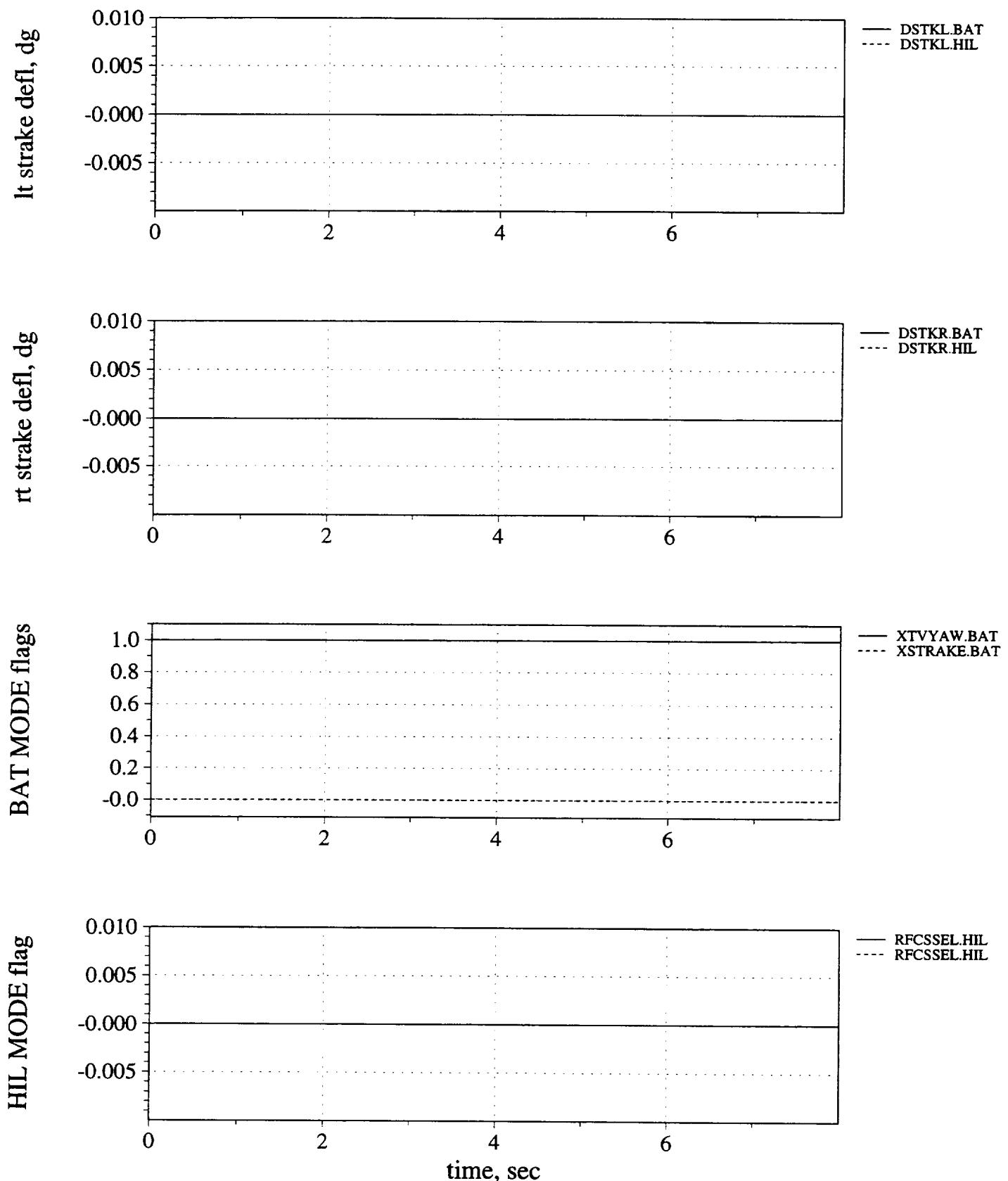


Appendix B - Batch versus HL Version 154.0

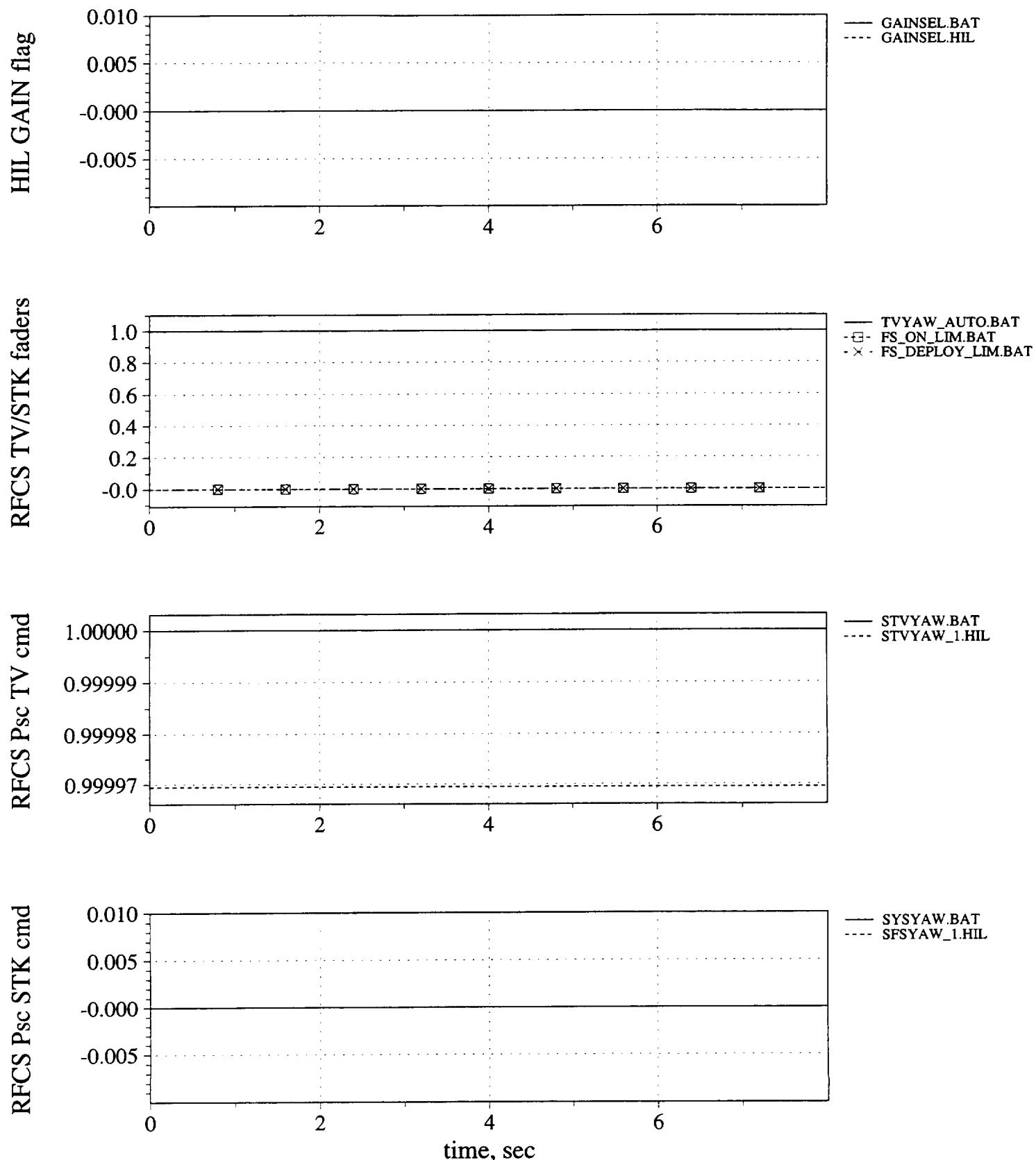
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 10/12



Appendix B - Batch versus HIL Version 154.0
CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 11/12



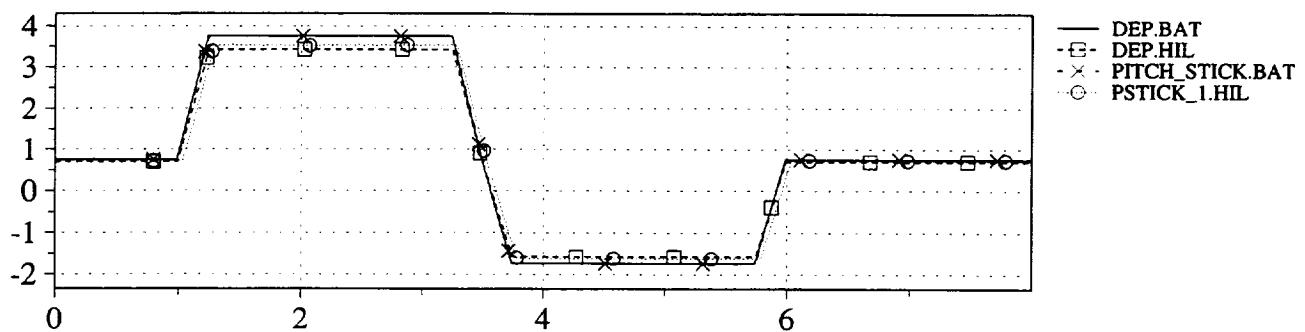
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CASE_1_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep page 12/12



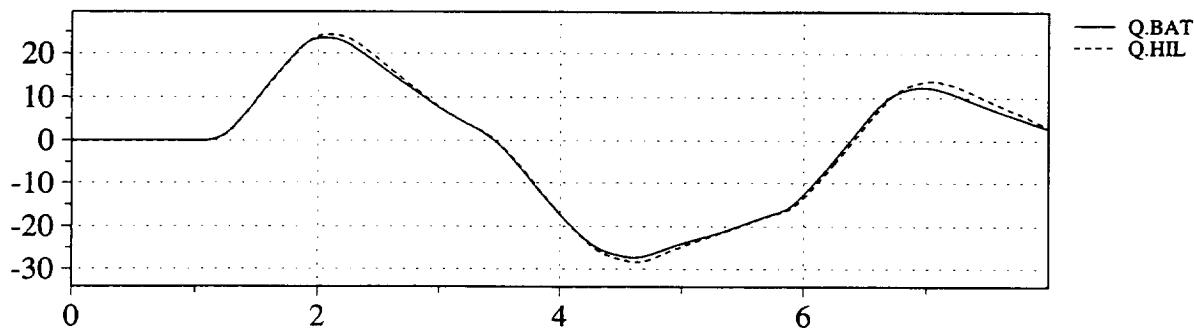
Appendix B - Batch versus HIL Version 154.0

CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep

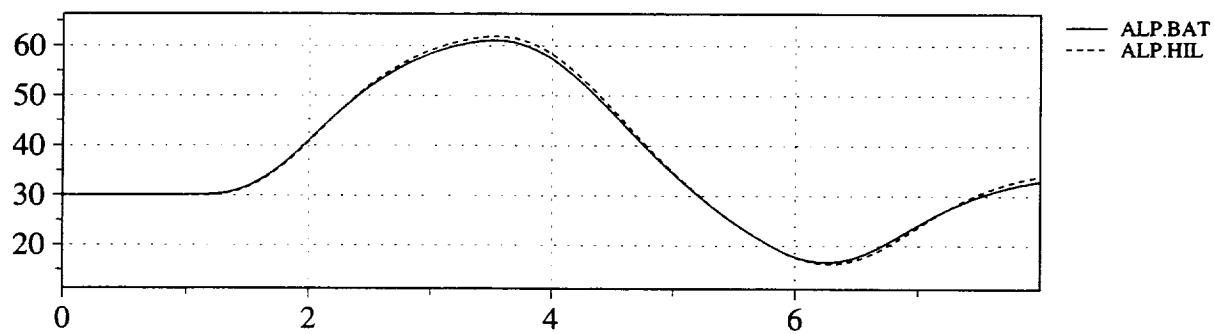
total pit stk cmd, in



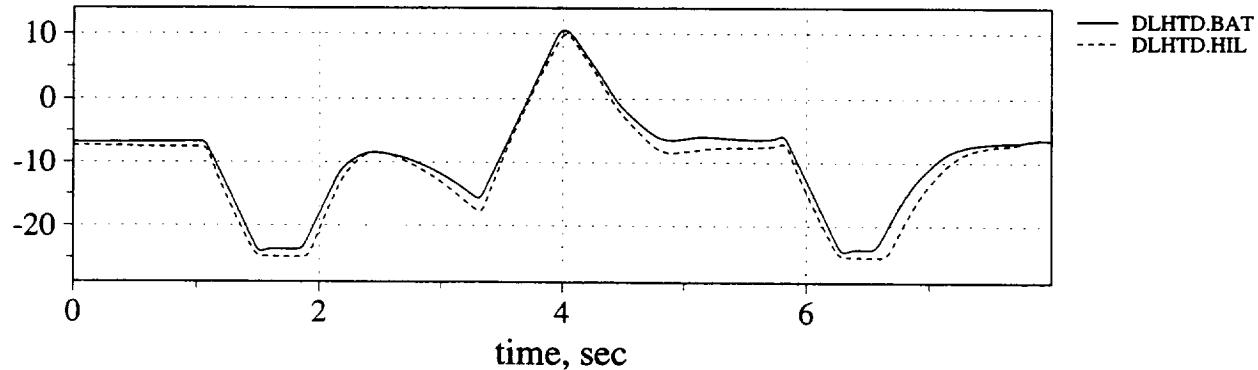
pitch rate, d/s



Angle-of-attack, dg

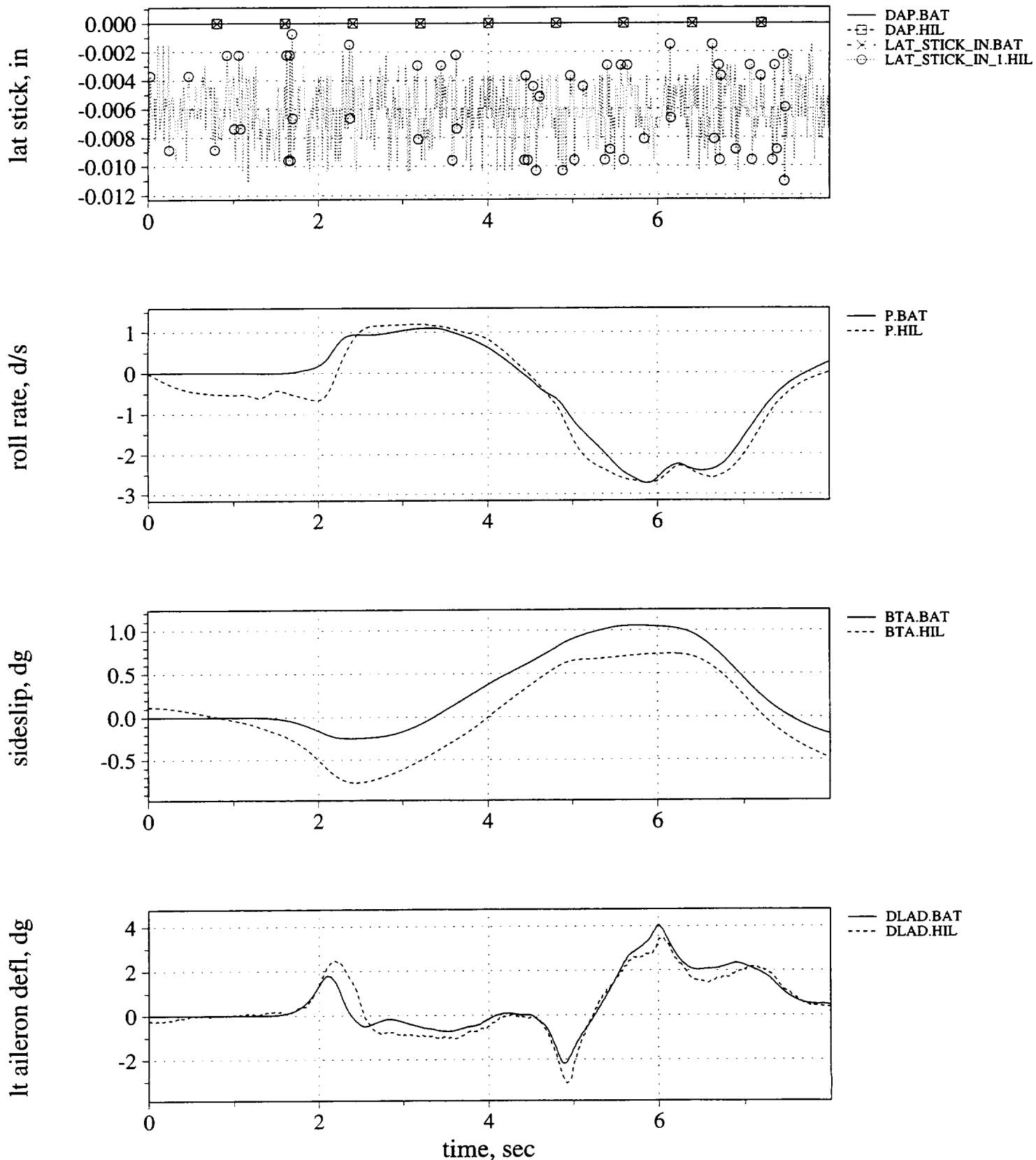


lt stab defl, dg



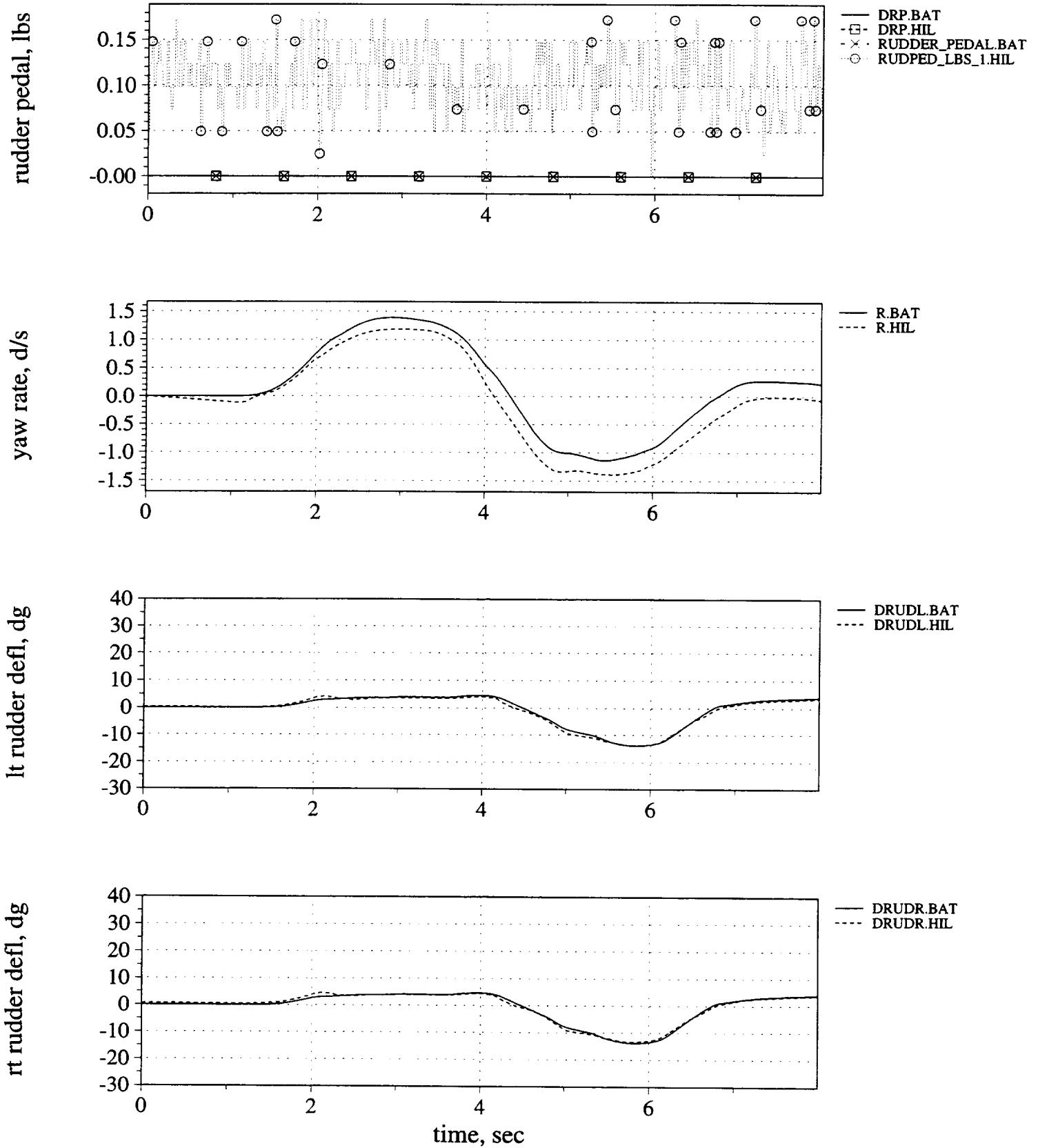
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 2/12



Performance Validation Checkcases

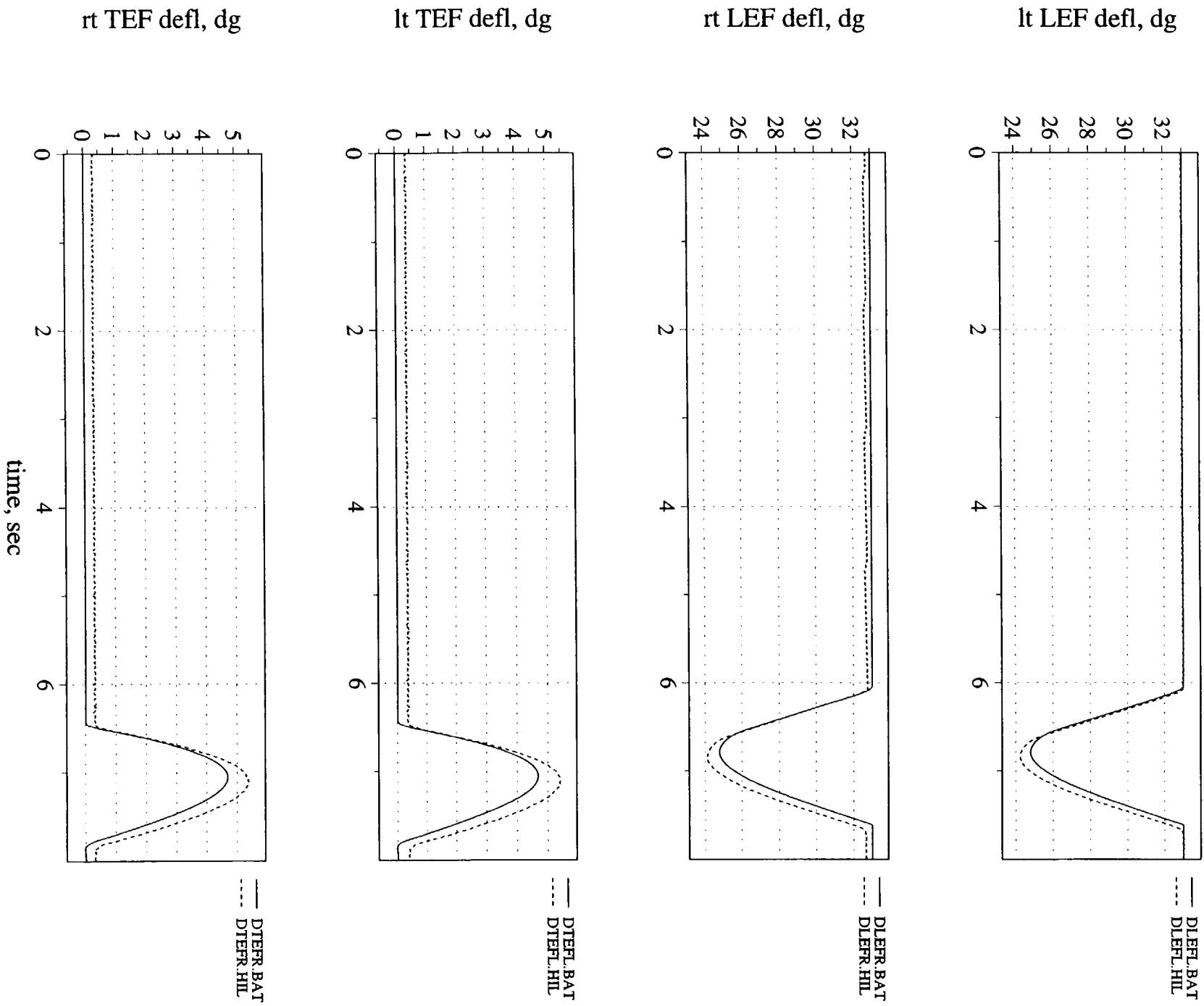
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CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 3/12



Performance Validation Checkcases

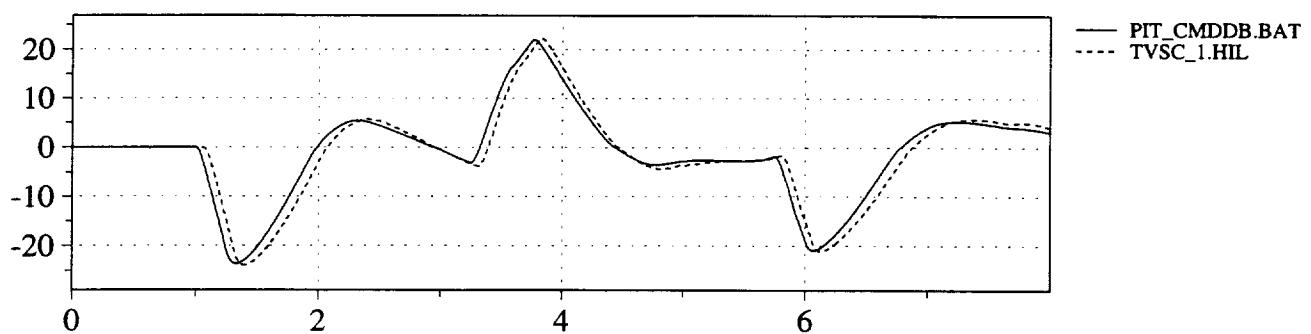
Appendix B - Batch versus HIL Version 154.0

CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 4/12

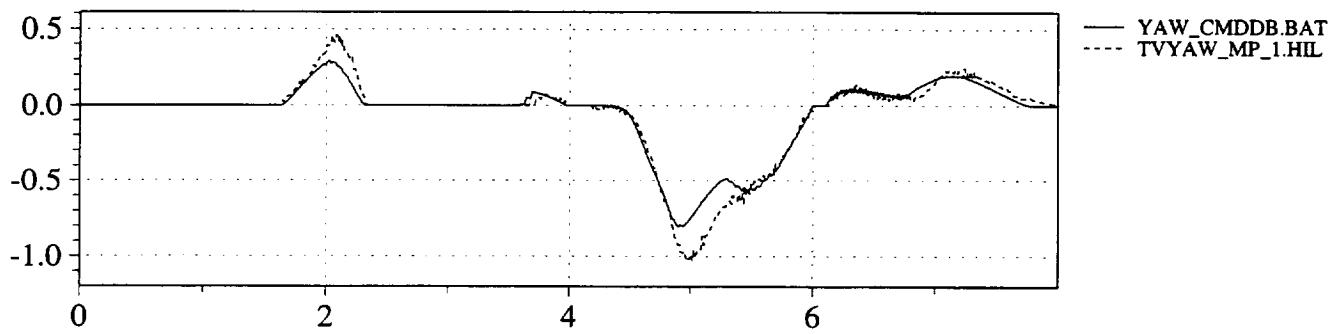


Appendix B - Batch versus HIL Version 154.0
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 5/12

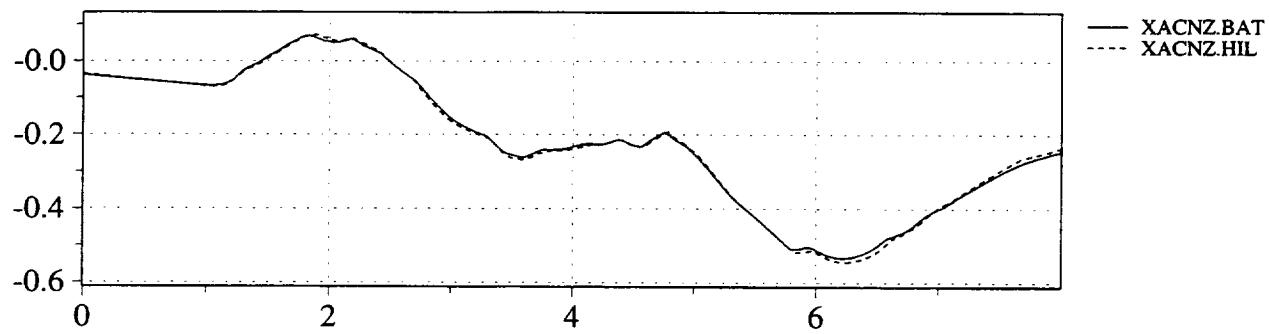
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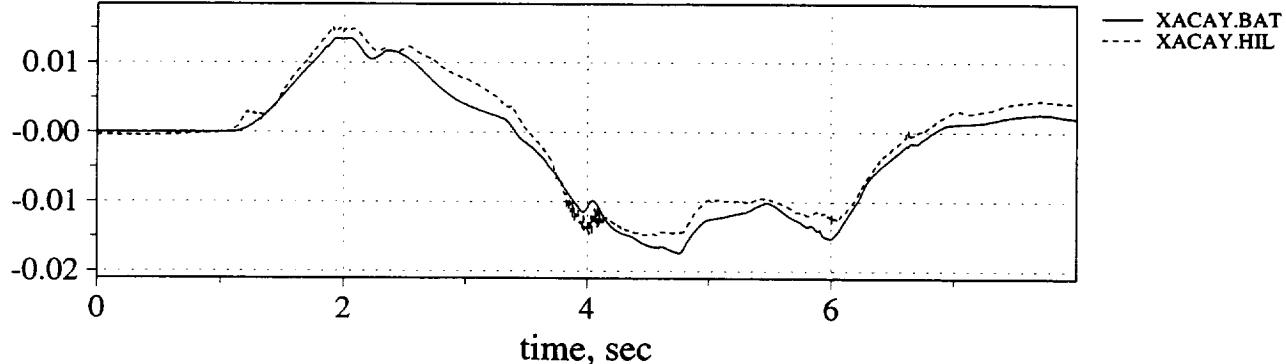
RFCS yaw jet cmd, dg



normal accel, g

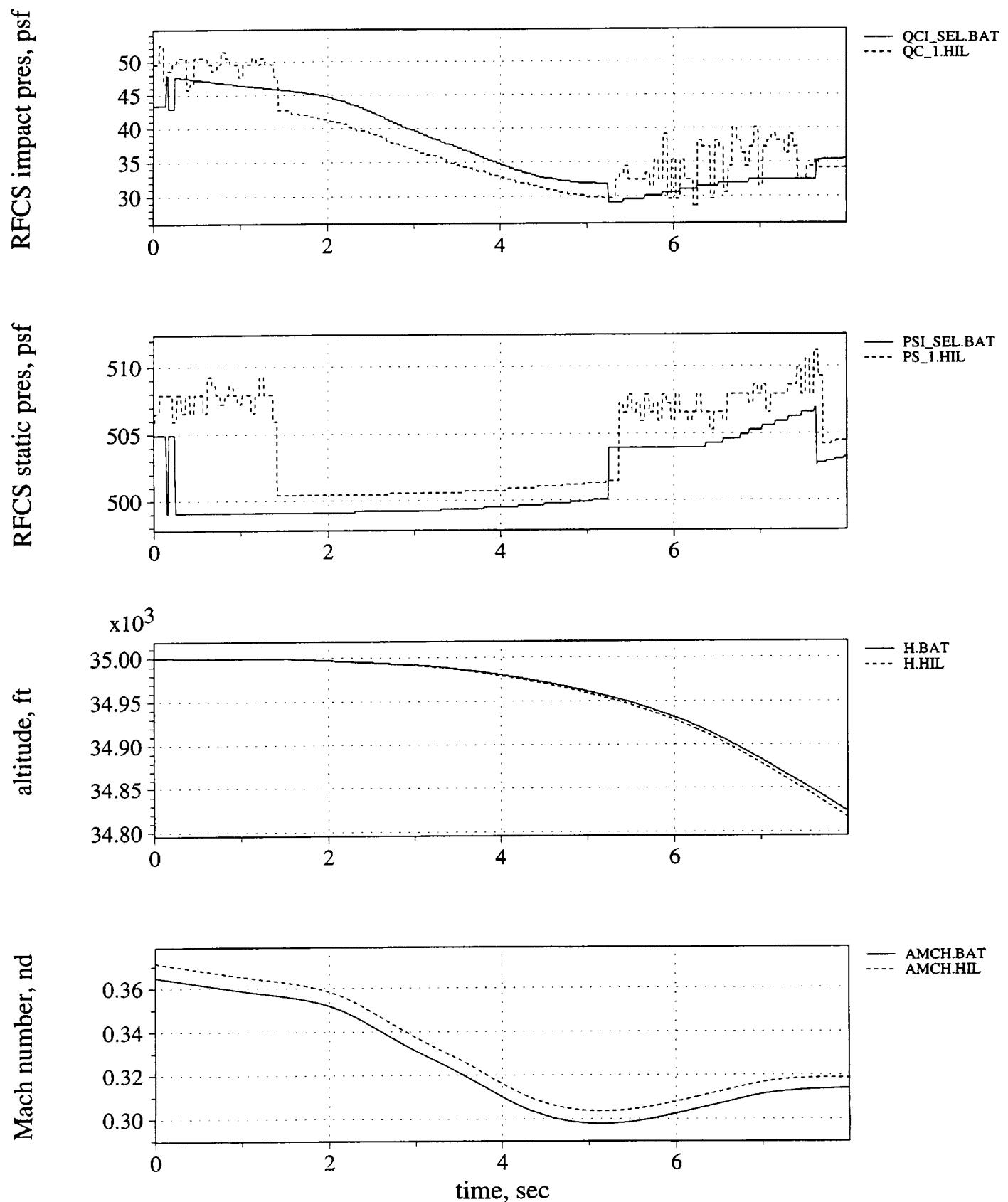


lateral accel, g

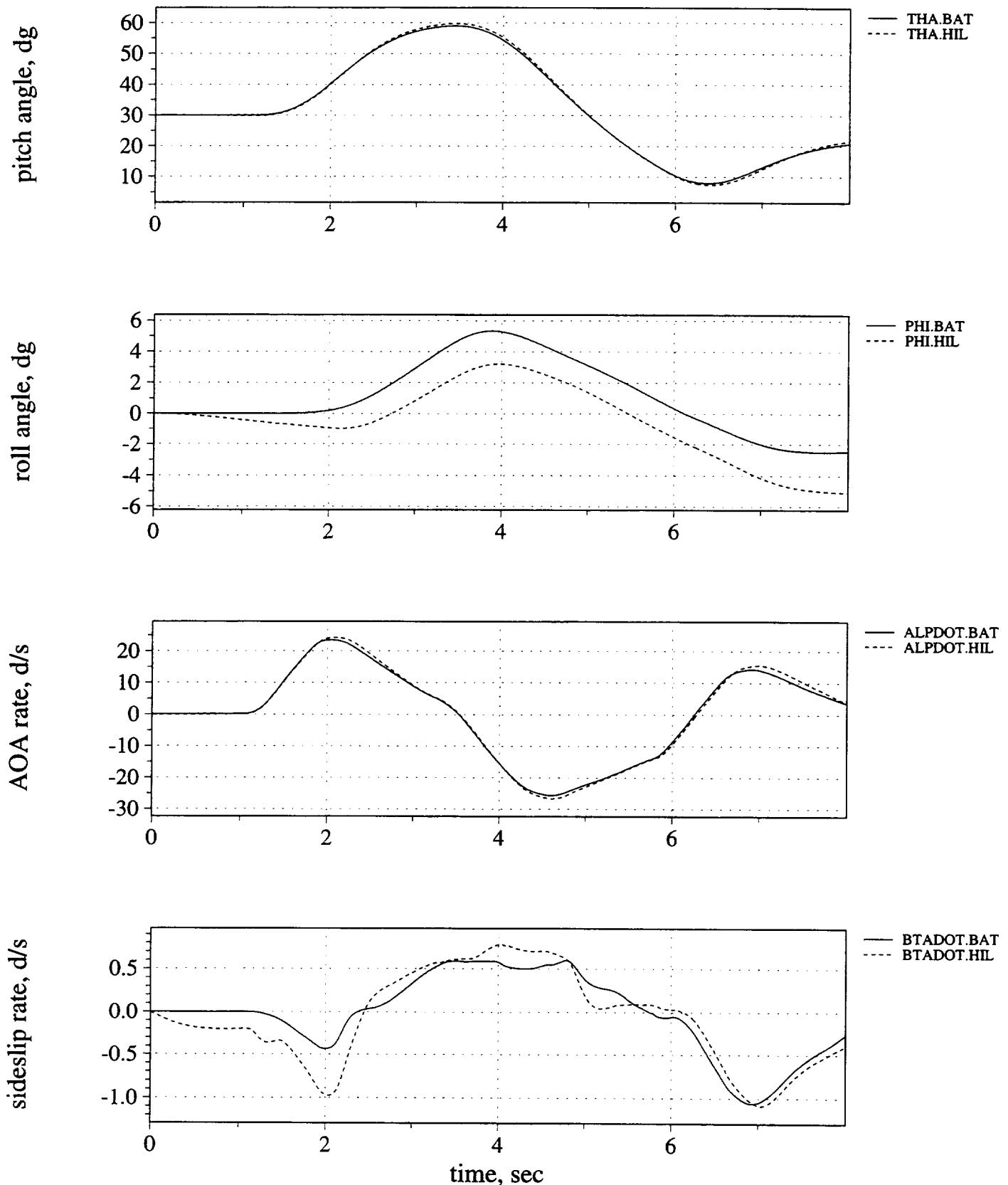


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 6/12

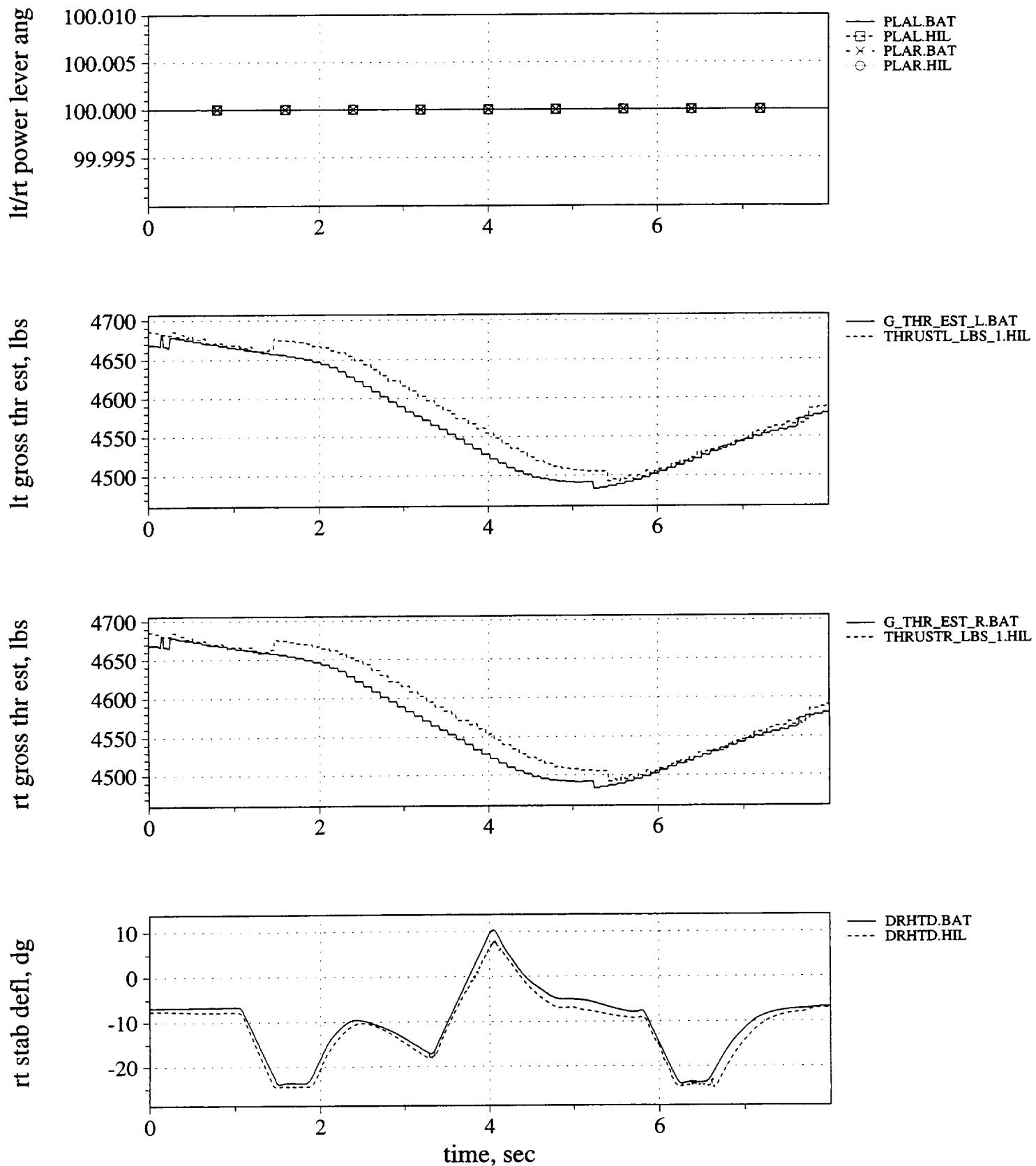


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CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 7/12



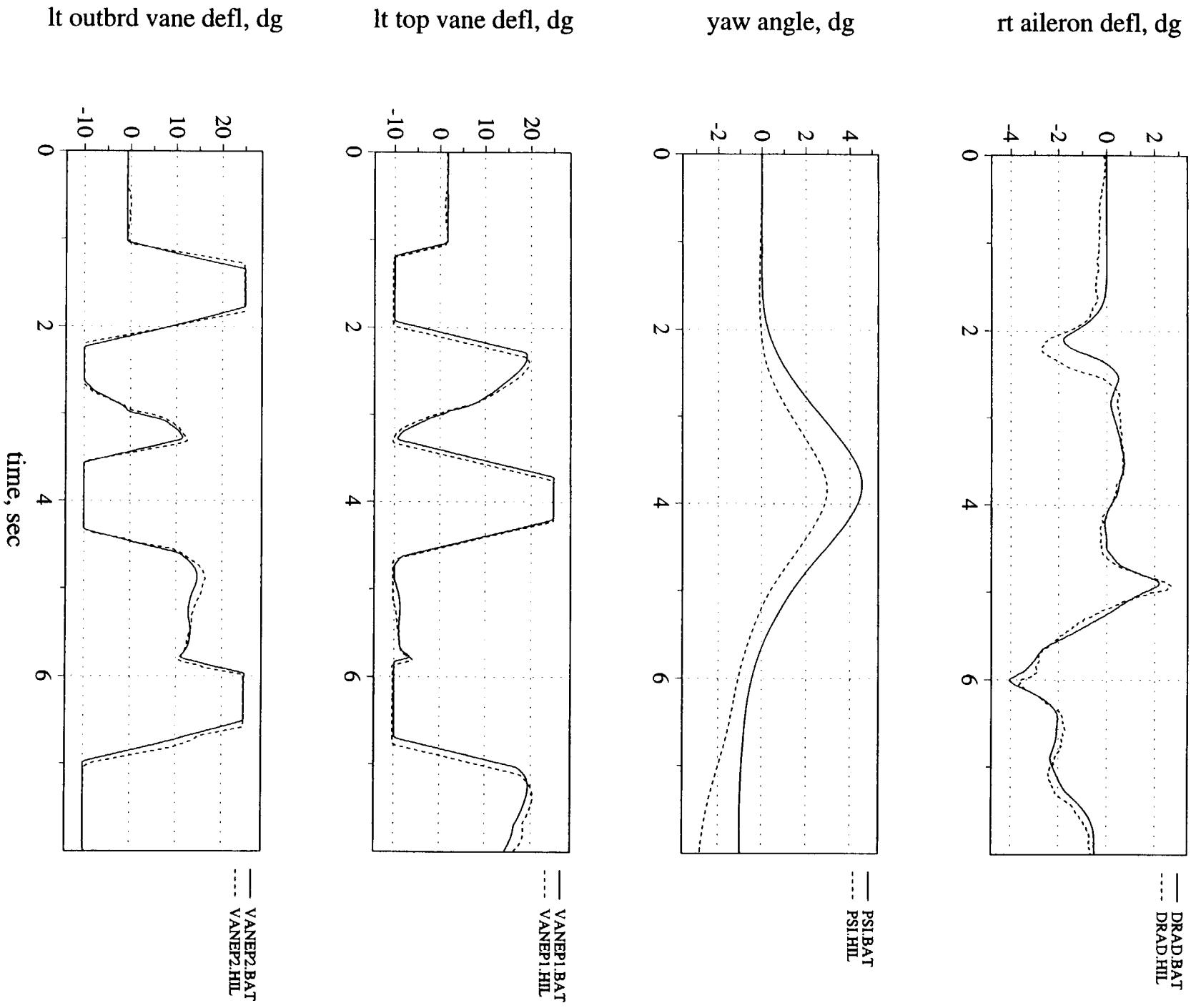
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 8/12



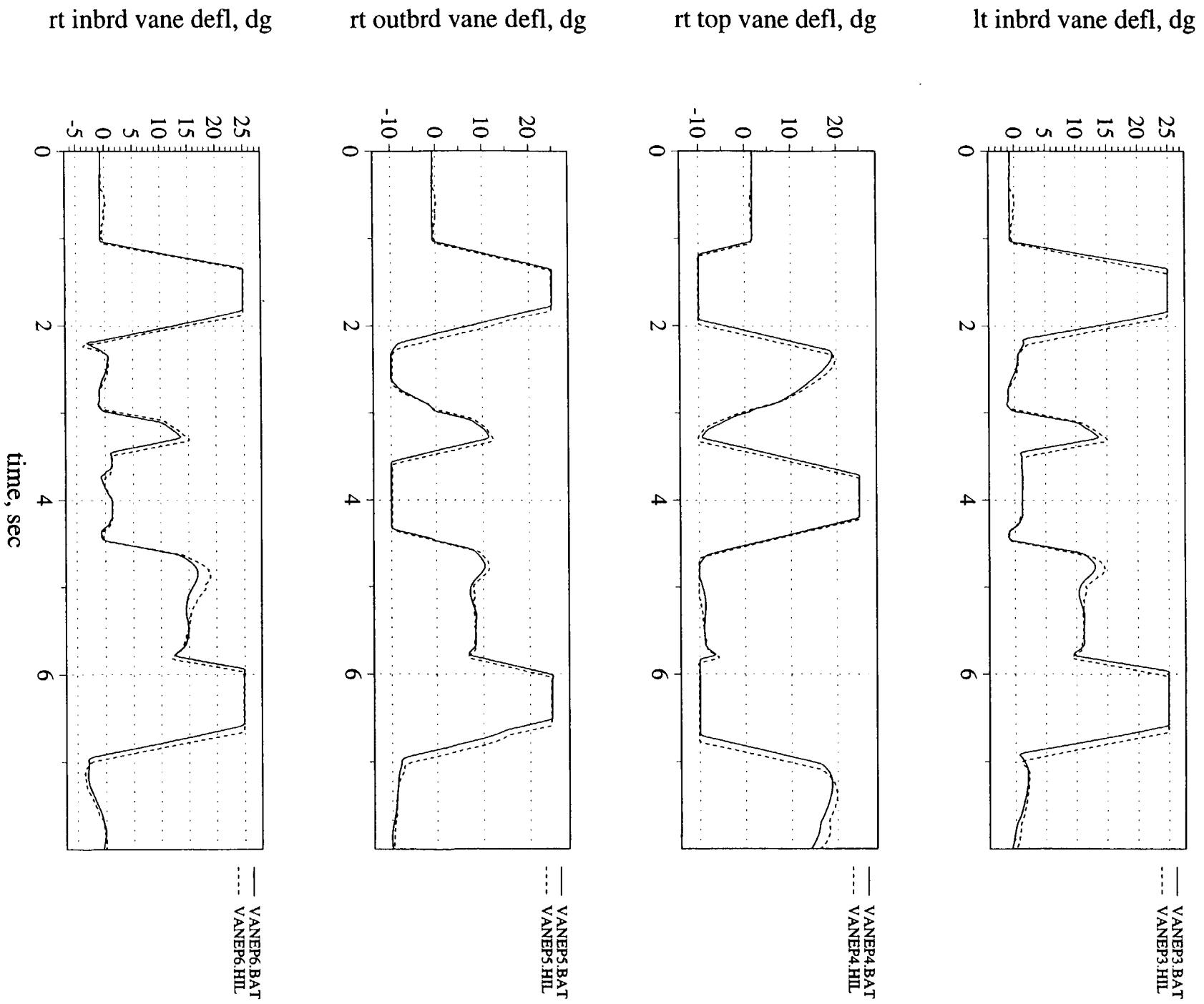
Appendix B - Batch versus HIL Version 154.0

CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 9/12

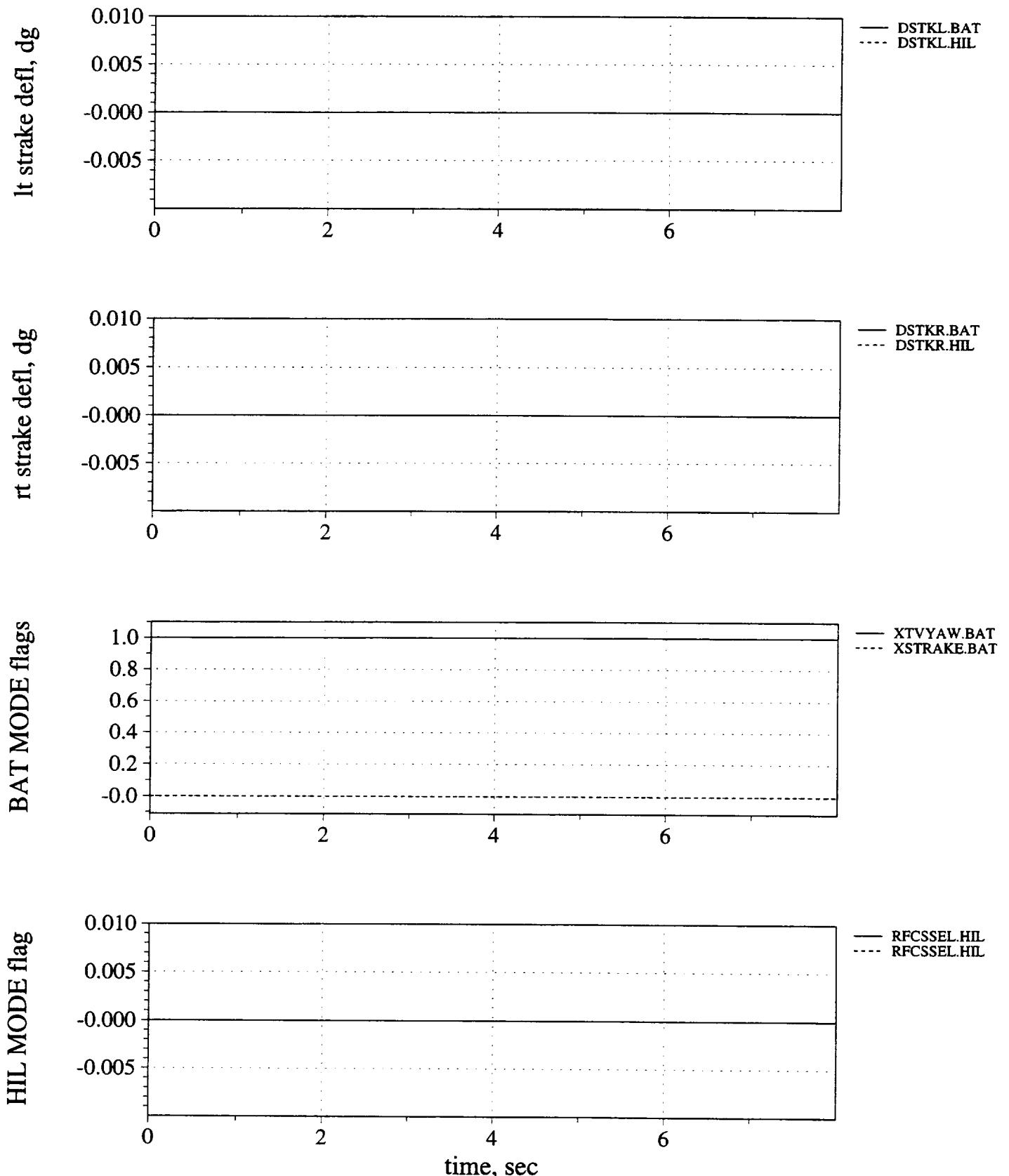


Appendix B - Batch versus HIL Version 154.0

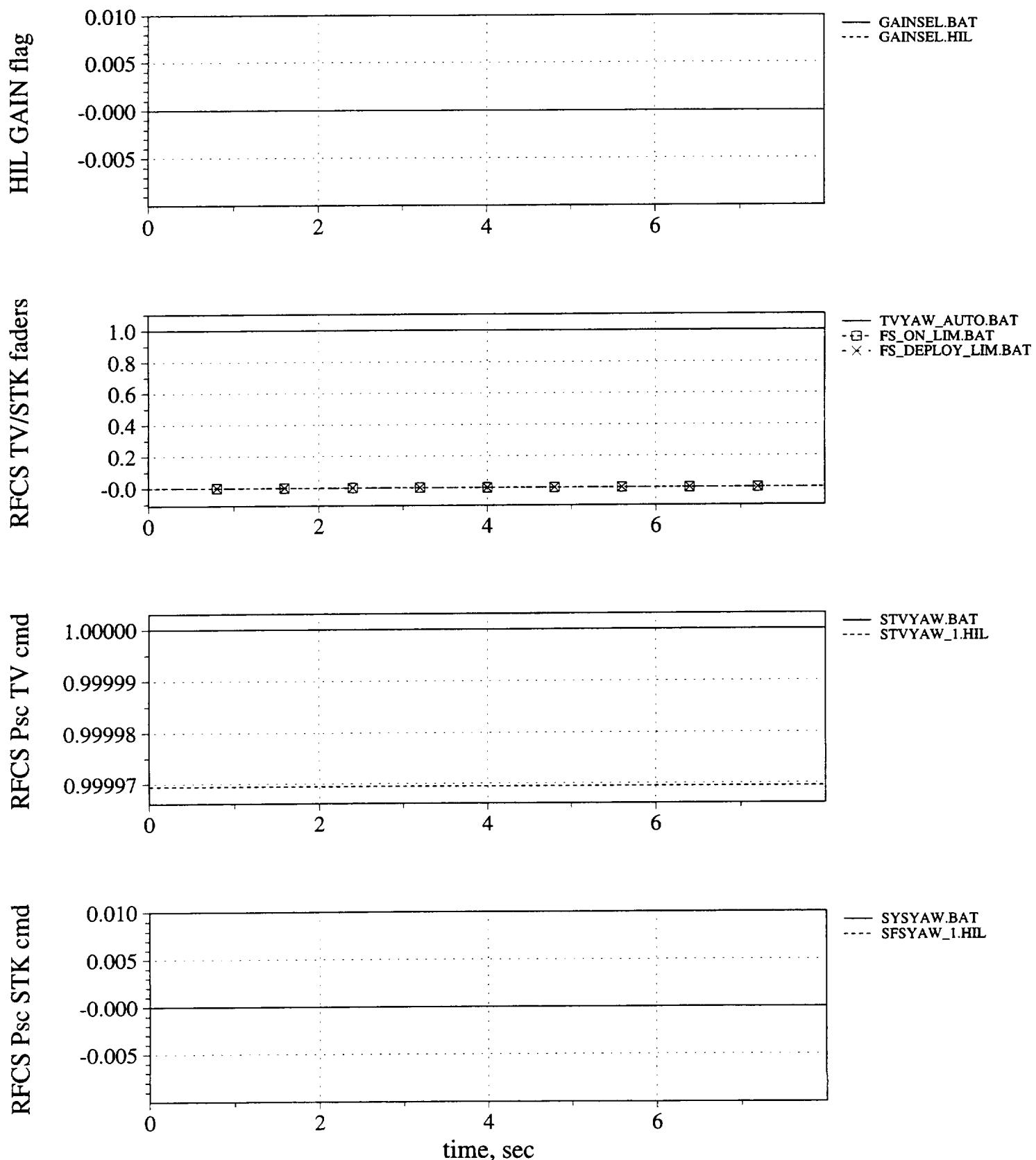
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 10/12



Appendix B - Batch versus HIL Version 154.0
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 11/12

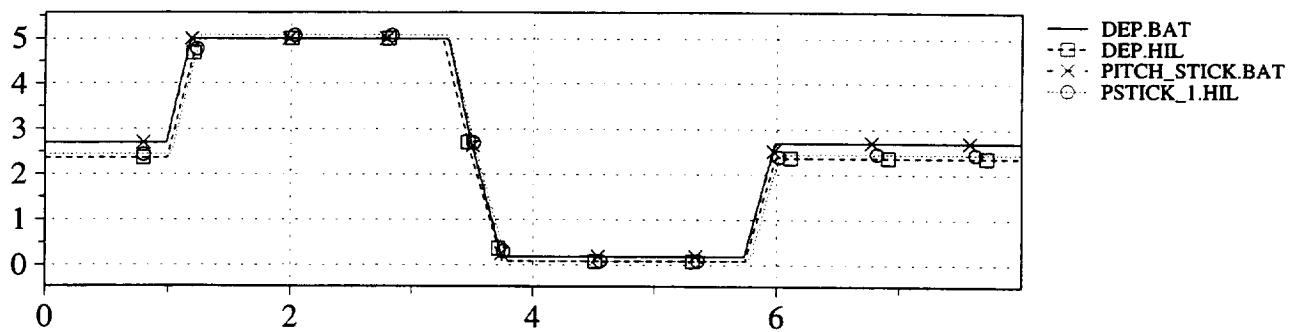


Appendix B - Batch versus HIL Version 154.0
CASE_4_TV_Mode_30alp-35k_Max_AB_3_-2.5_in_ramp_dep page 12/12

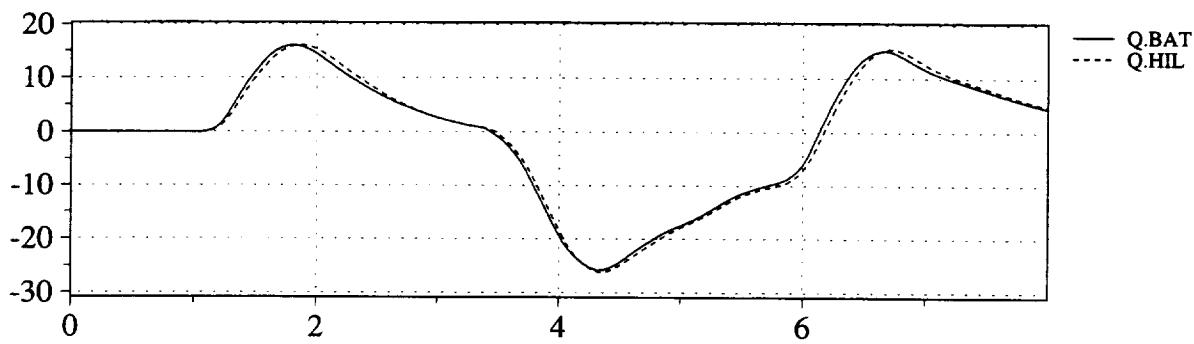


Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep

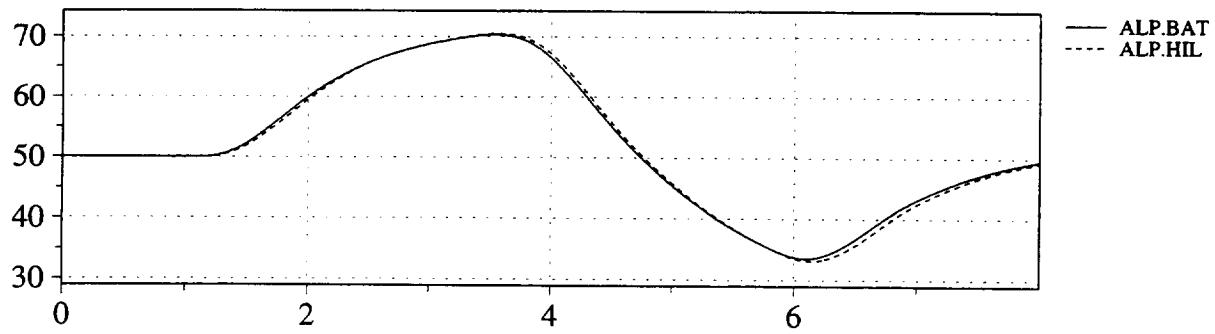
total pit stk cmd, in



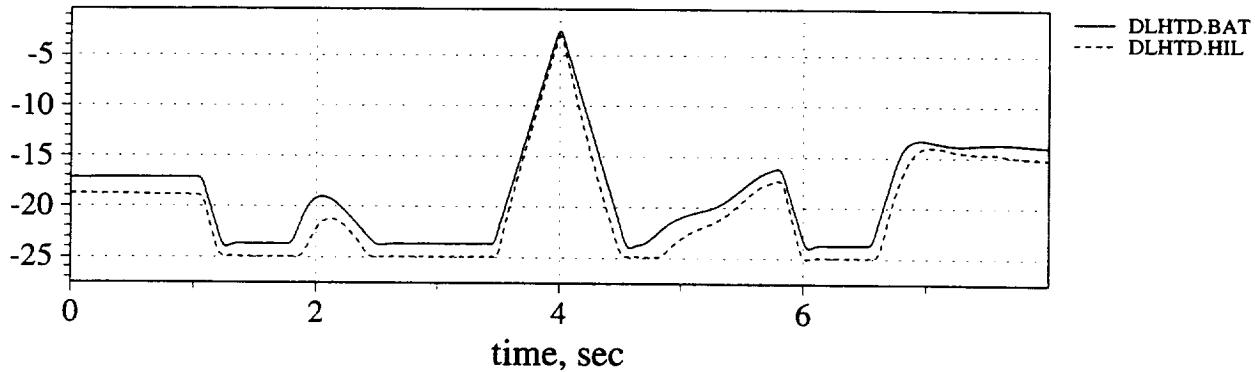
pitch rate, d/s



Angle-of-attack, dg

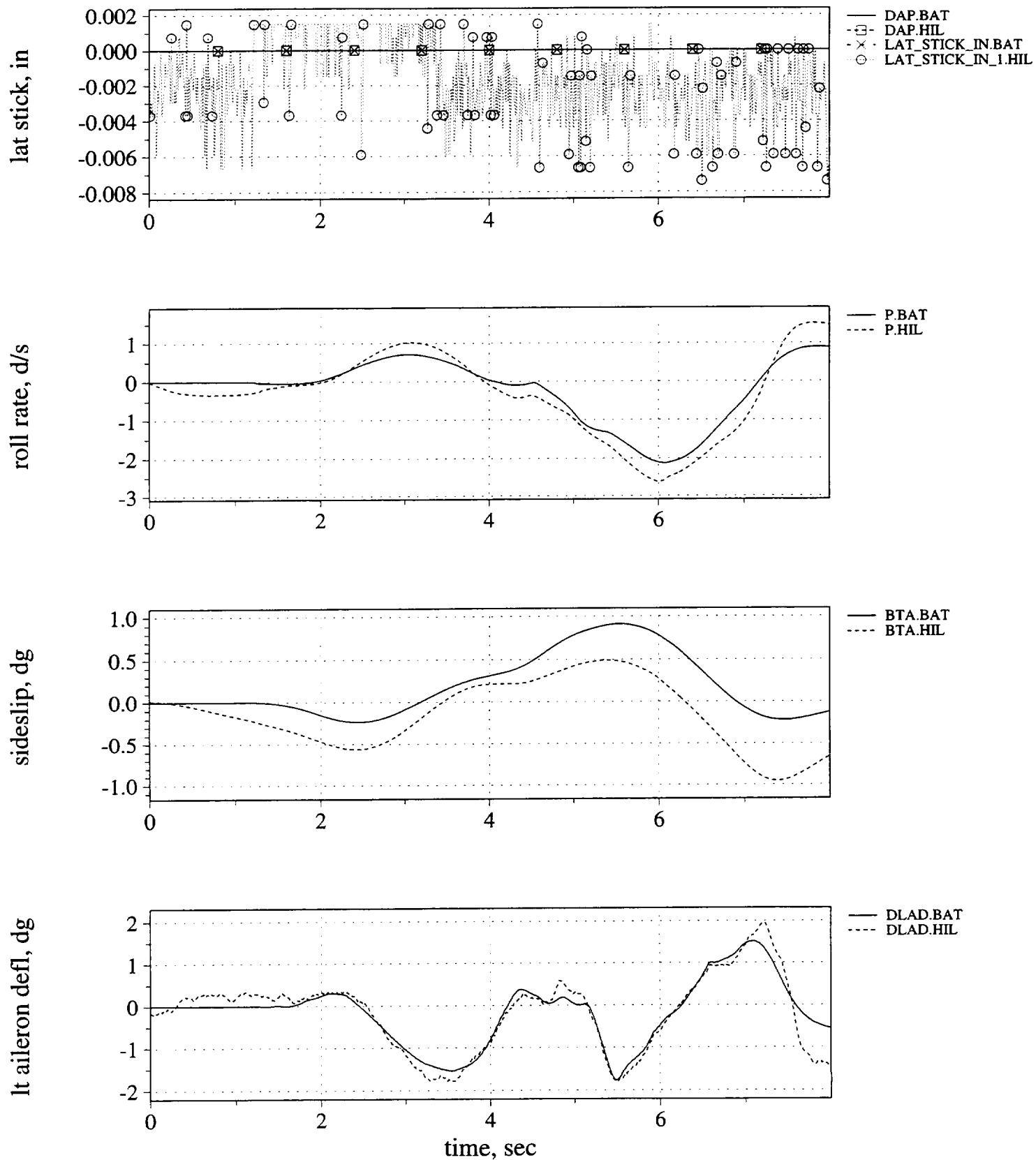


lt stab defl, dg

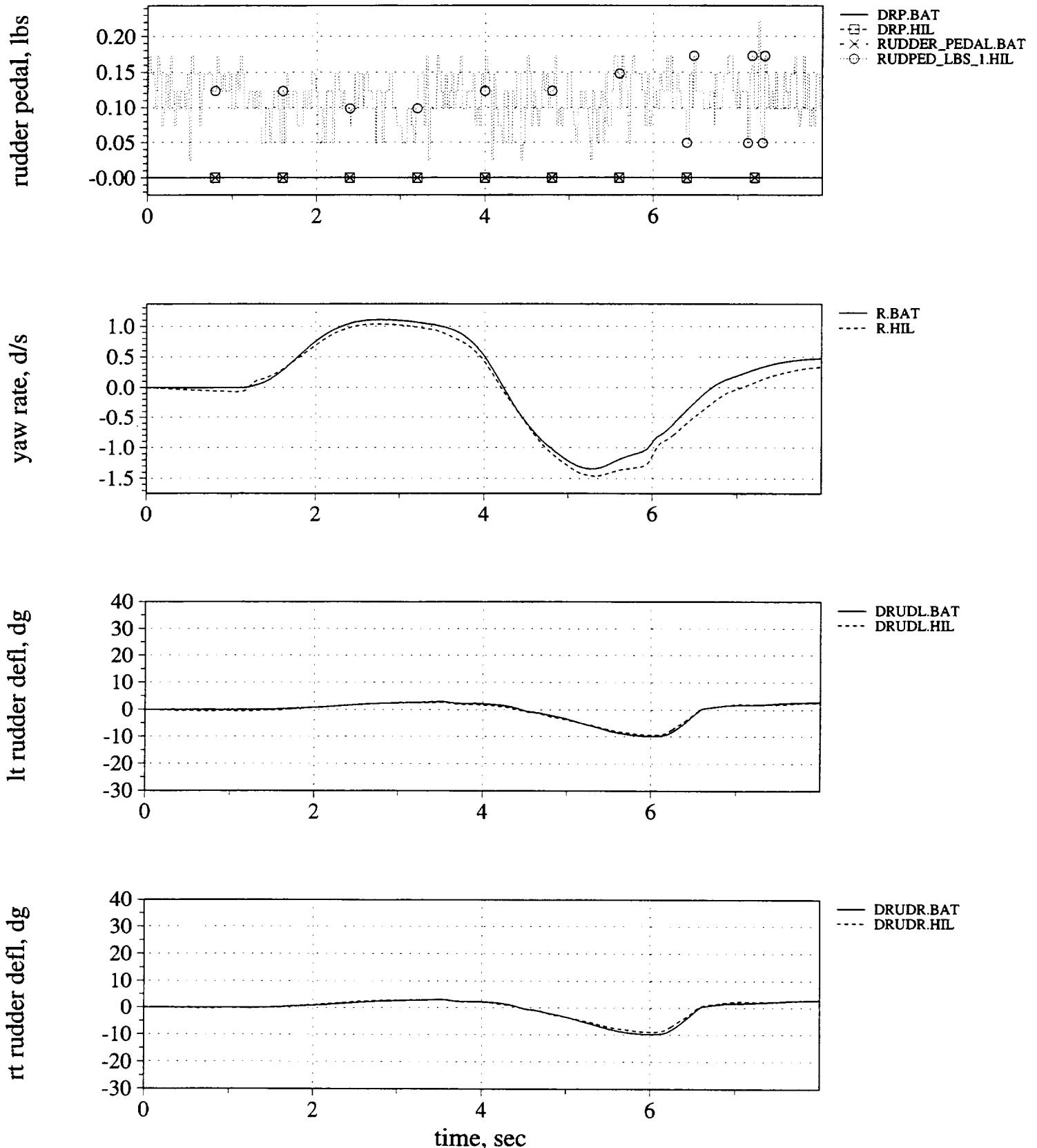


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 2/12



Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 3/12

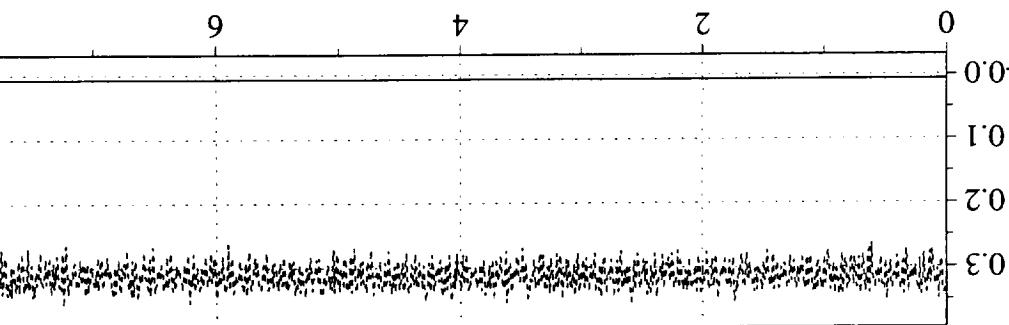


Performance Validation Checks

rt TEF defl, dg

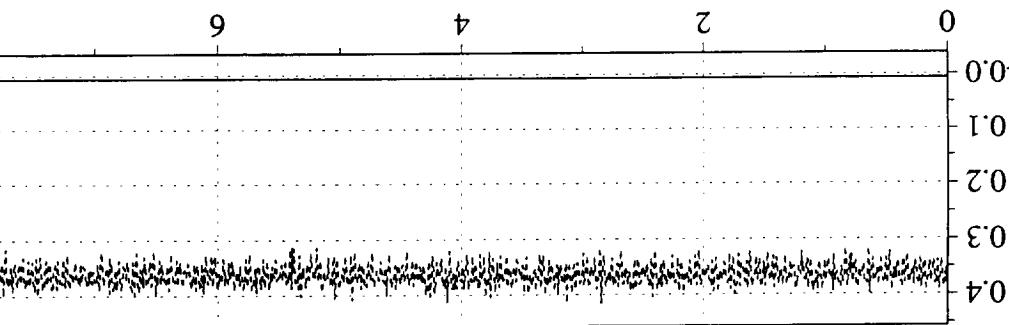
DTEFR,BAT

time, sec



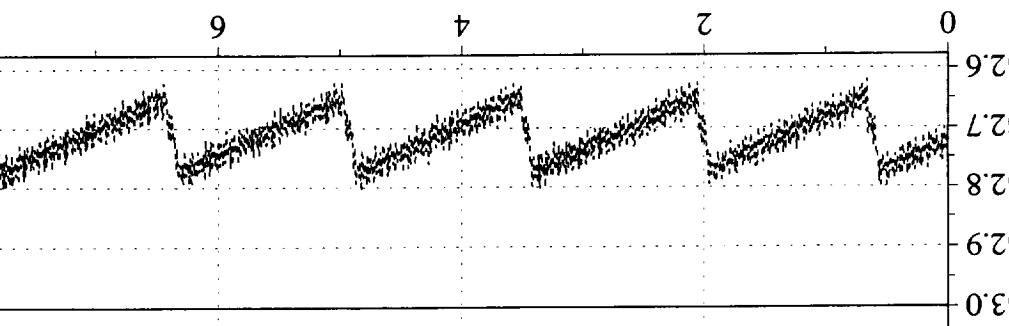
lt TEF defl, dg

DTEFR,BAT



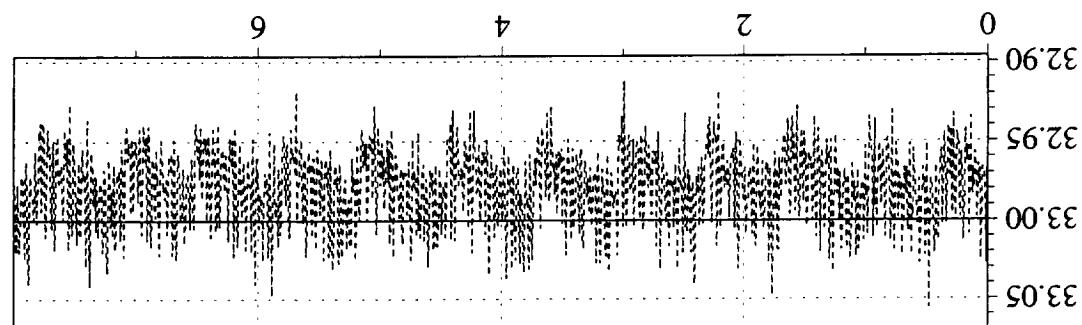
rt LEF defl, dg

DLTEFR,HIL



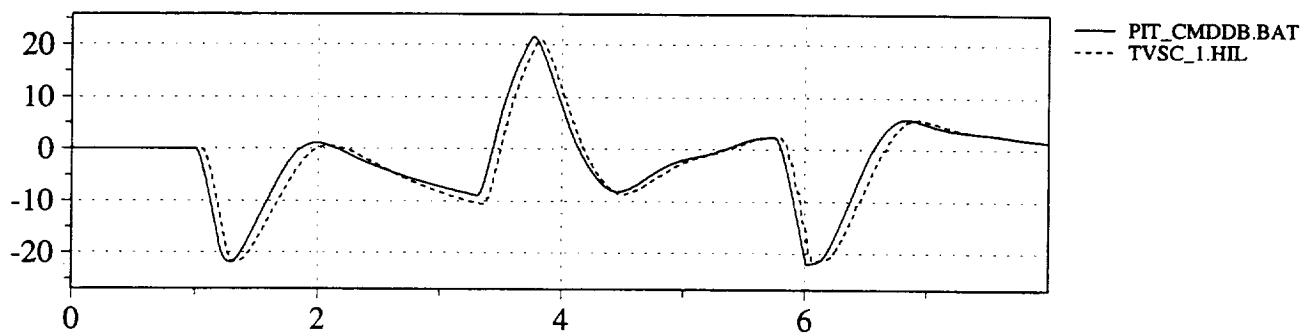
lt LEF defl, dg

DLTEFR,HIL

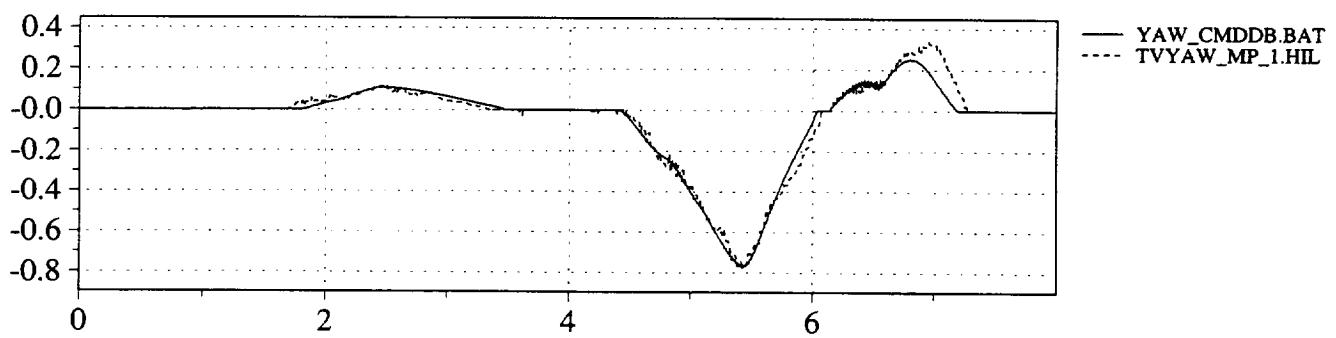


Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 5/12

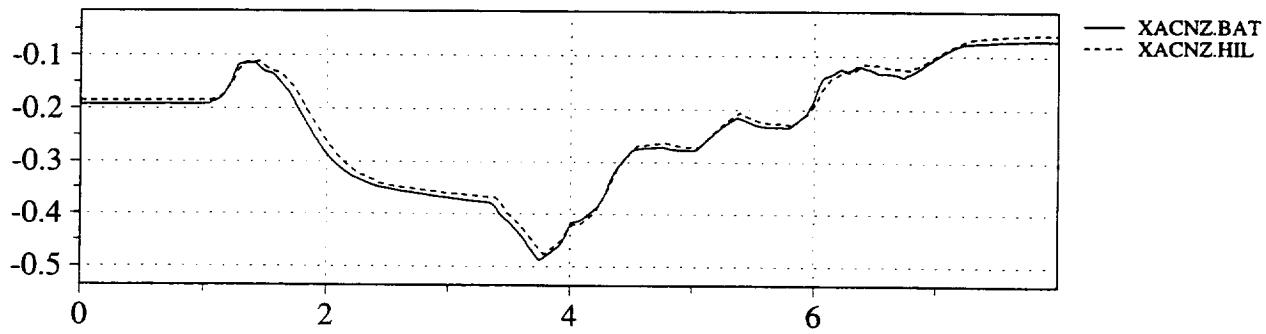
RFCS pit jet cmd, dg



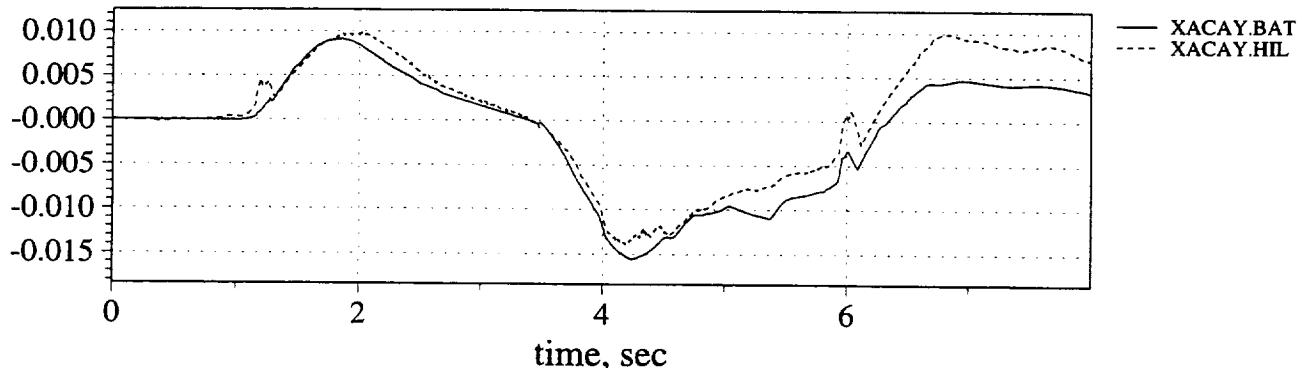
RFCS yaw jet cmd, dg



normal accel, g



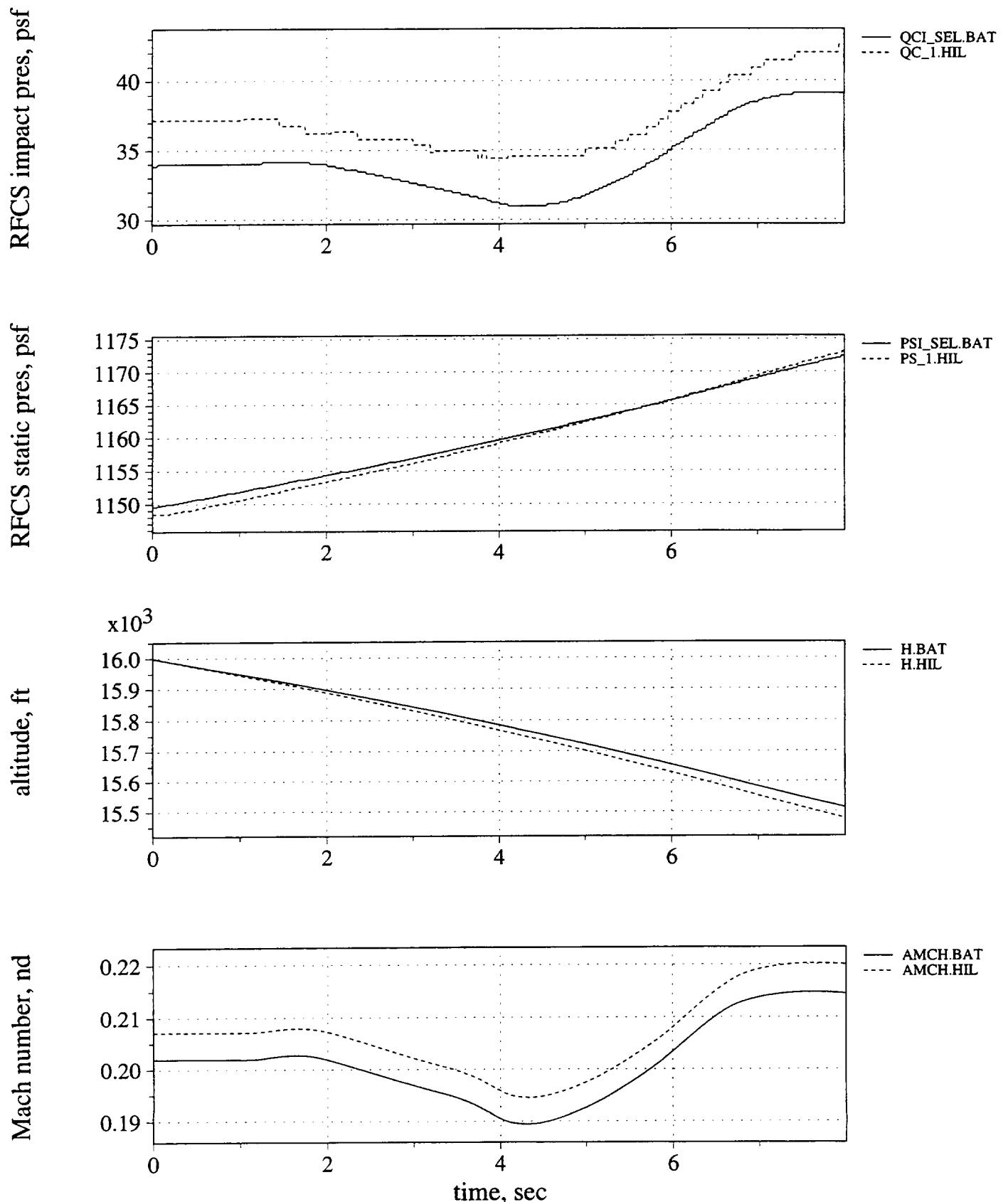
lateral accel, g



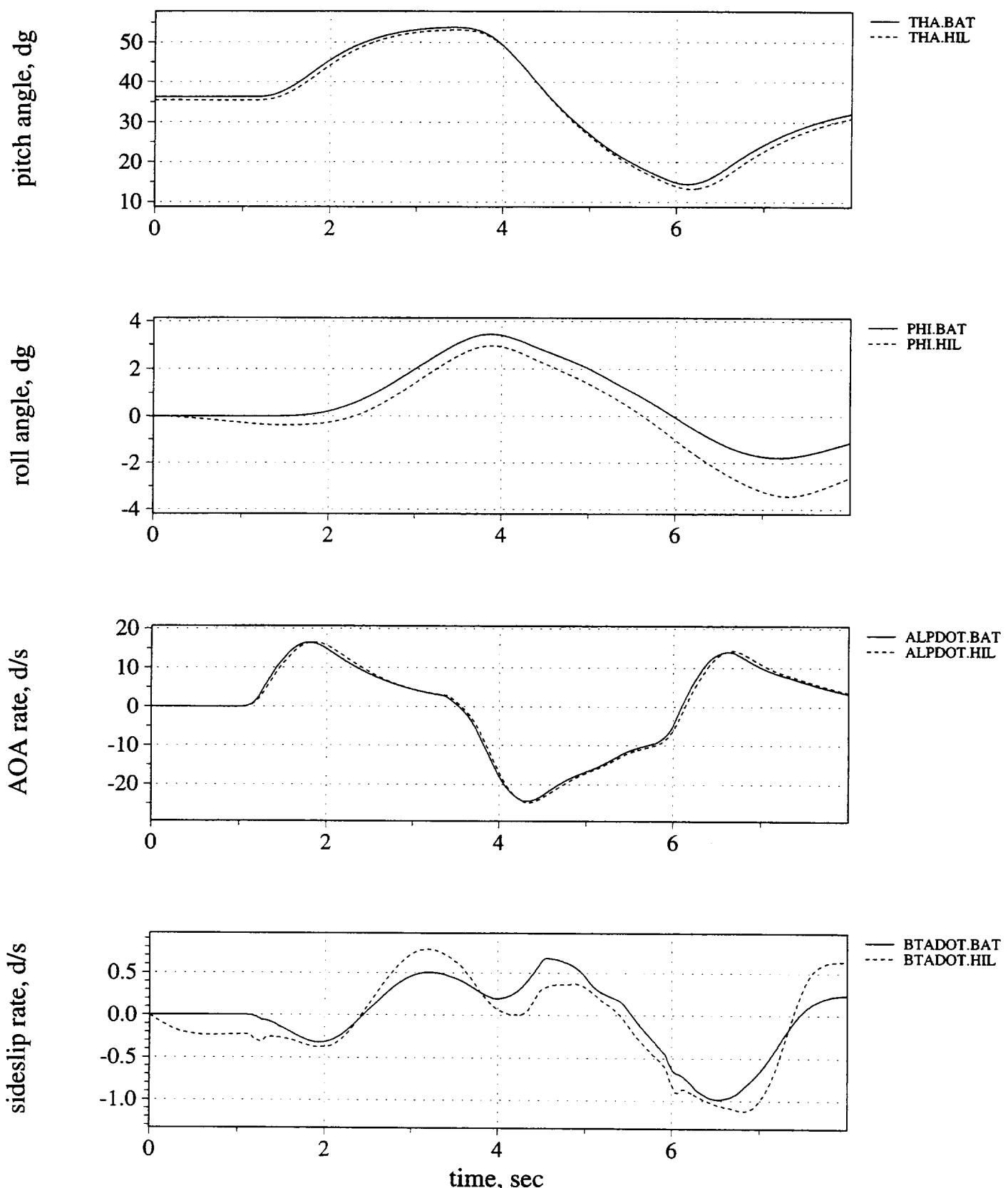
Performance Validation Checkcases

page 36

Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 6/12

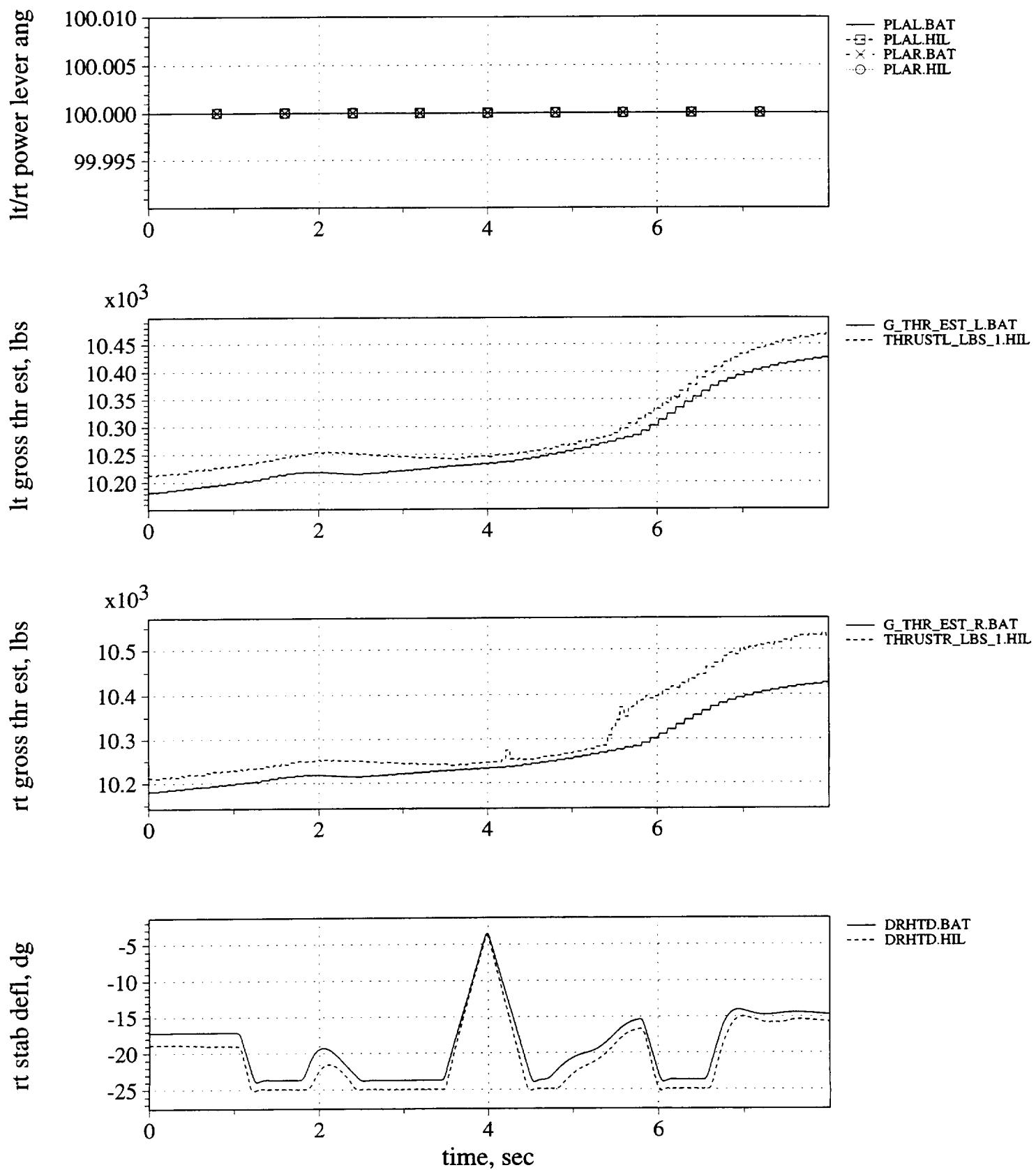


Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 7/12

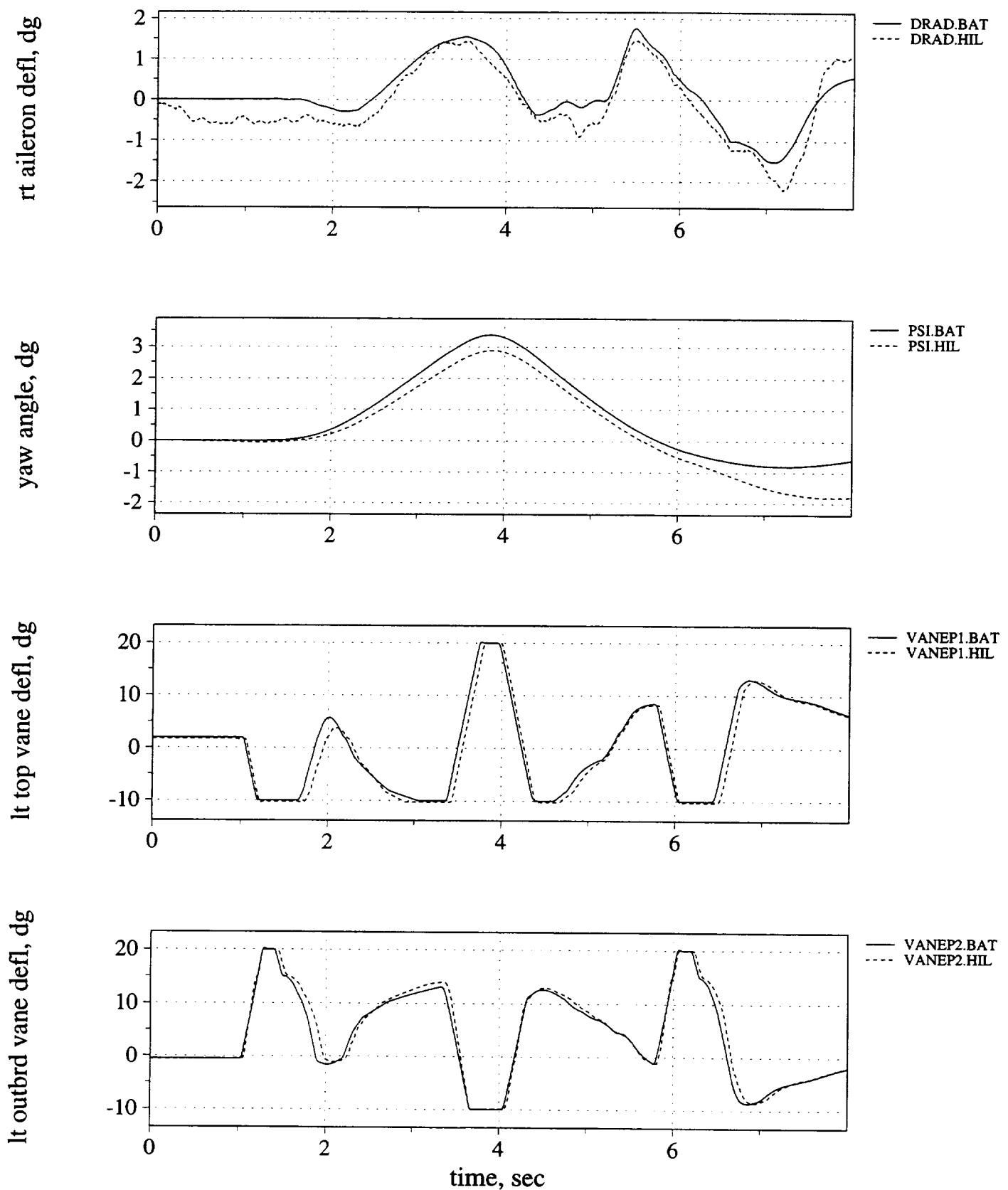


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 8/12



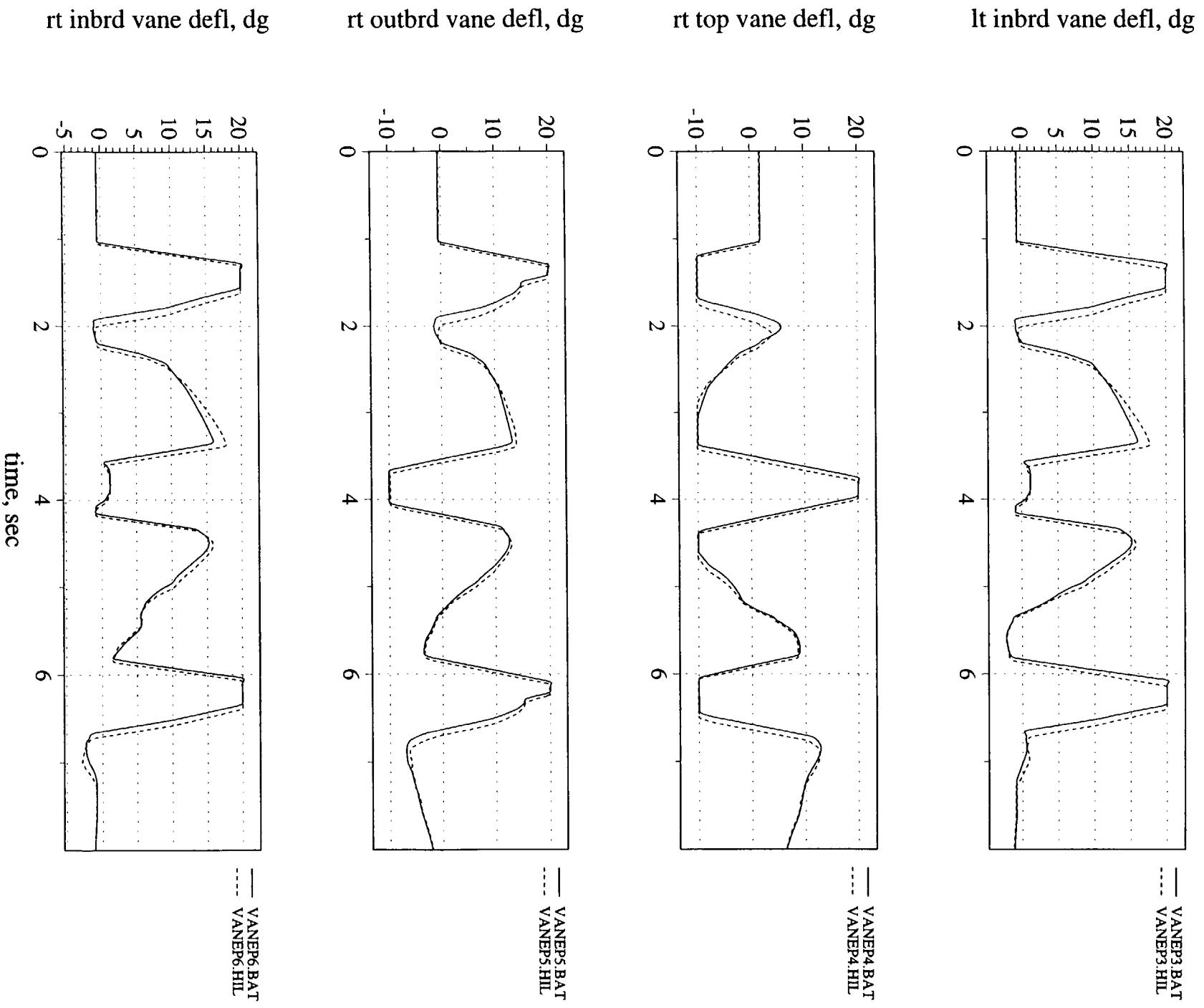
Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 9/12



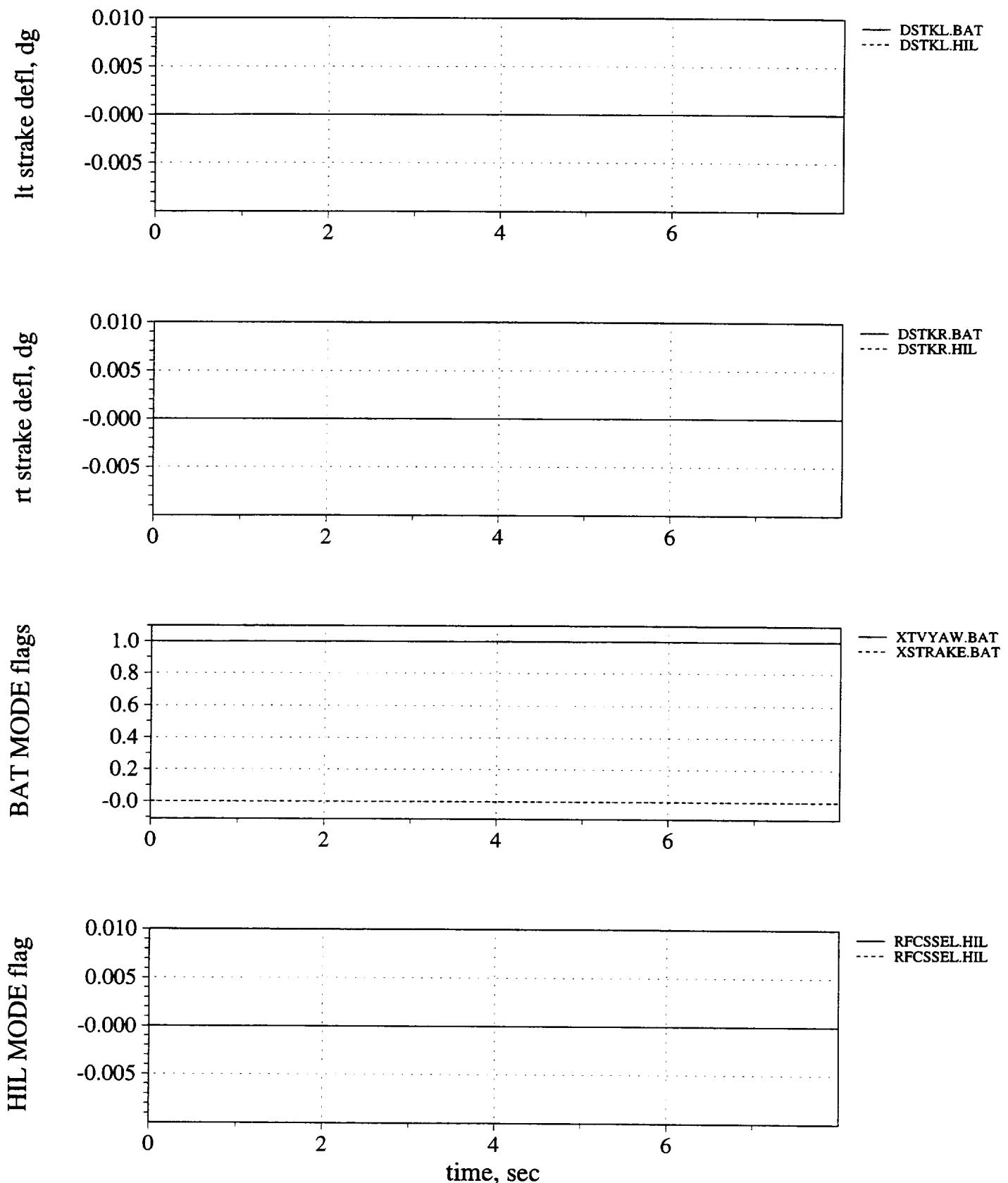
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

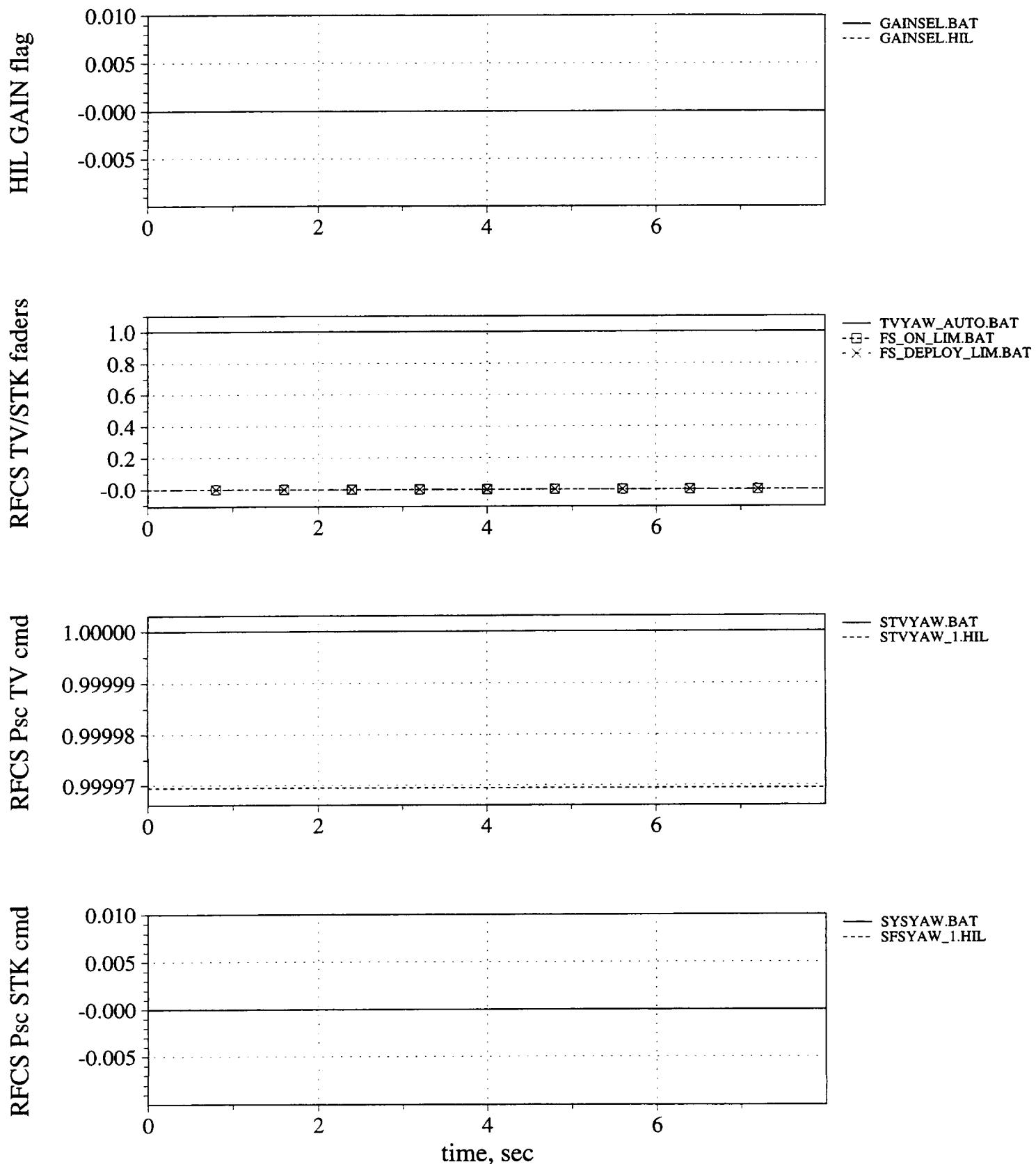
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 10/12



Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 11/12

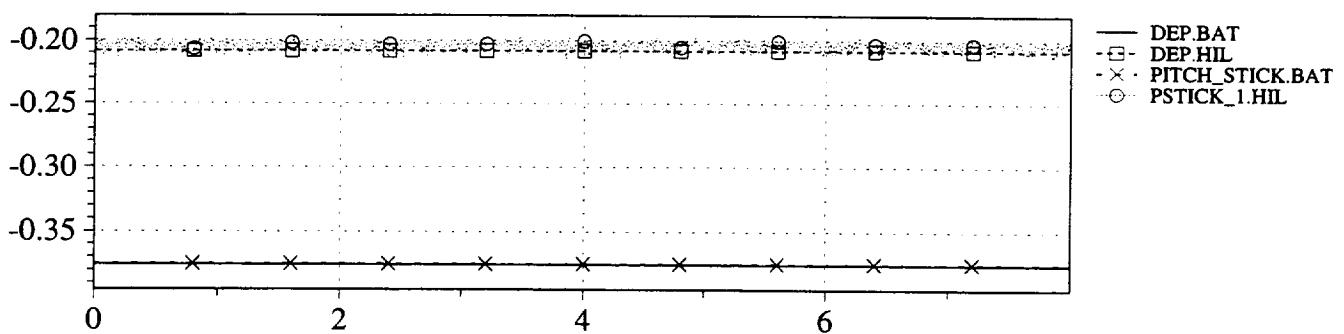


Appendix B - Batch versus HIL Version 154.0
CASE_7_TV_Mode_50alp-16k_Max_AB_3_-2.5_in_ramp_dep page 12/12

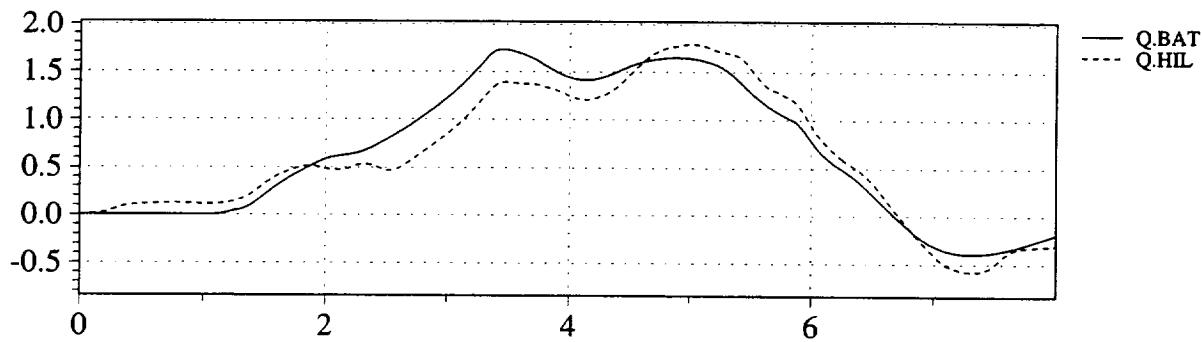


Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 1/12

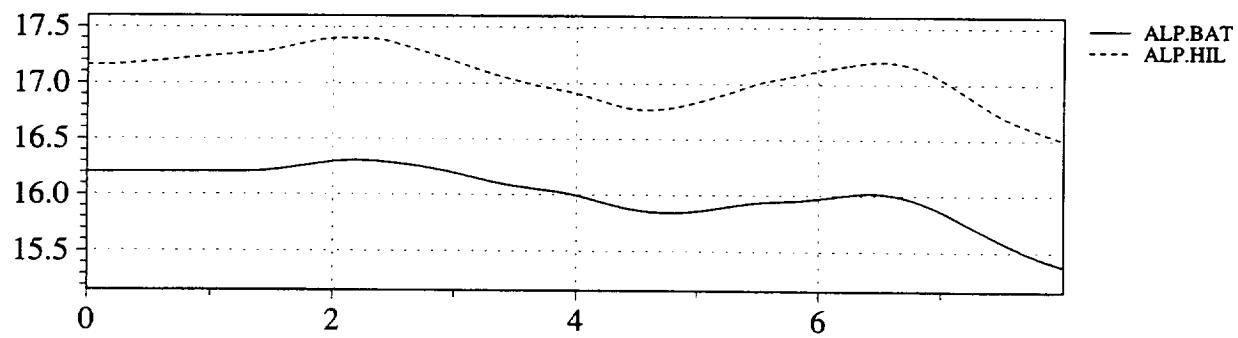
total pit stk cmd, in



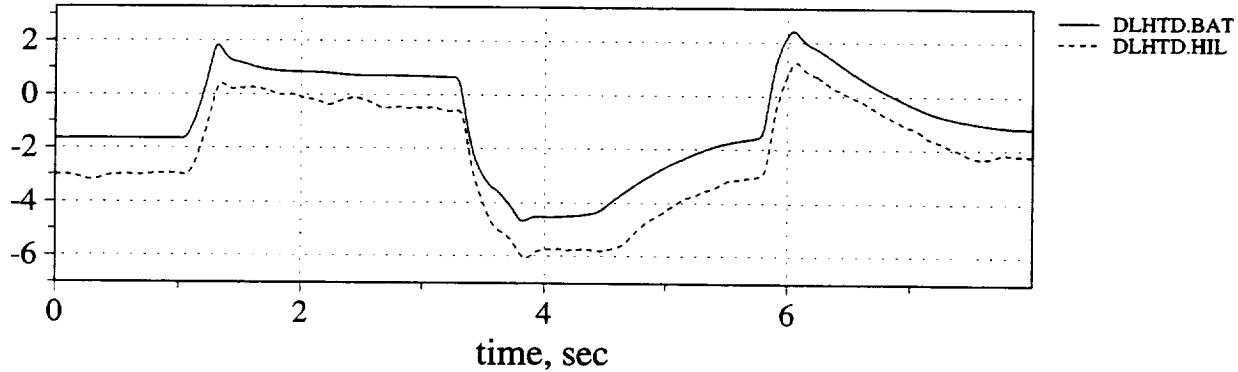
pitch rate, d/s



Angle-of-attack, dg

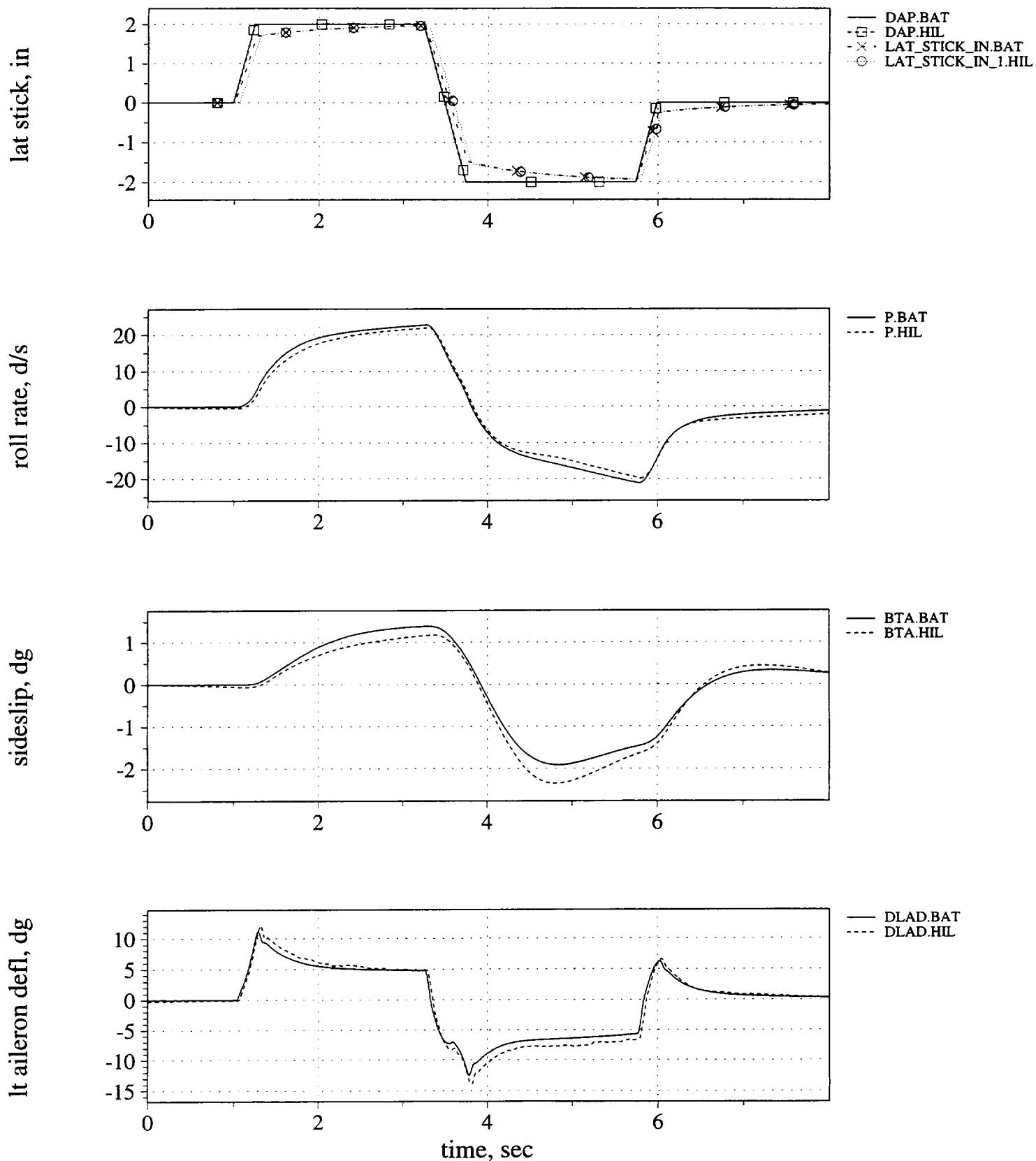


lt stab defl, dg



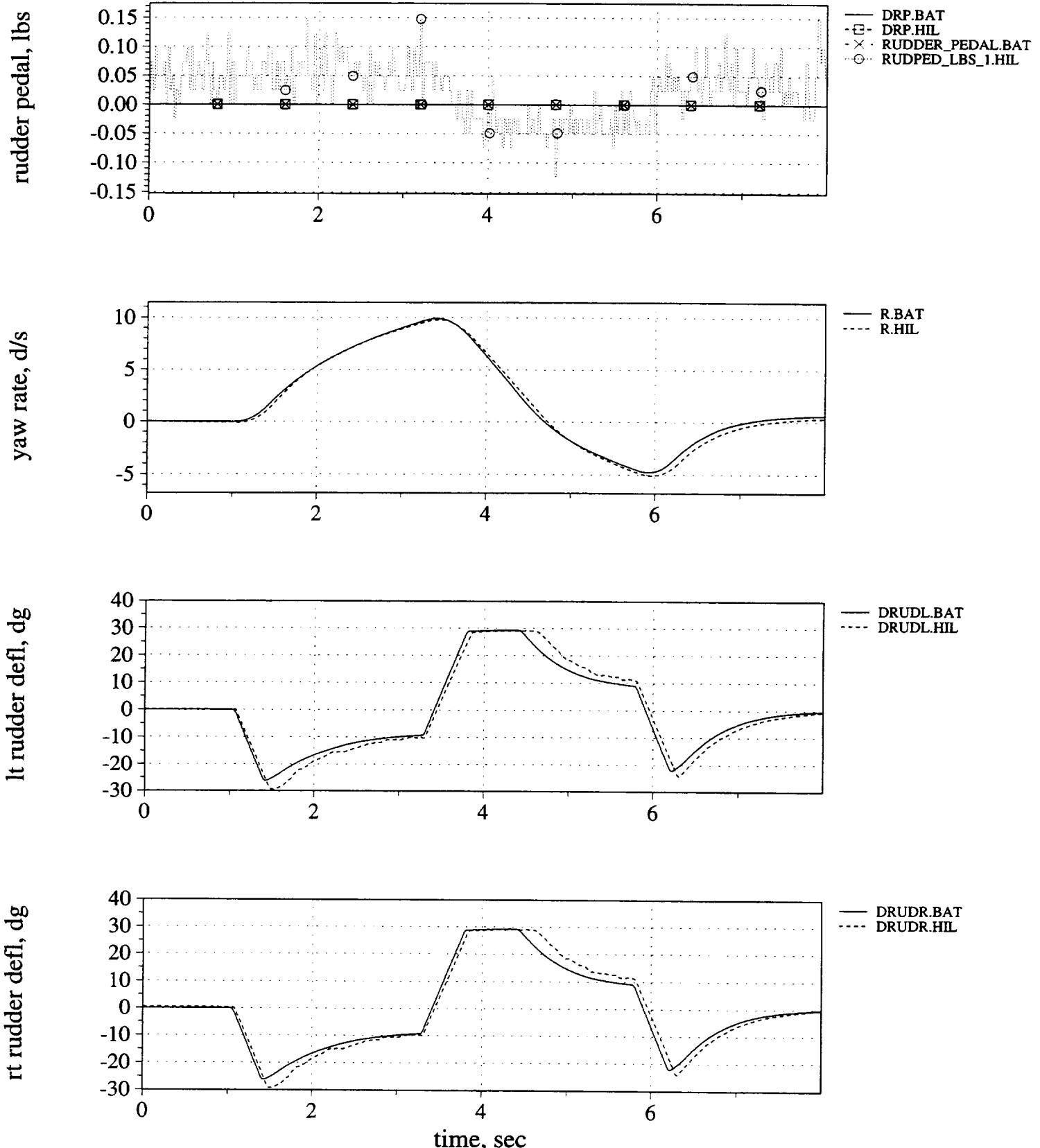
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 2/12



Performance Validation Checkcases

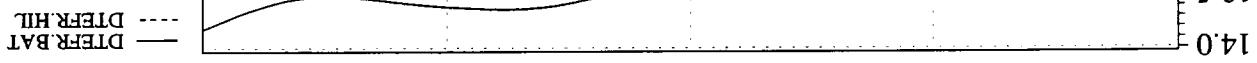
Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 3/12



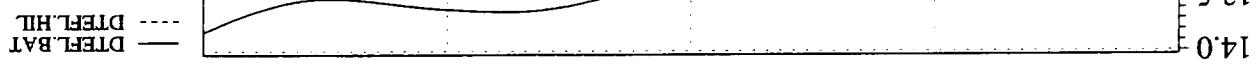
Performance Validation Checkcases

Performance Validation Checks

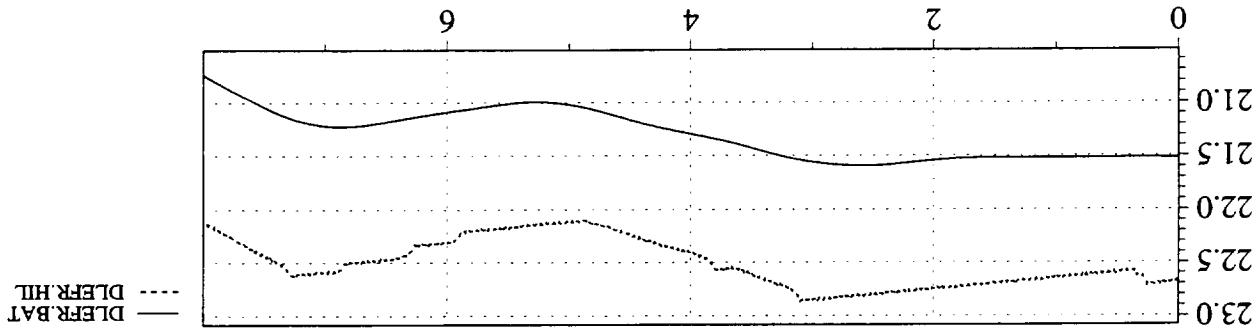
rt TEF defl, dg



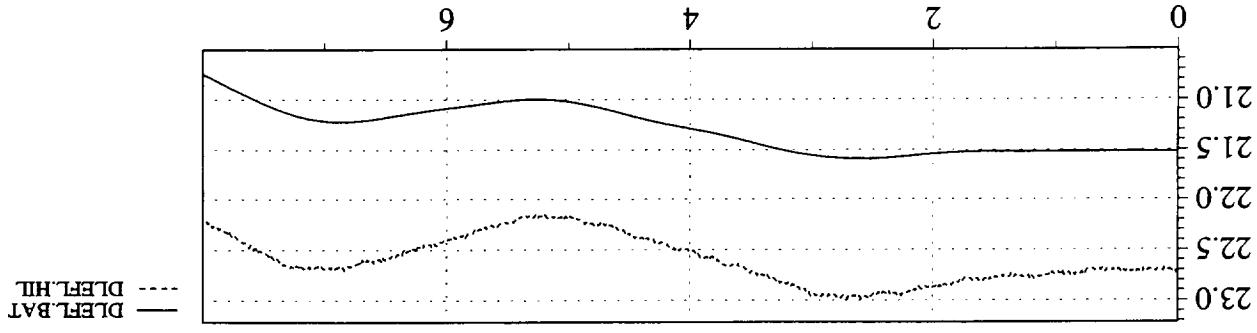
lt TEF defl, dg



rt LEF defl, dg

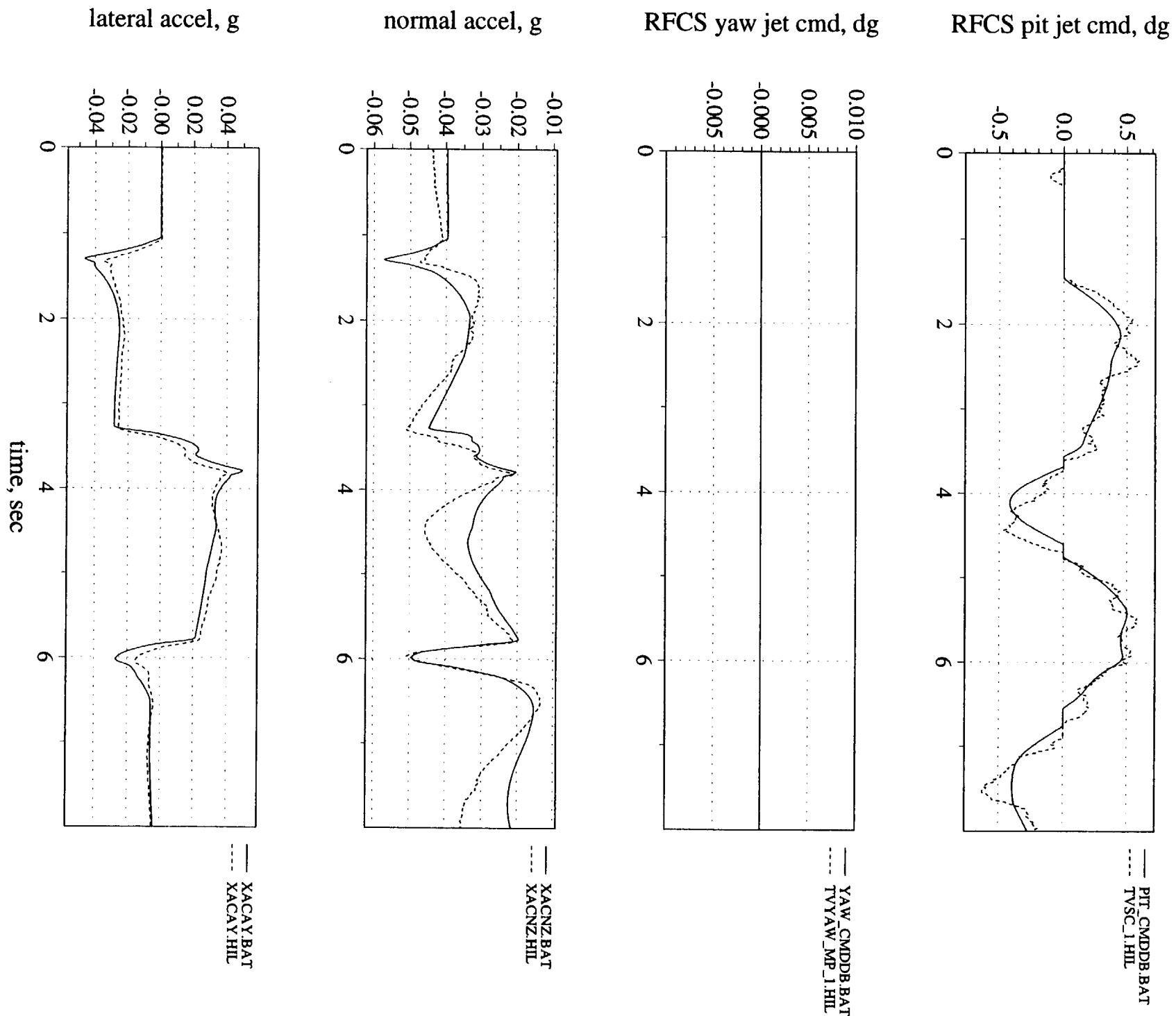


lt LEF defl, dg



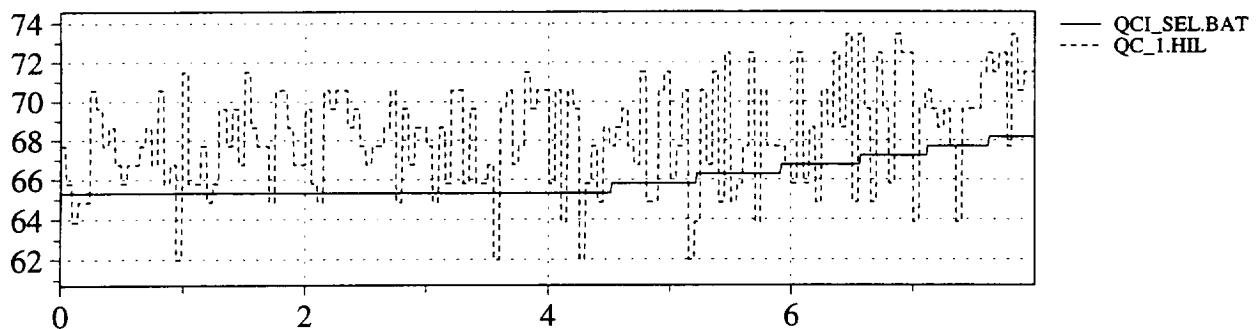
Appendix B - Batch versus HIL Version 154.0

CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 5/12

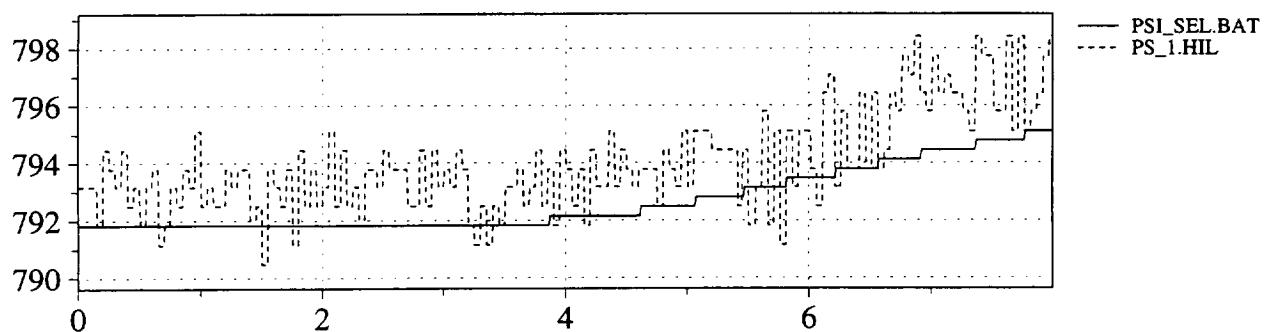


Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 6/12

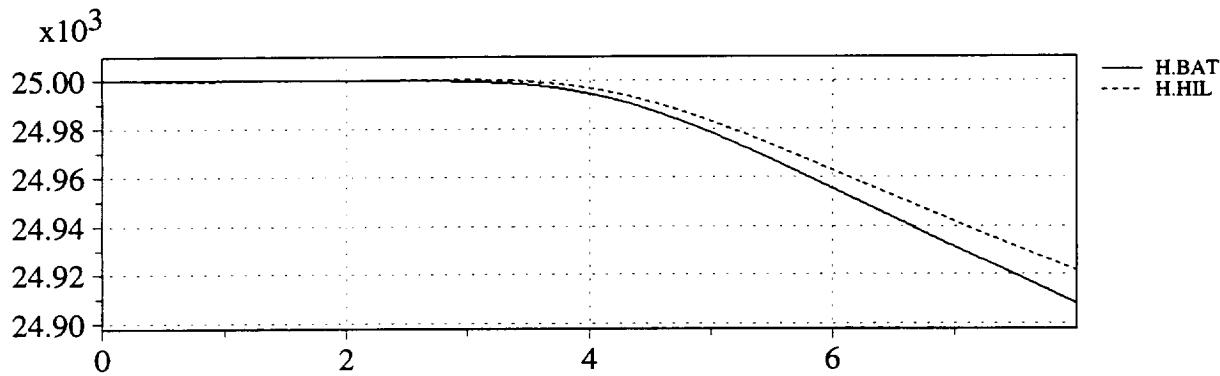
RFCS impact pres, psf



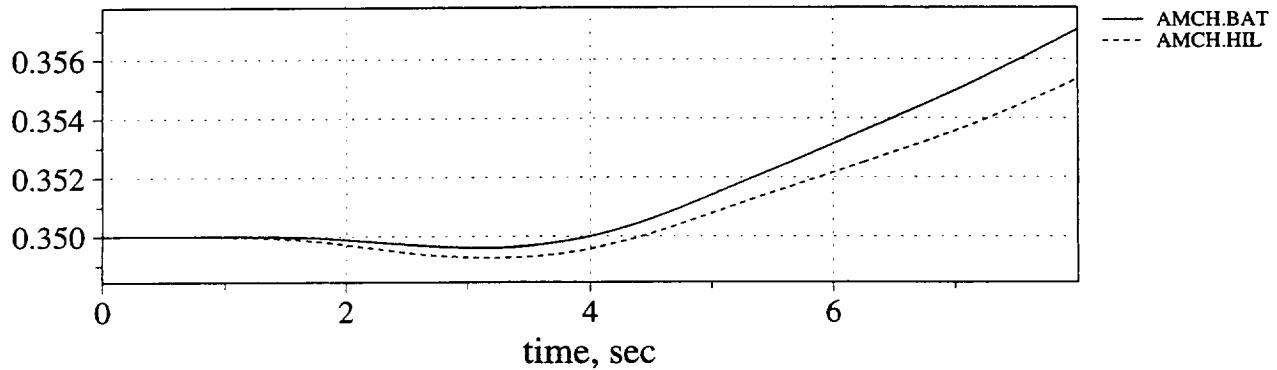
RFCS static pres, psf



altitude, ft

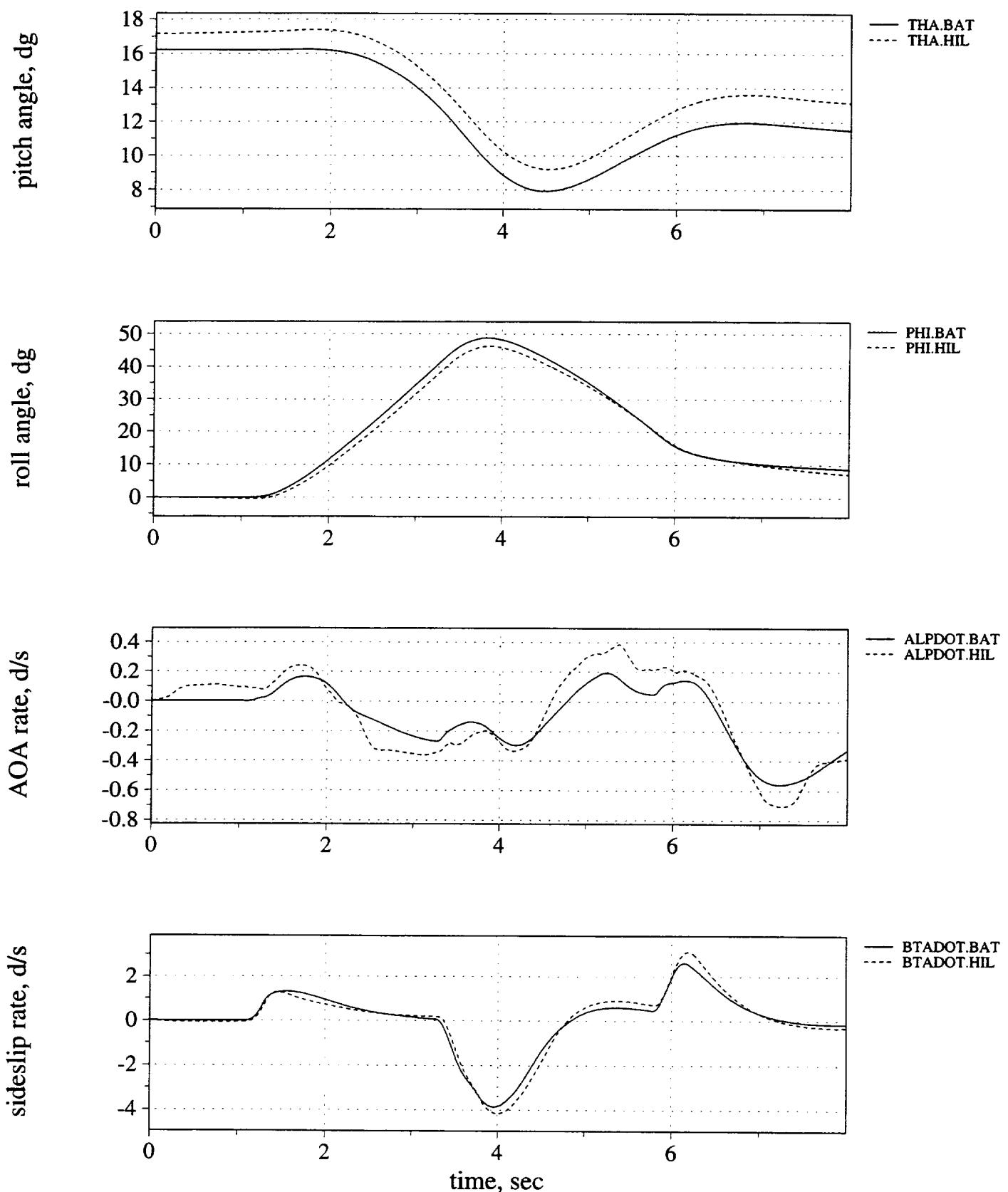


Mach number, nd



Performance Validation Checkcases

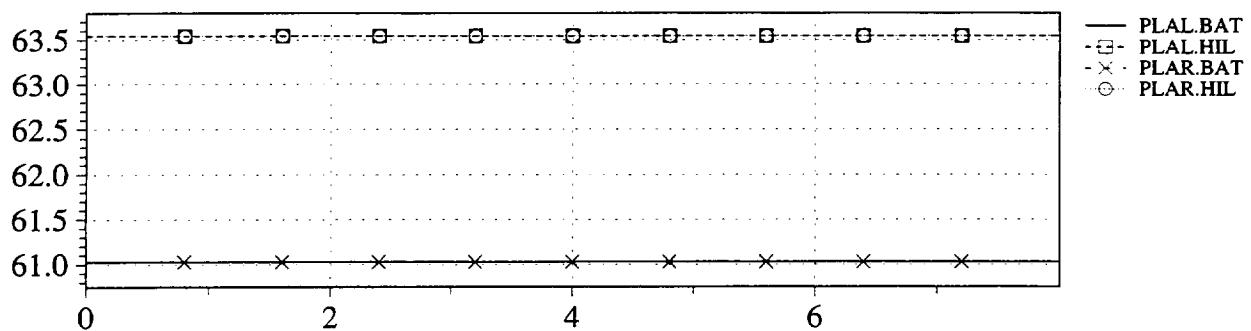
Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 7/12



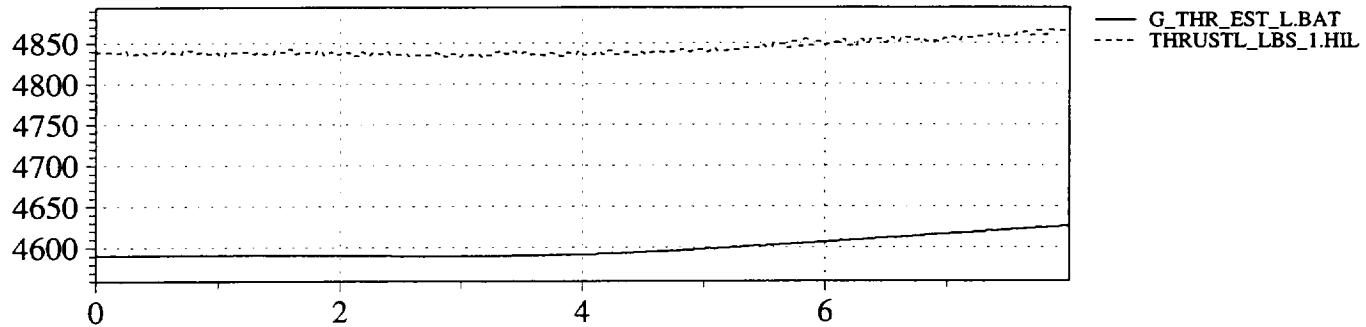
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 8/12

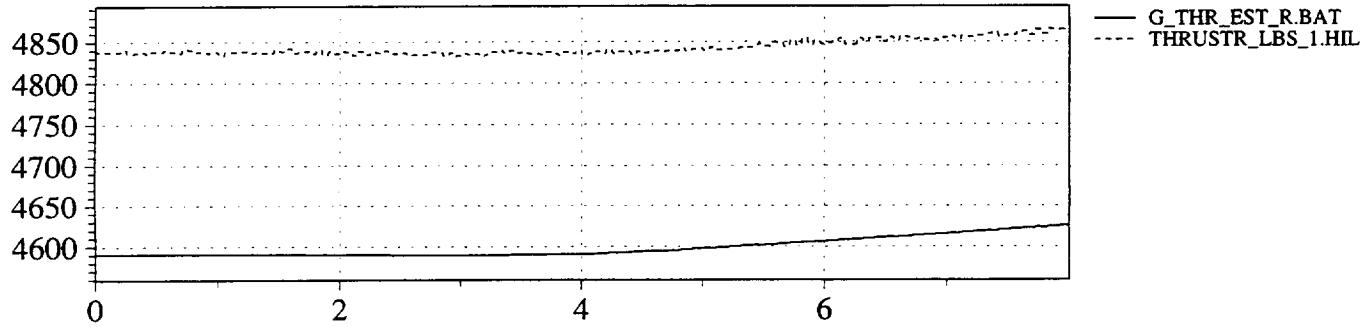
lt/rt power lever ang



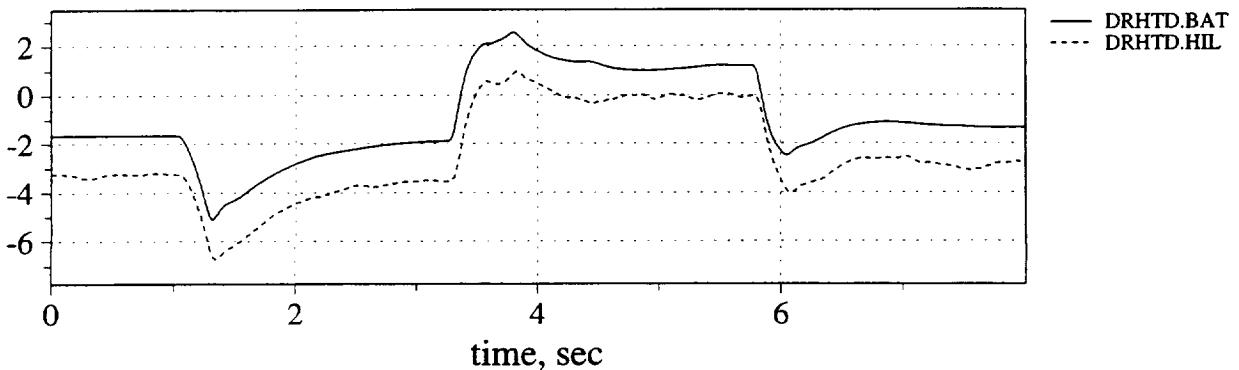
lt gross thr est, lbs



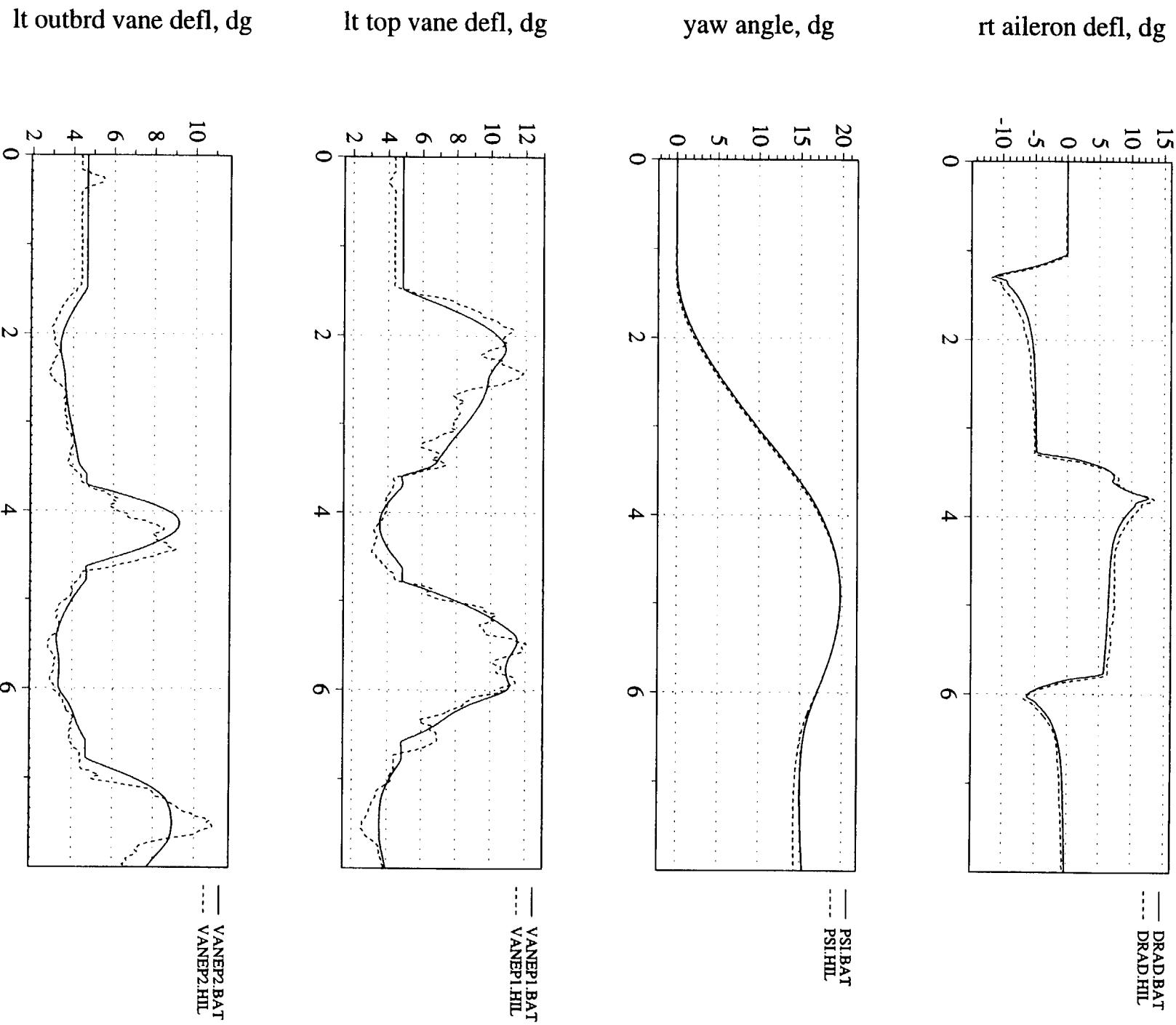
rt gross thr est, lbs



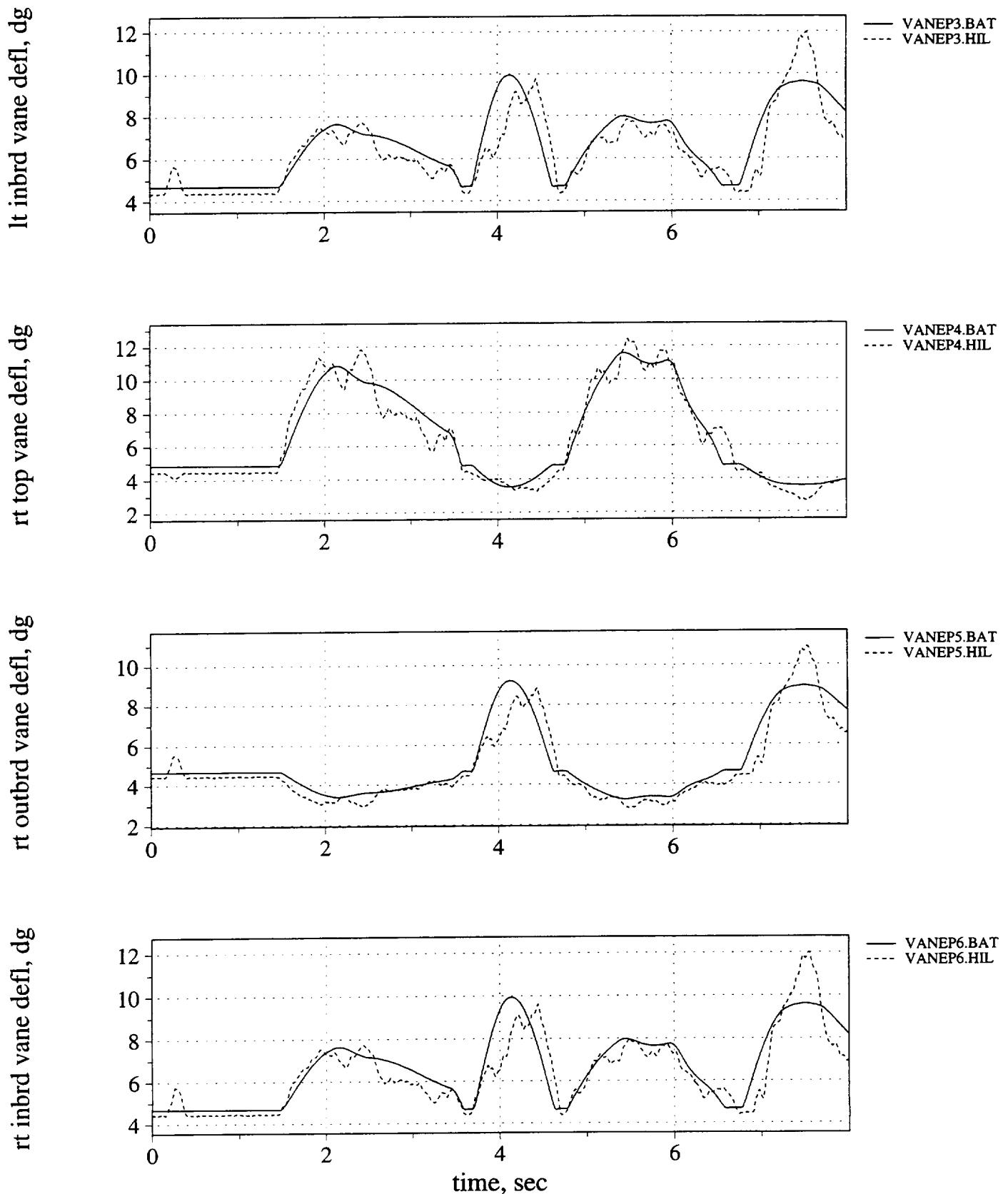
rt stab defl, dg



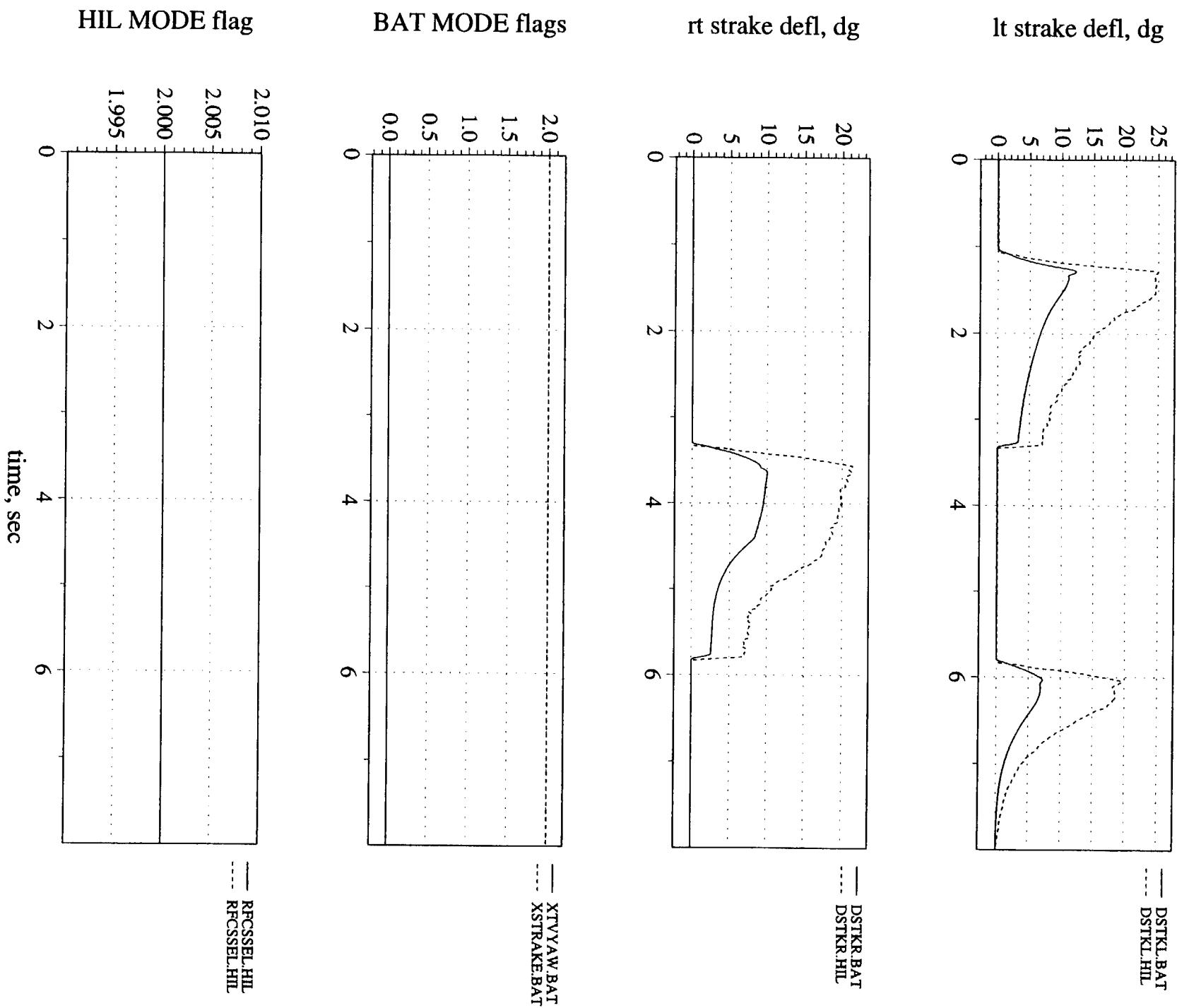
Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 9/12



Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 10/12

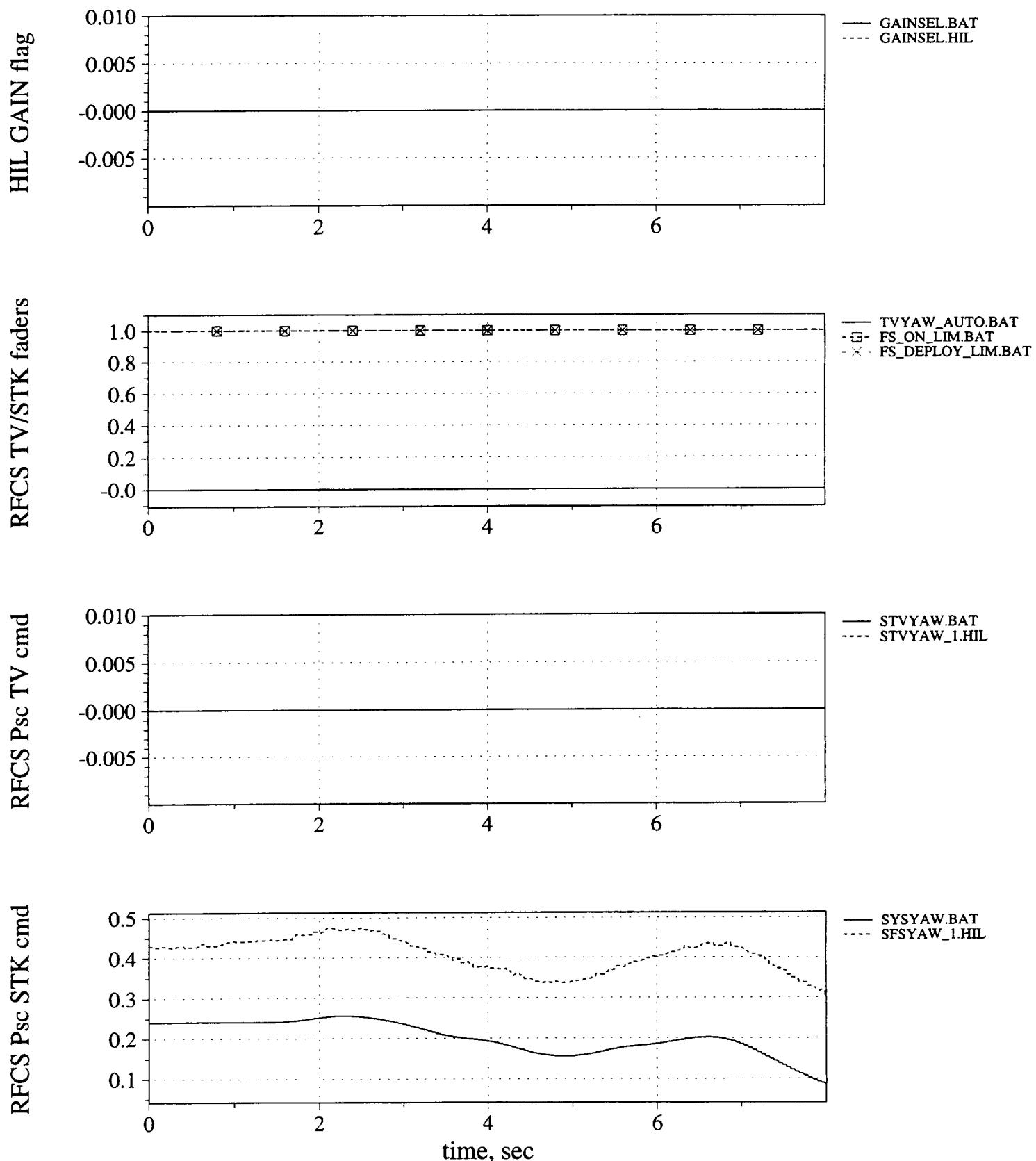


Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_35-25k_ttim_AOA_2_in_ramp_dap page 11/12



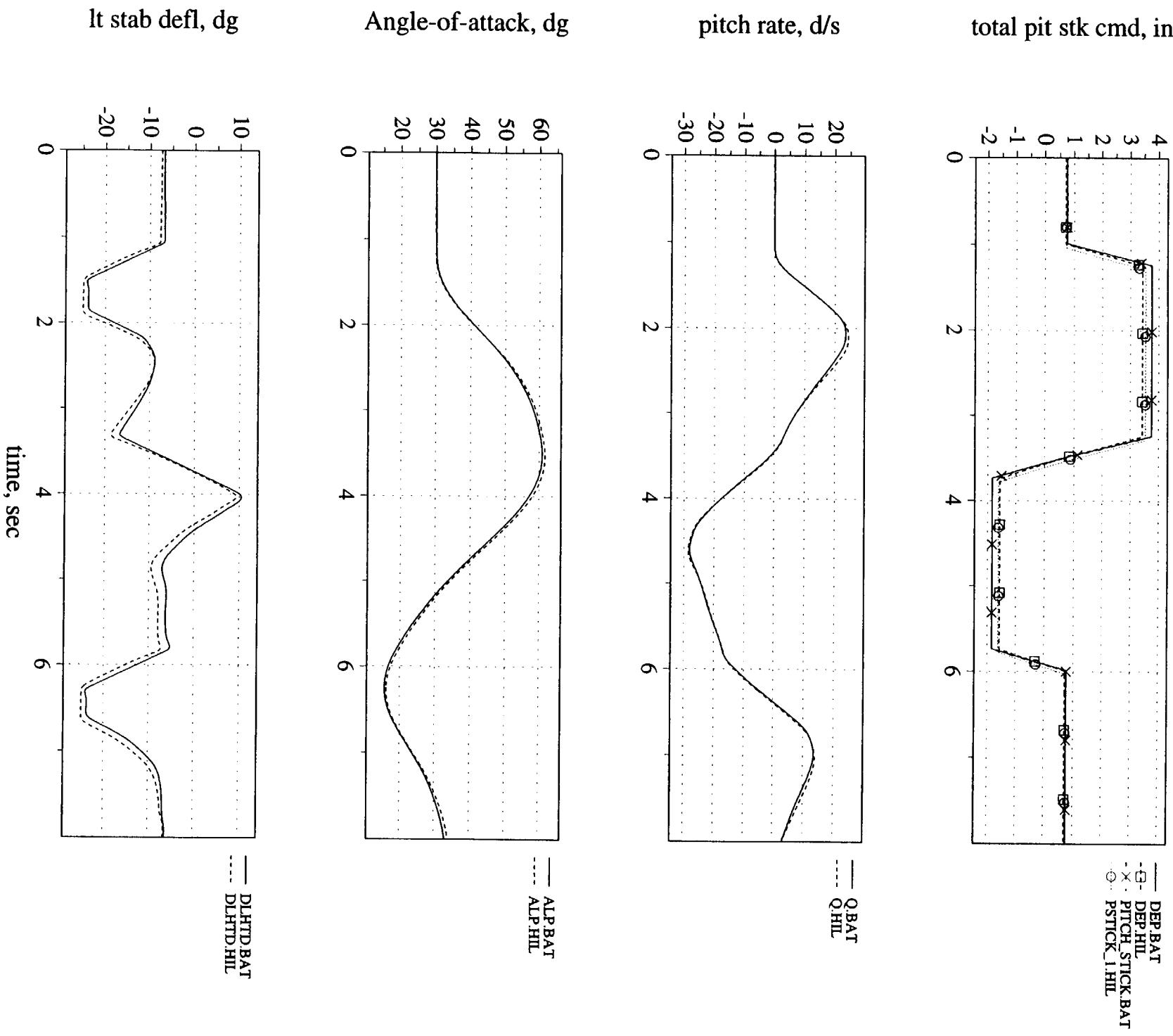
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_20_S_Mode_.35-25k_trim_AOA_2_in_ramp_dap page 12/12



Appendix B - Batch versus HIL Version 154.0

CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 1/12

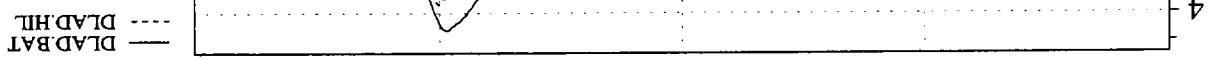


Performance Validation Checks

lt aileron defl, dg

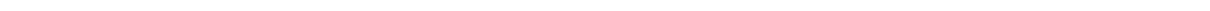
DLAD_HIL

time, sec



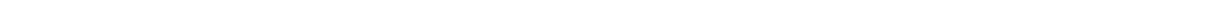
sideslip, dg

BTABAT



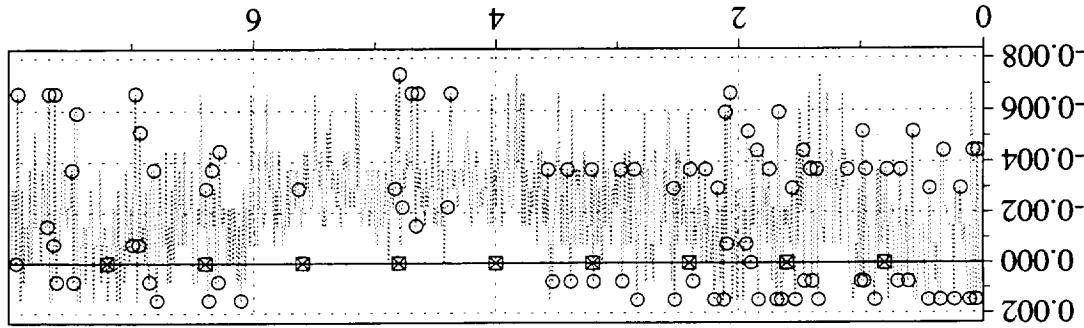
roll rate, d/s

PBAT

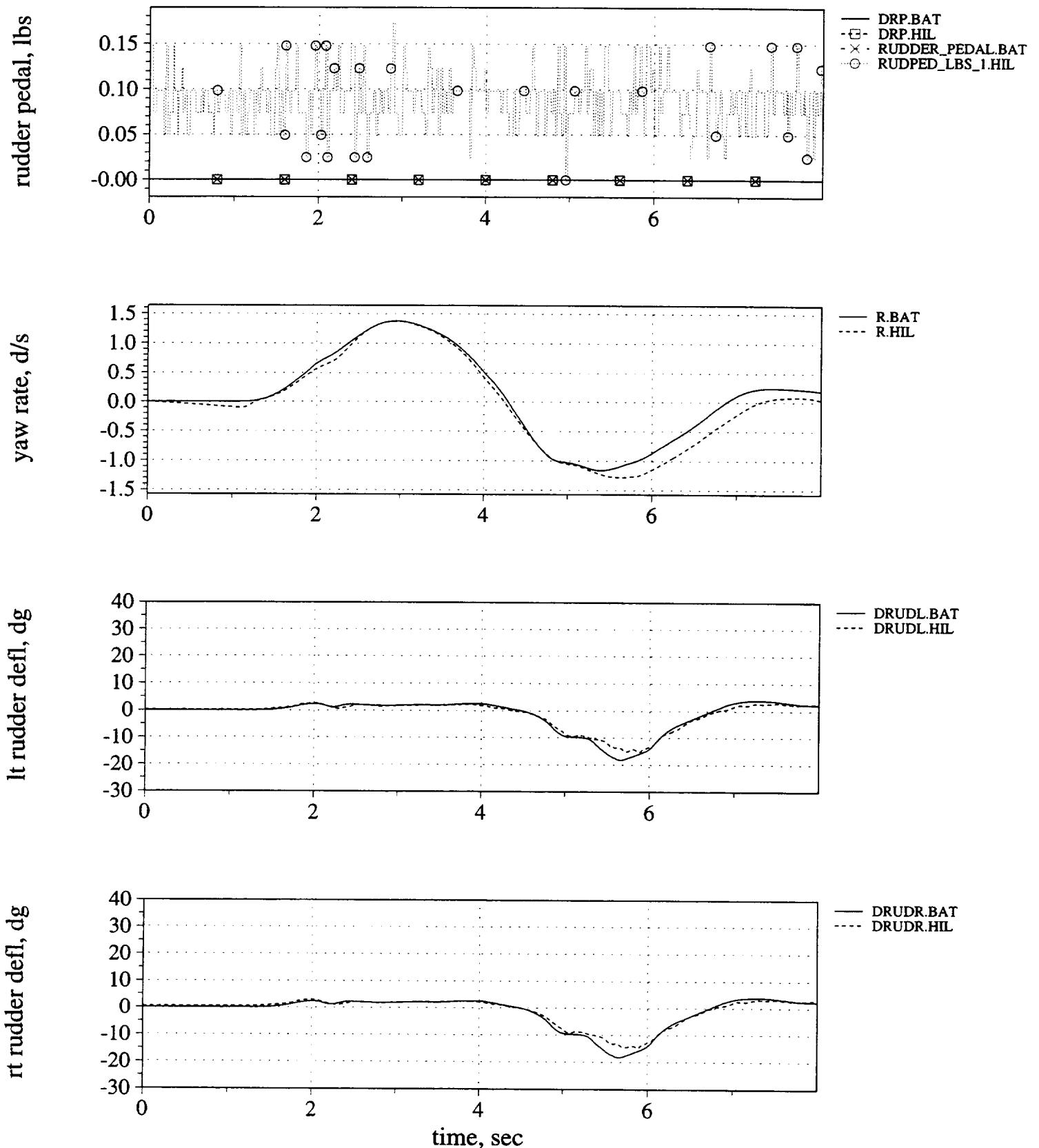


lat stick, in

DAP_HIL LAT_STICK_IN_BAT

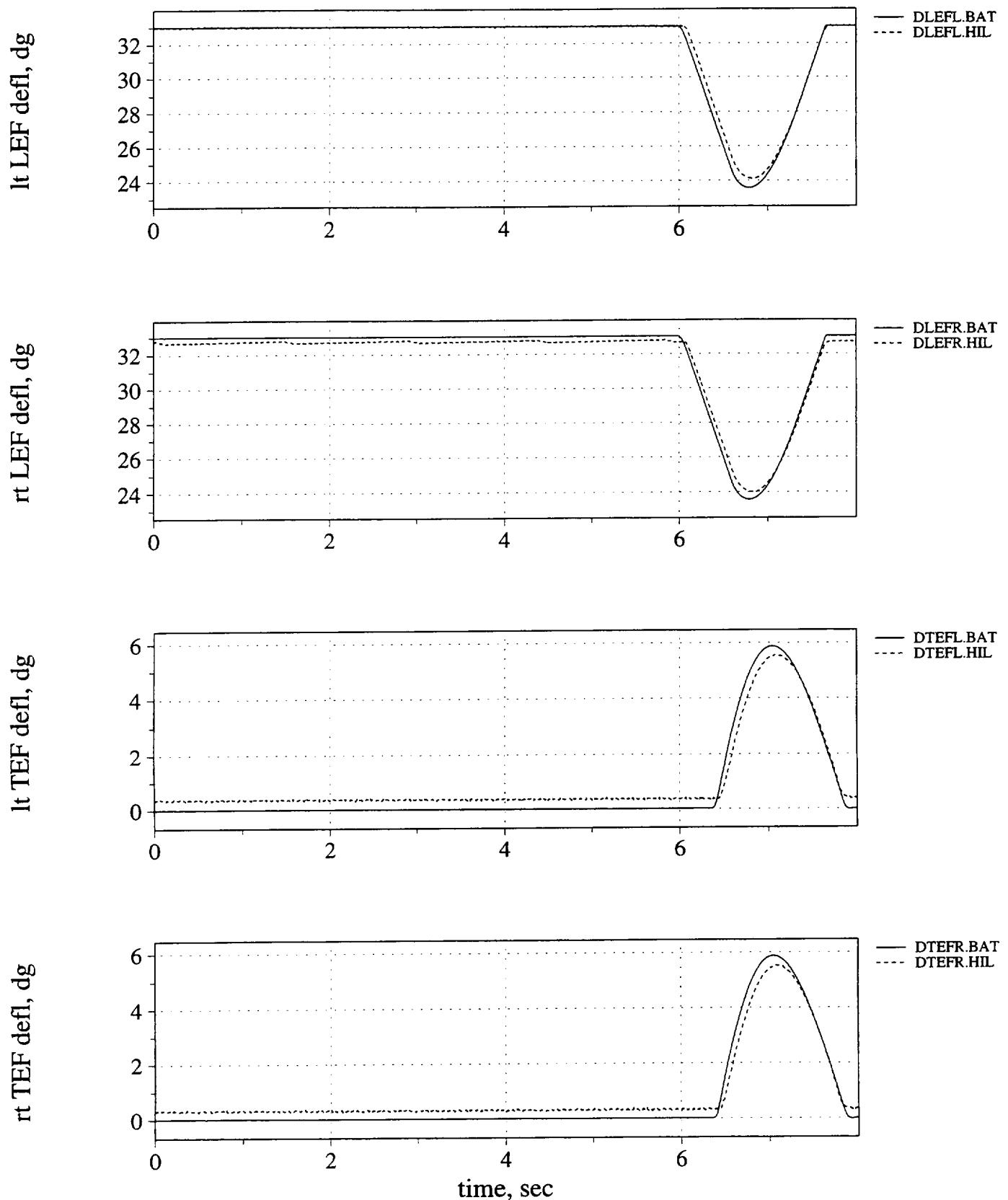


Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 3/12



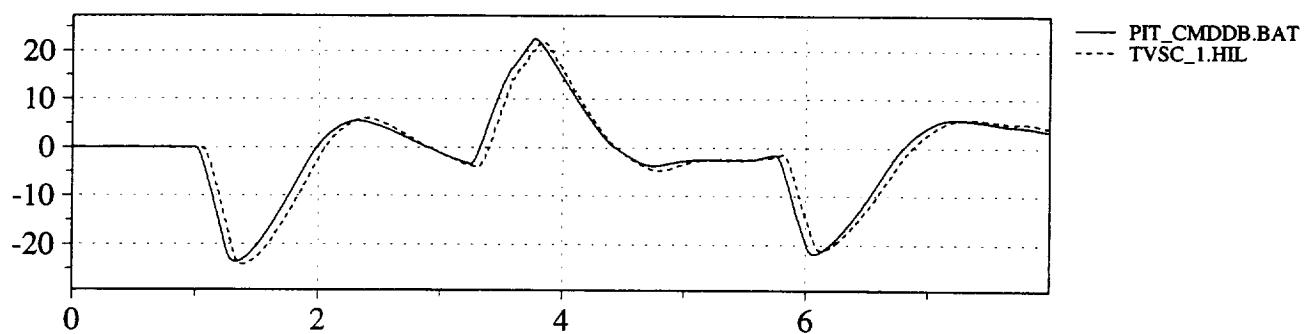
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 4/12

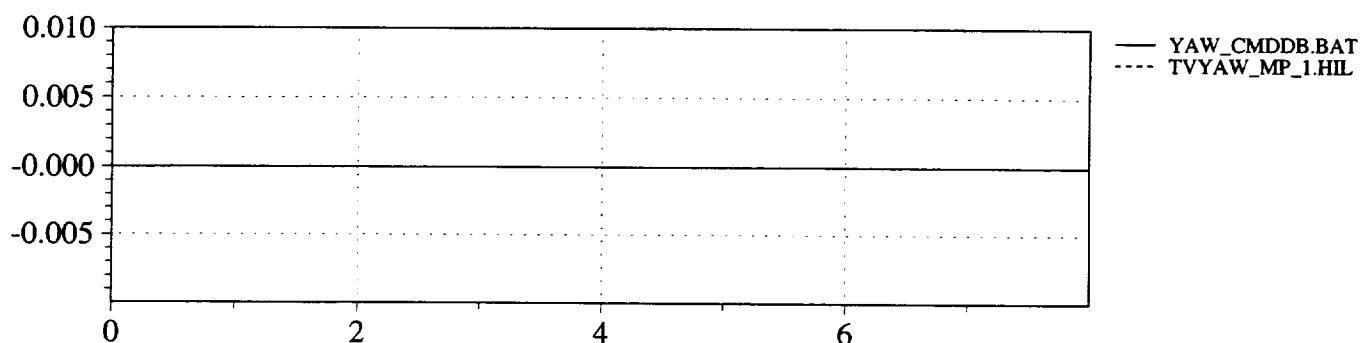


Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 5/12

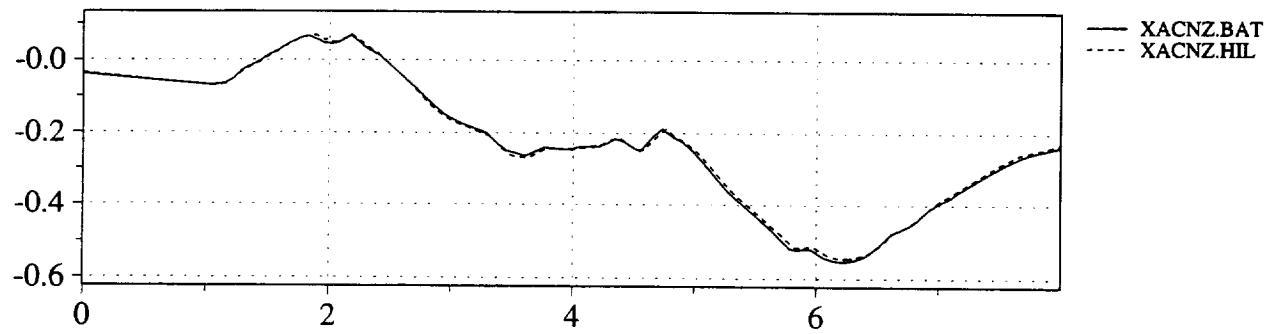
RFCS pit jet cmd, dg



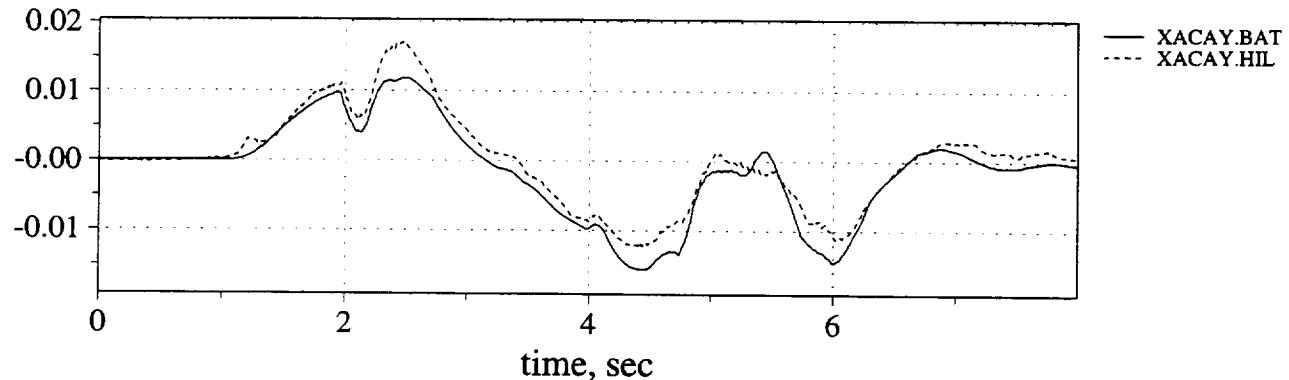
RFCS yaw jet cmd, dg



normal accel, g



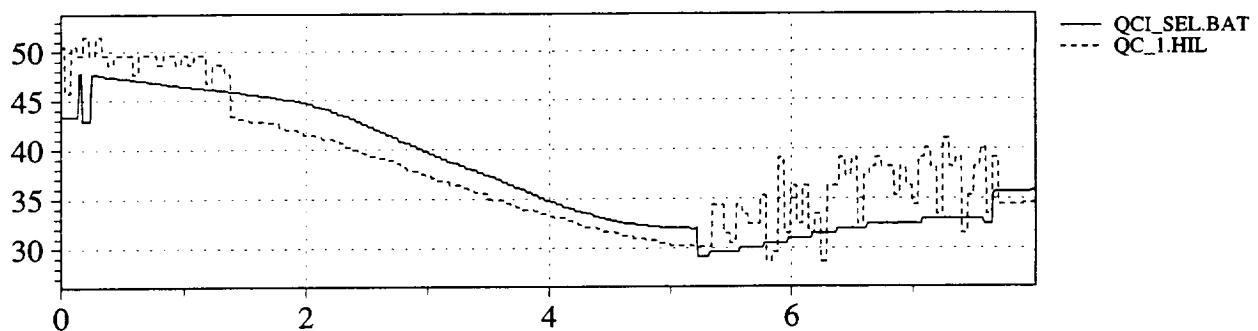
lateral accel, g



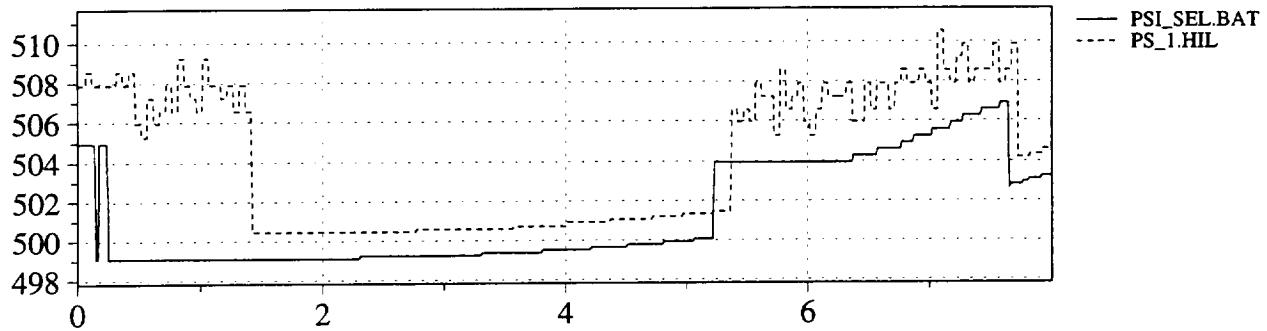
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 6/12

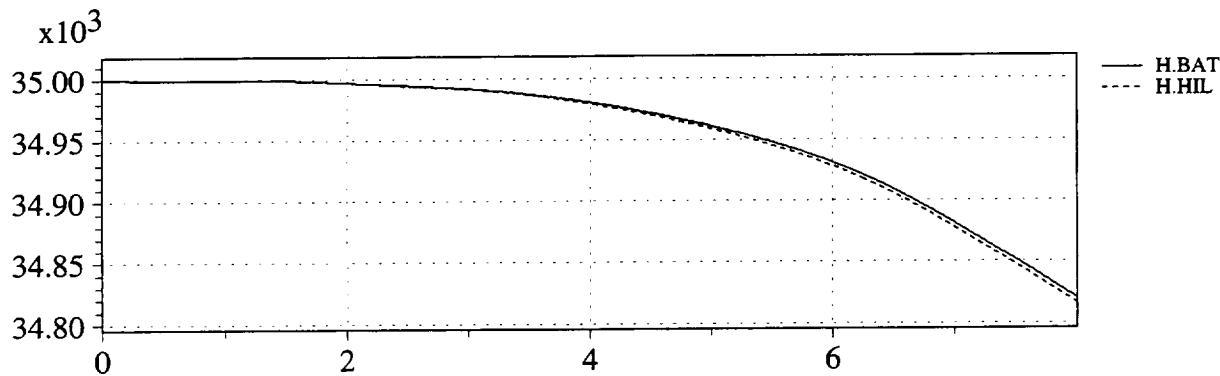
RFCS impact pres, psf



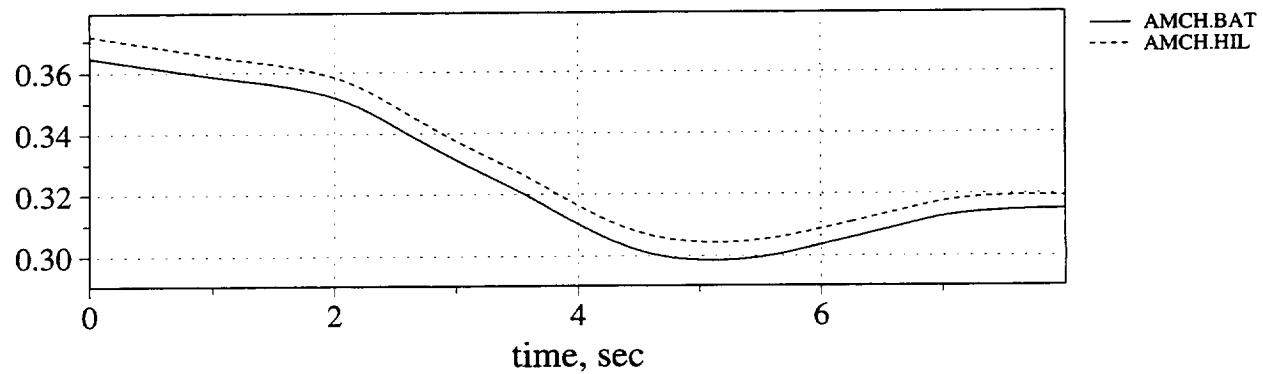
RFCS static pres, psf



altitude, ft
 $\times 10^3$

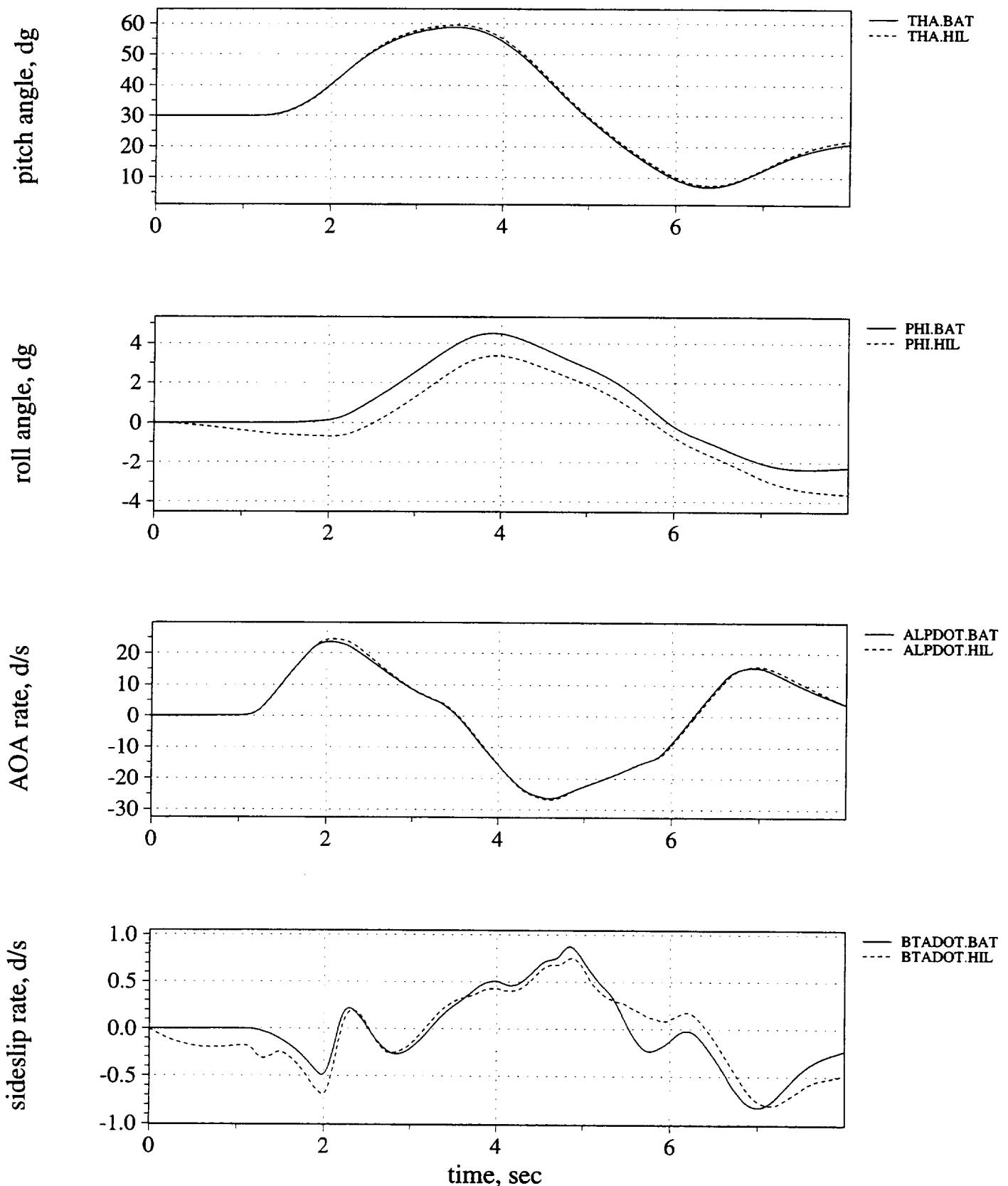


Mach number, nd



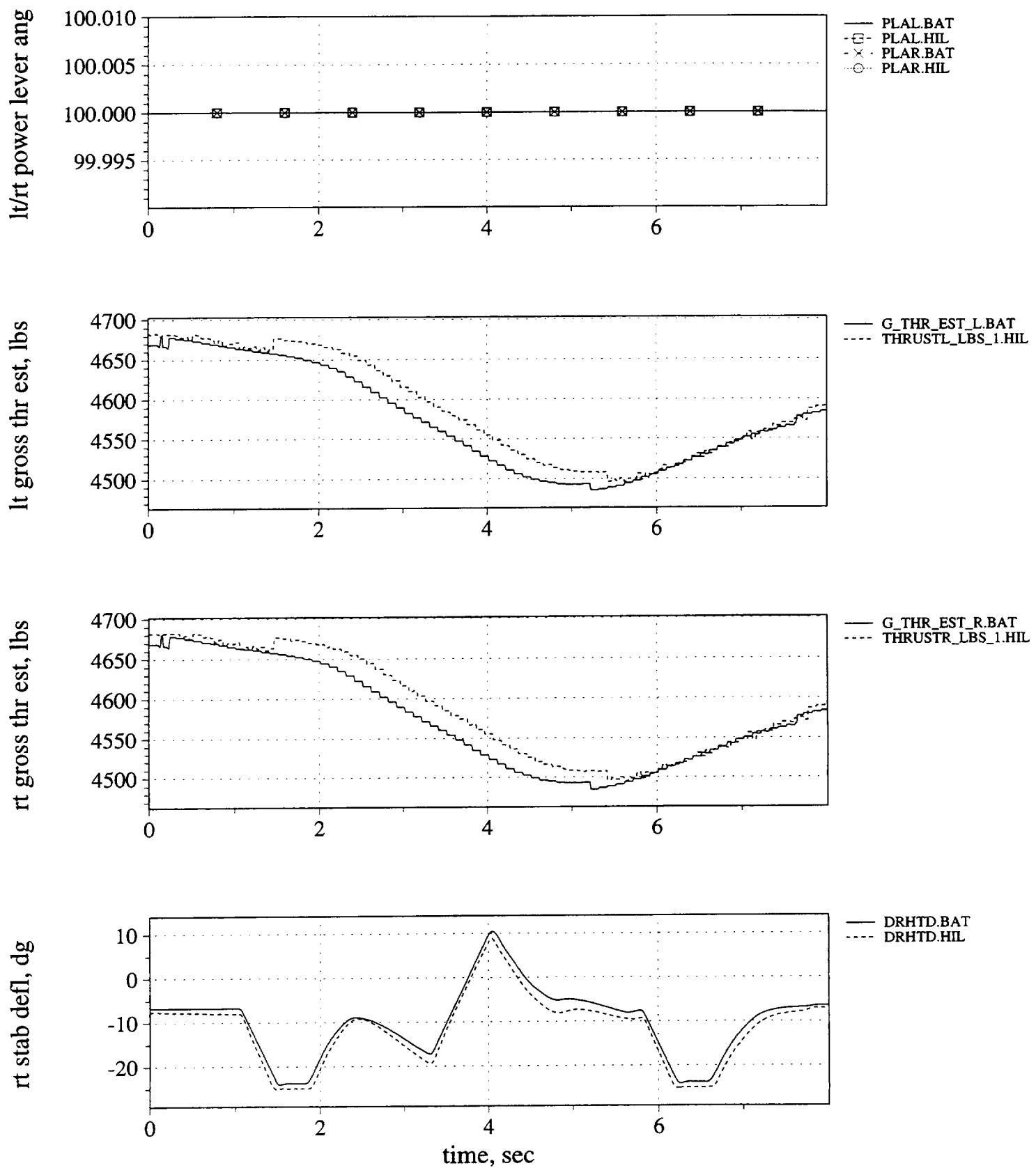
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 7/12

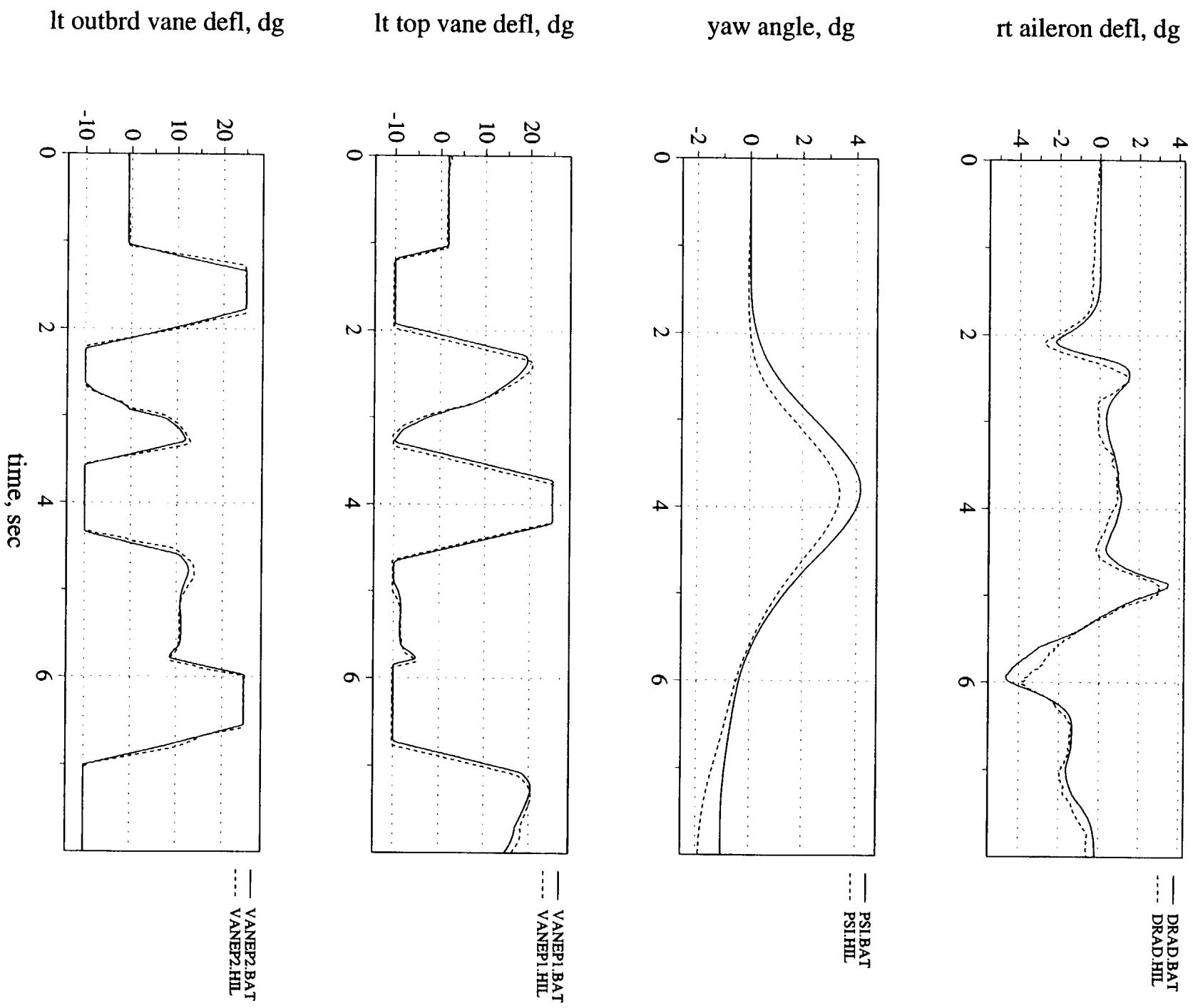


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 8/12

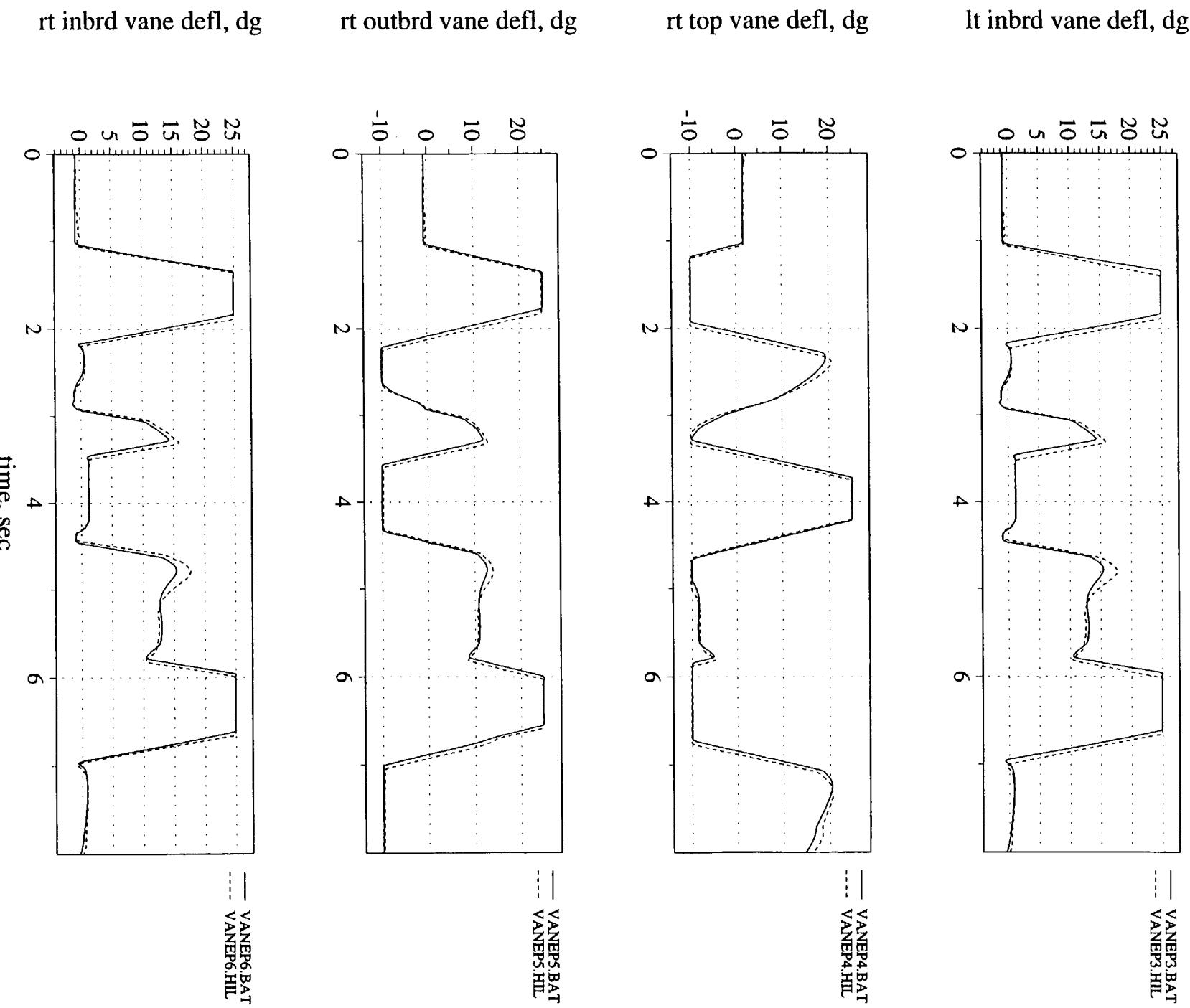


Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 9/12

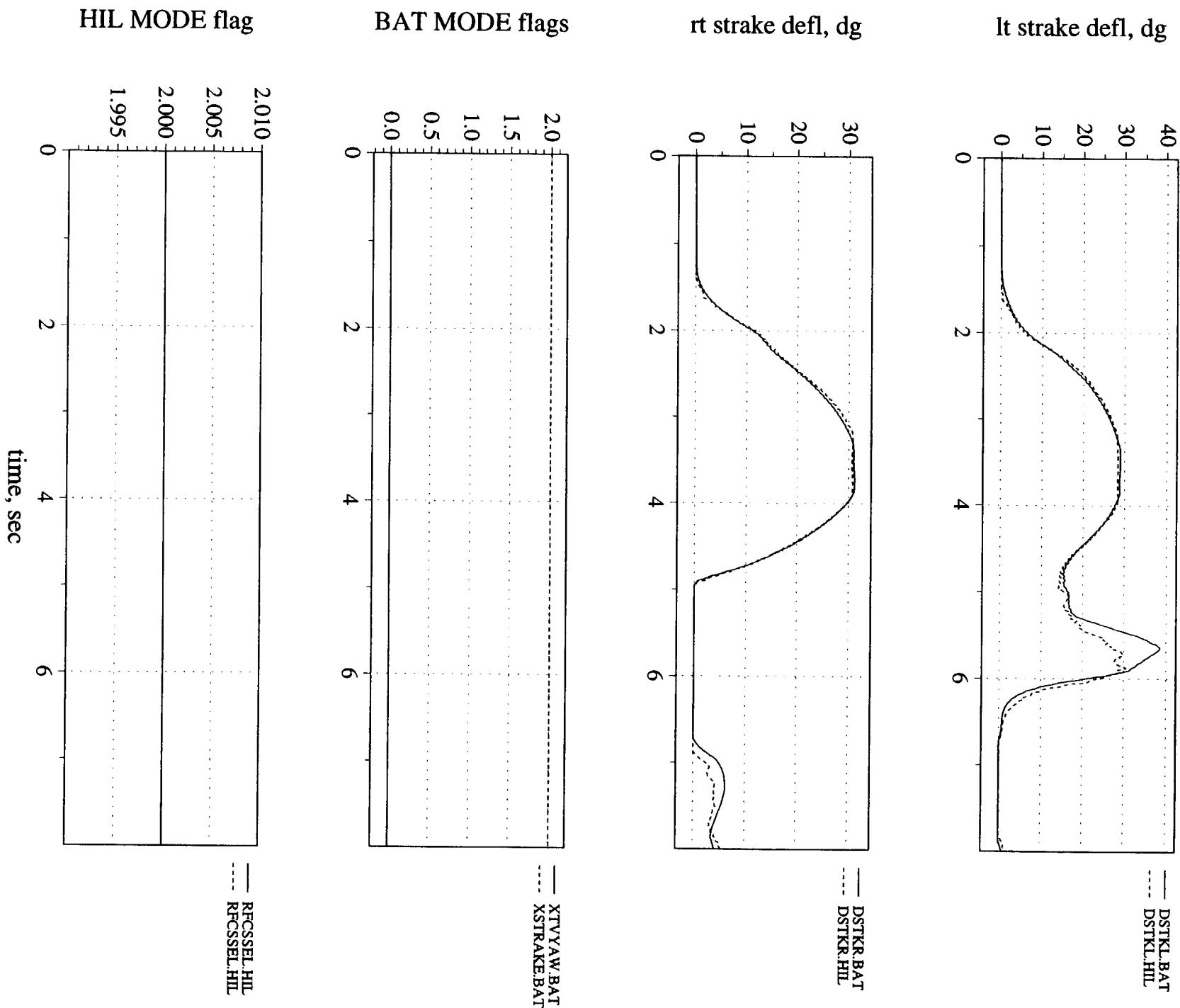


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 10/12

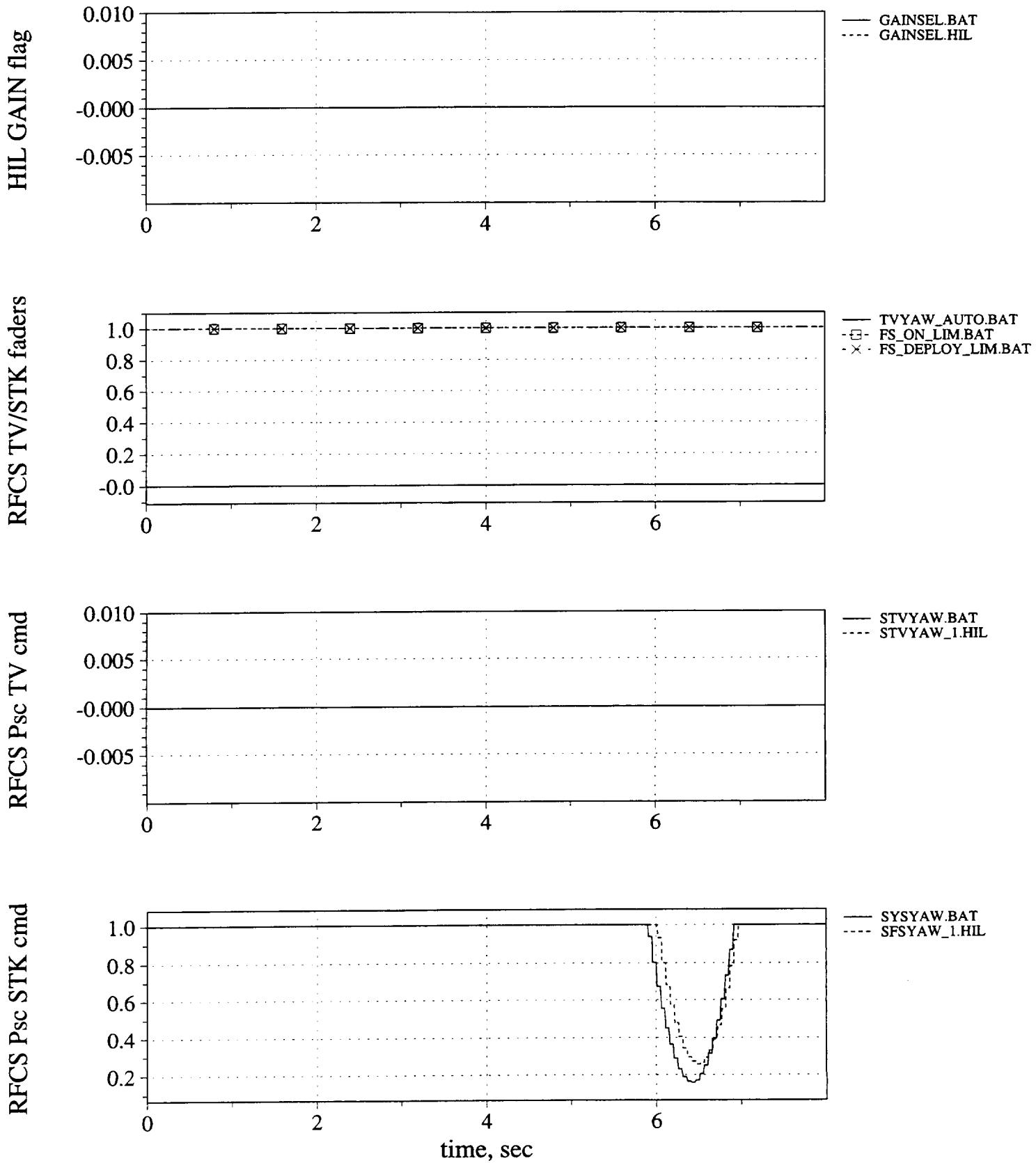


Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 11/12

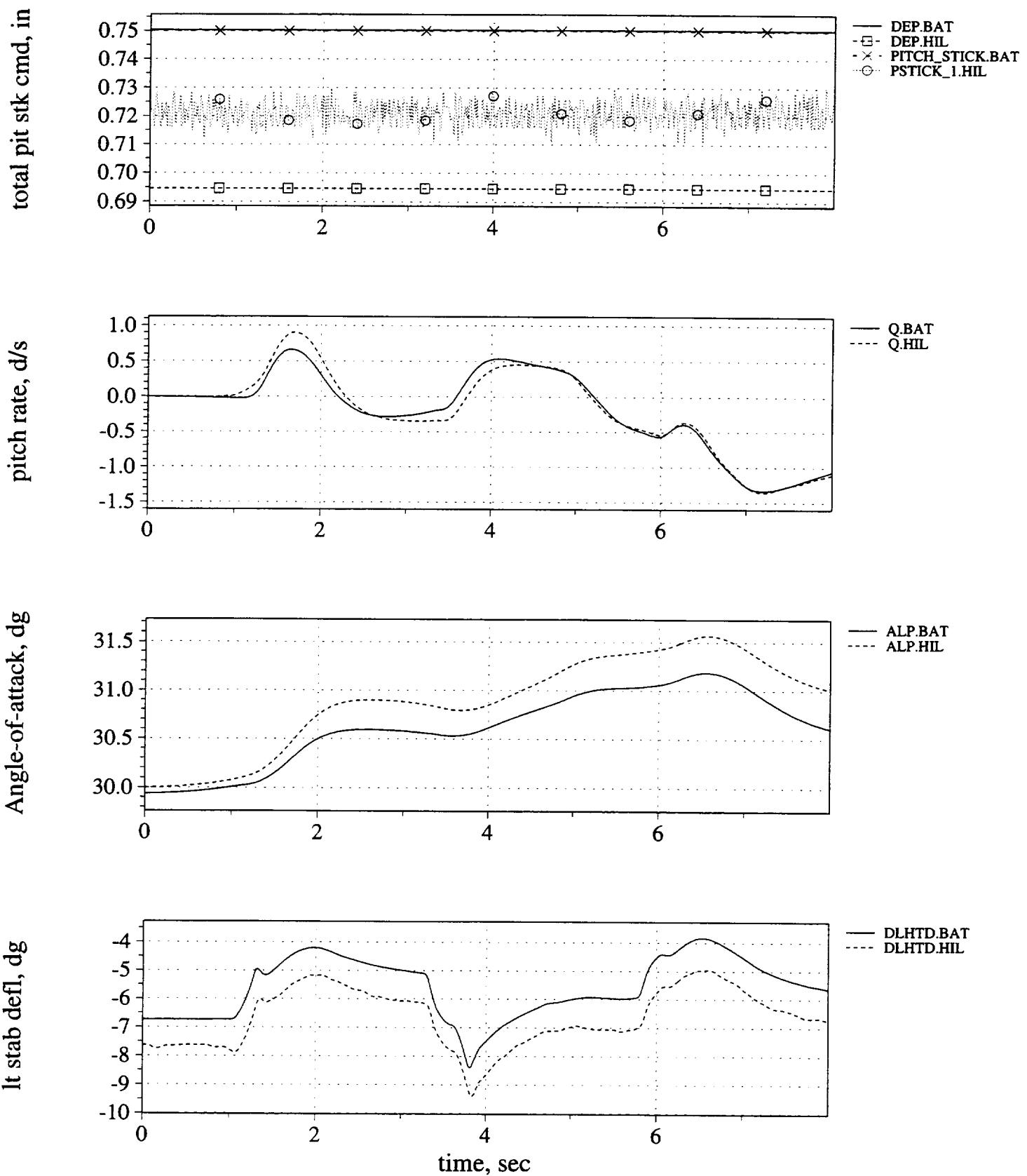


Performance Validation Checkcases

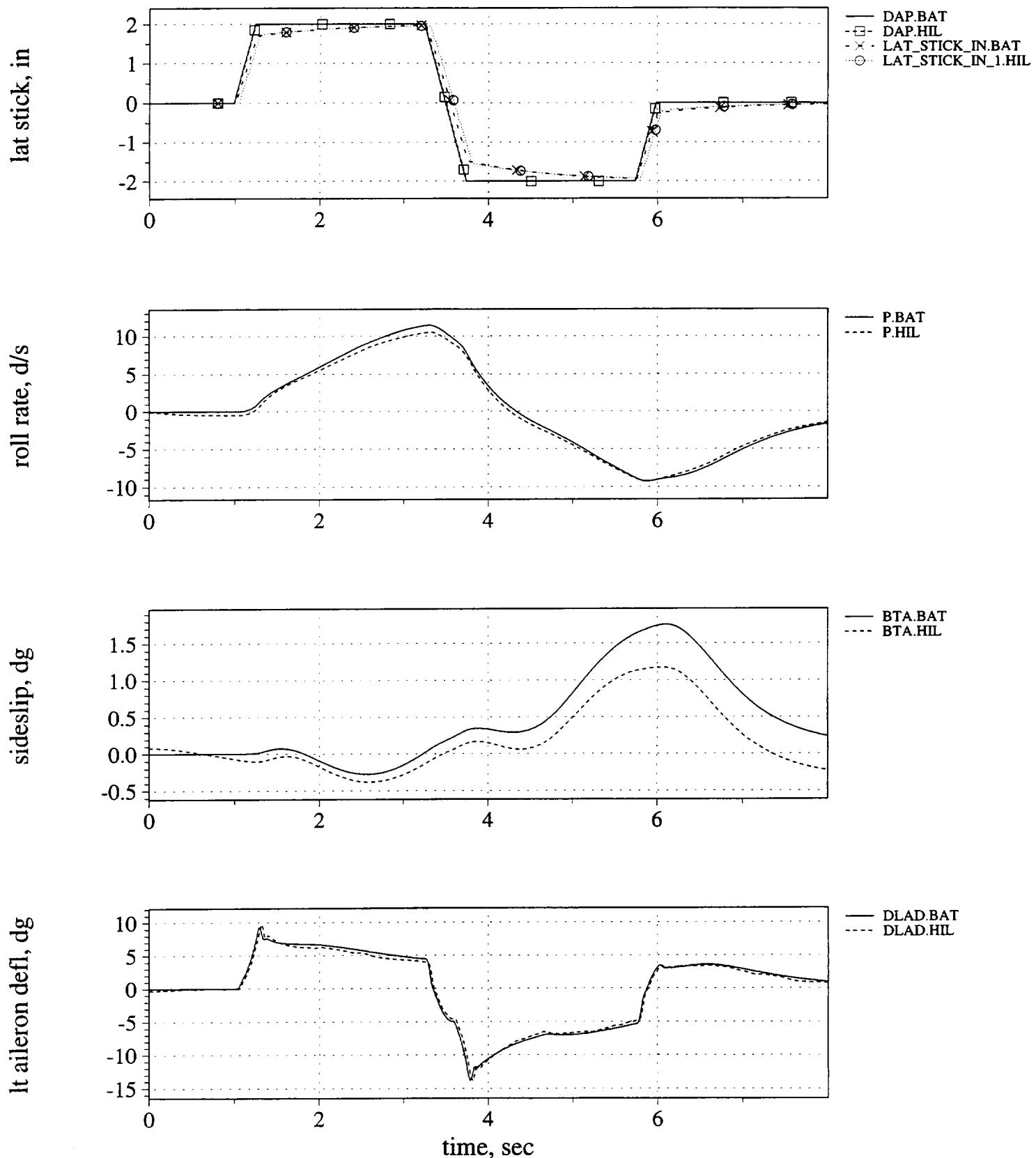
Appendix B - Batch versus HIL Version 154.0
CASE_22_S_Mode_30alp-35k_Max_AB_2_in_ramp_dep page 12/12



Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 1/12

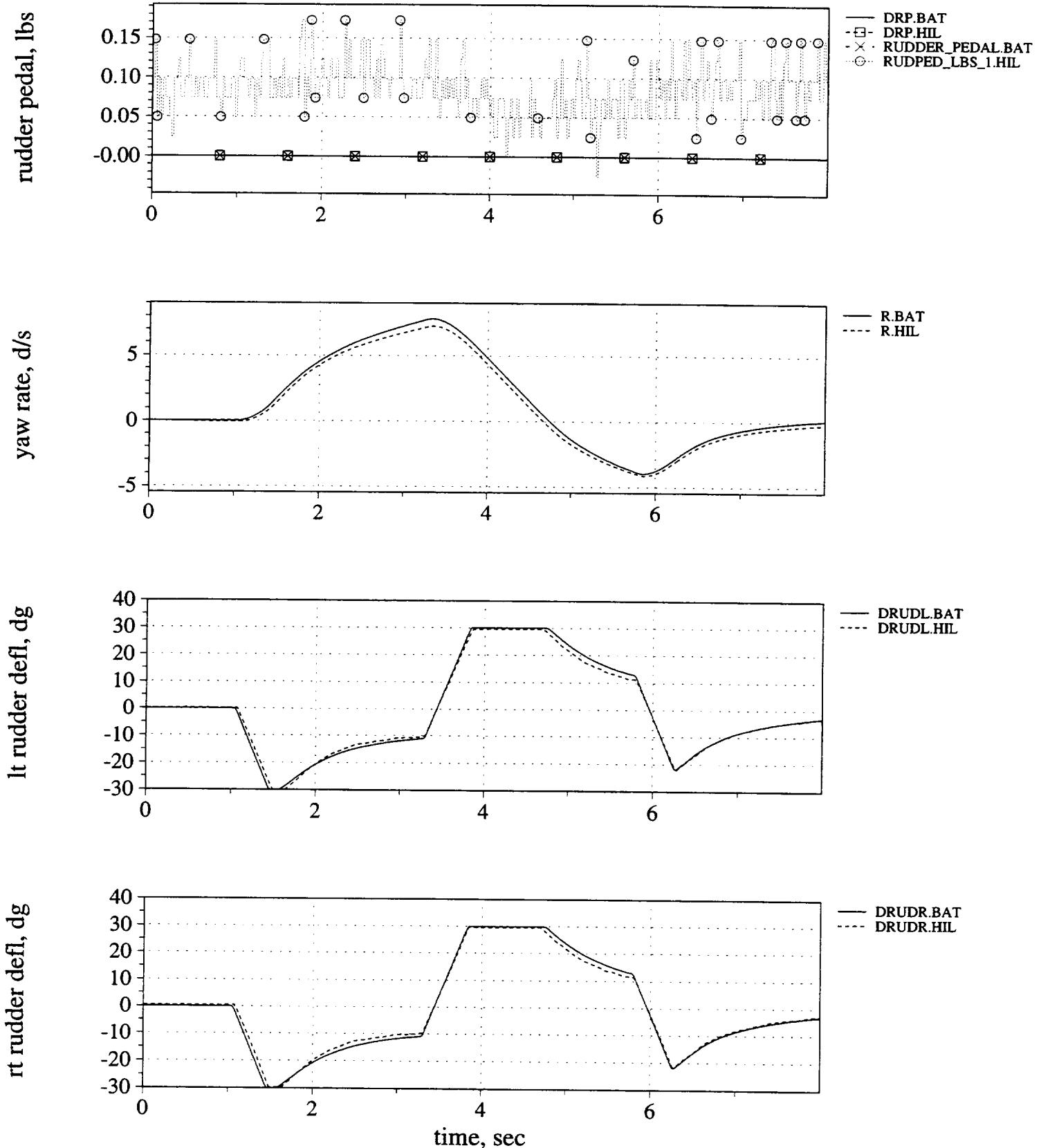


Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 2/12



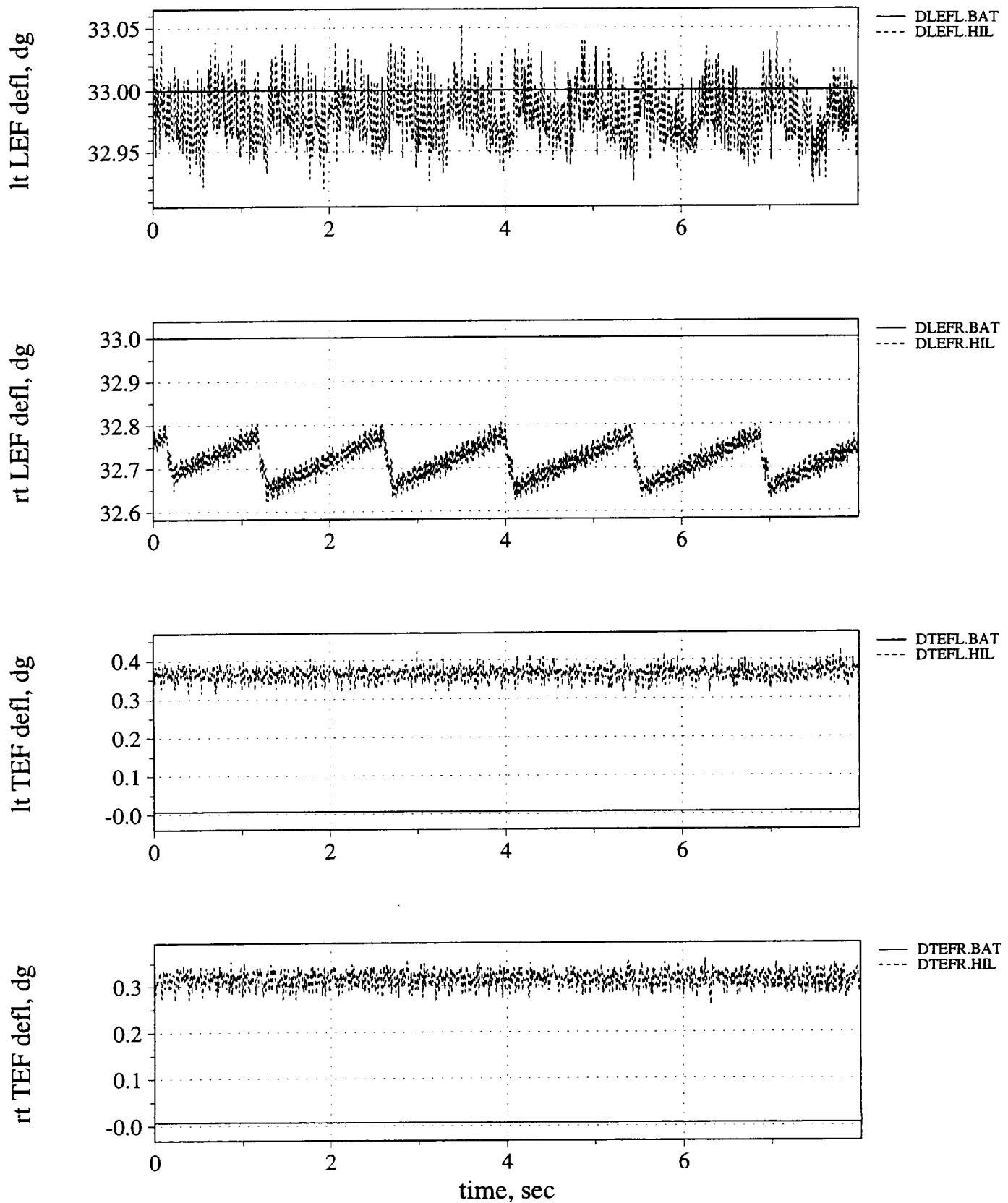
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 3/12

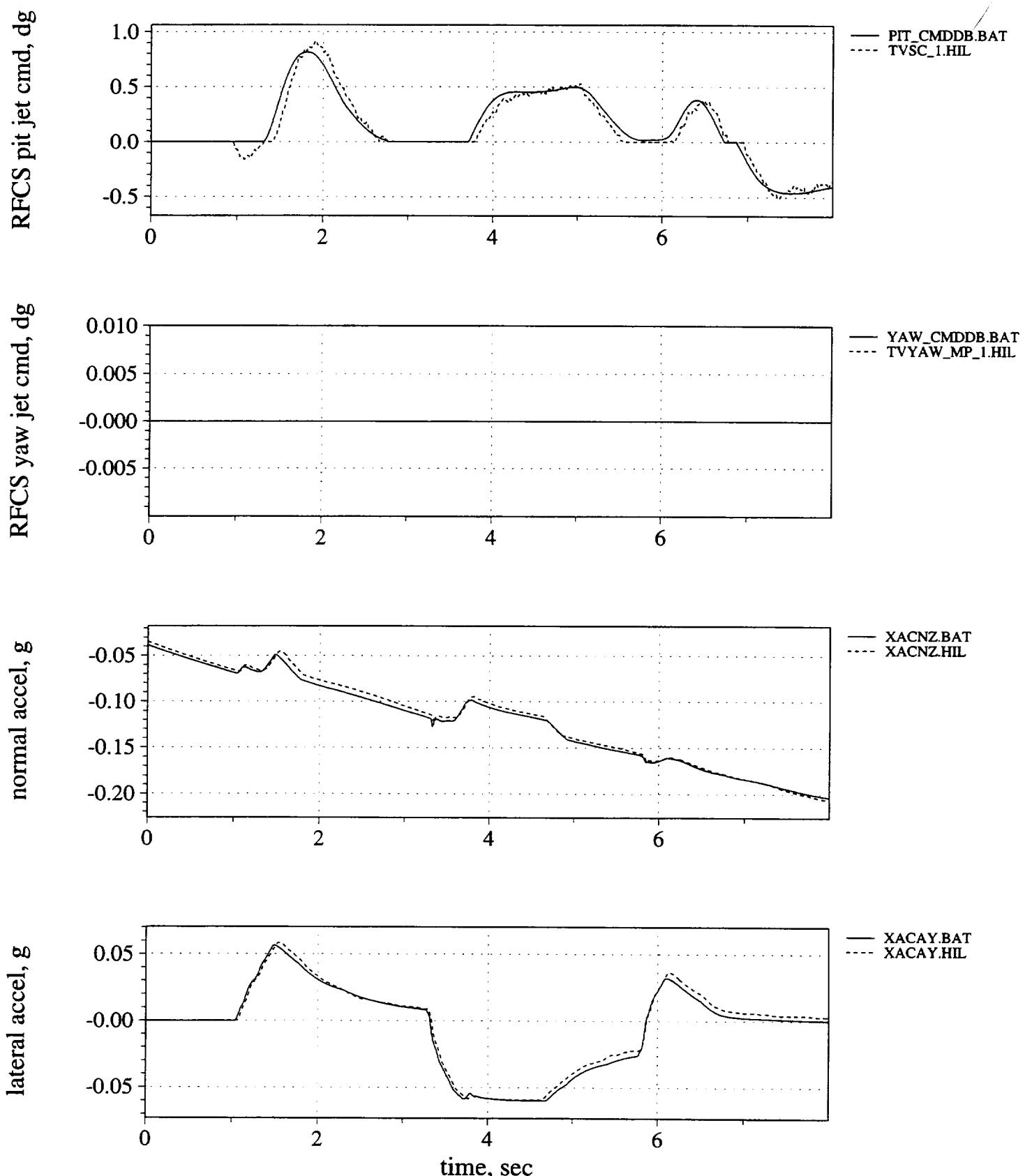


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 4/12

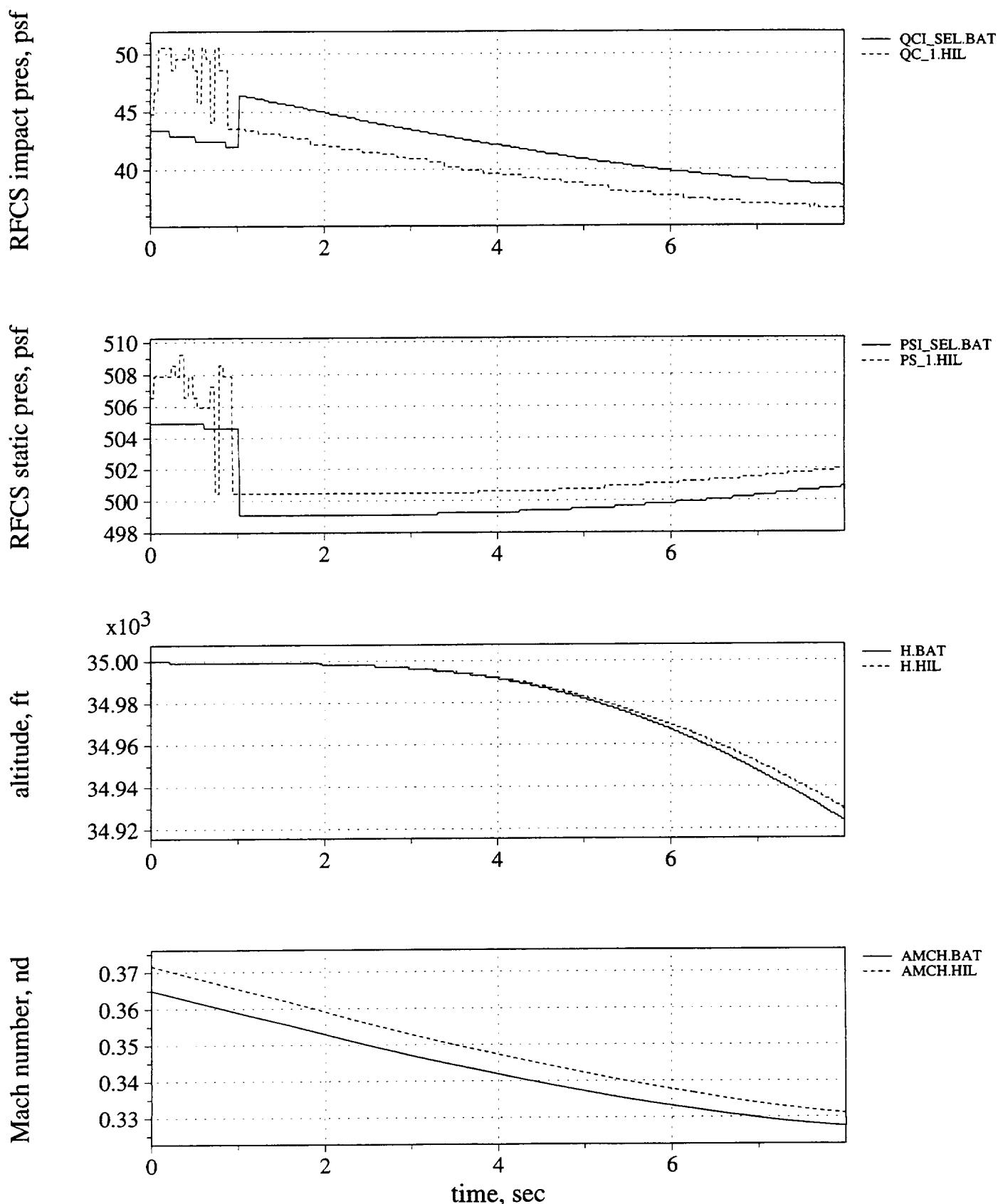


Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 5/12

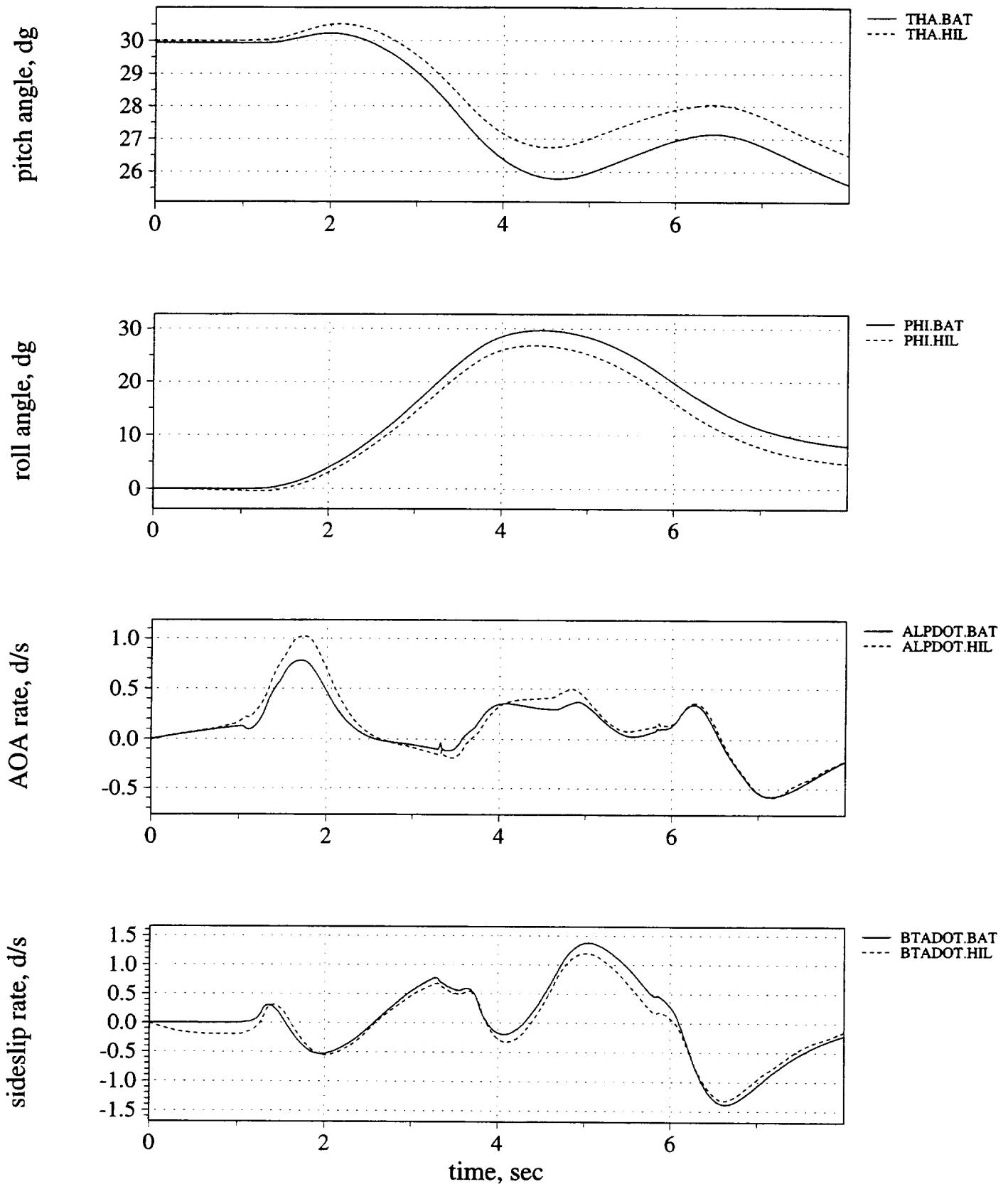


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 6/12

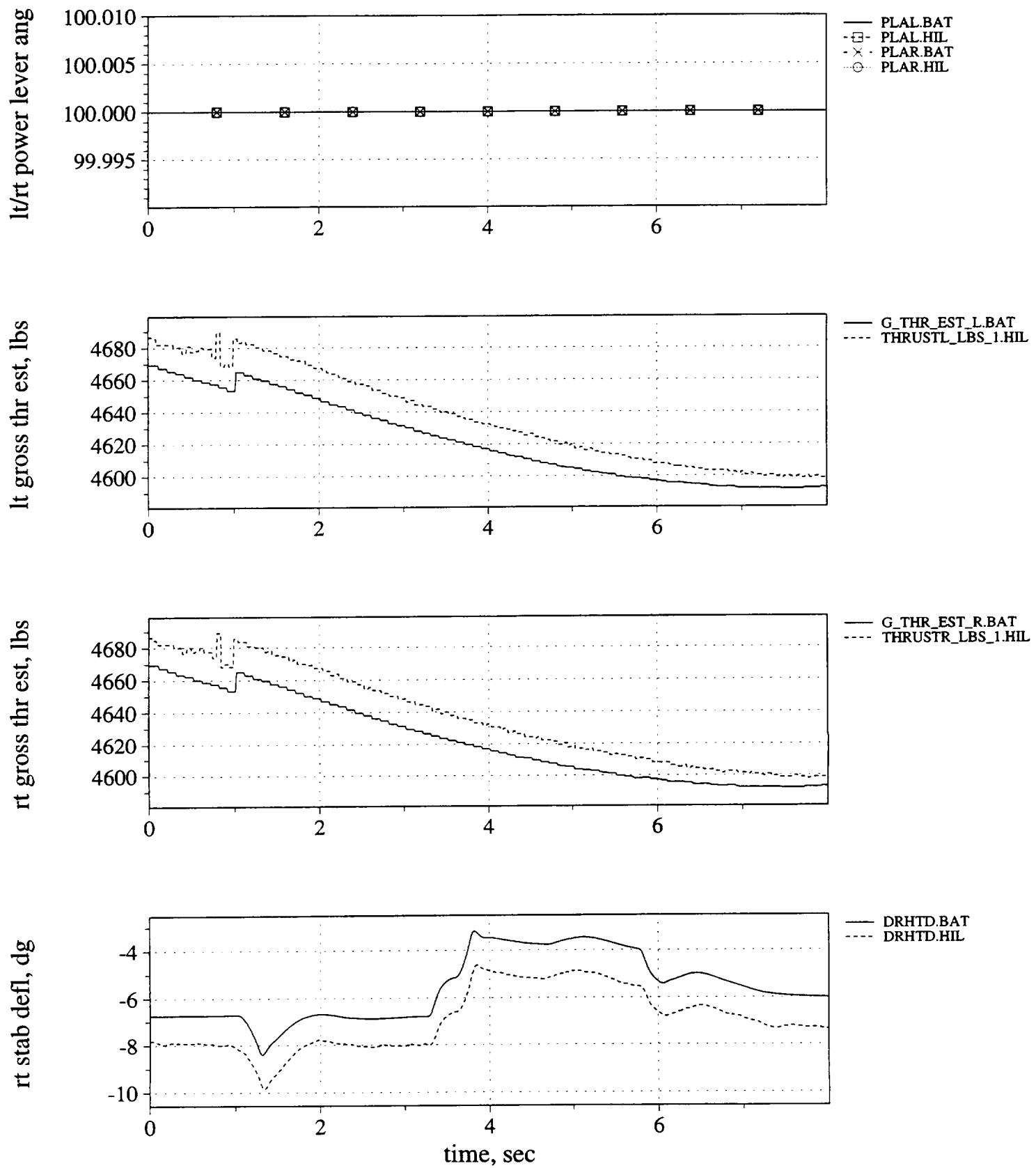


Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 7/12

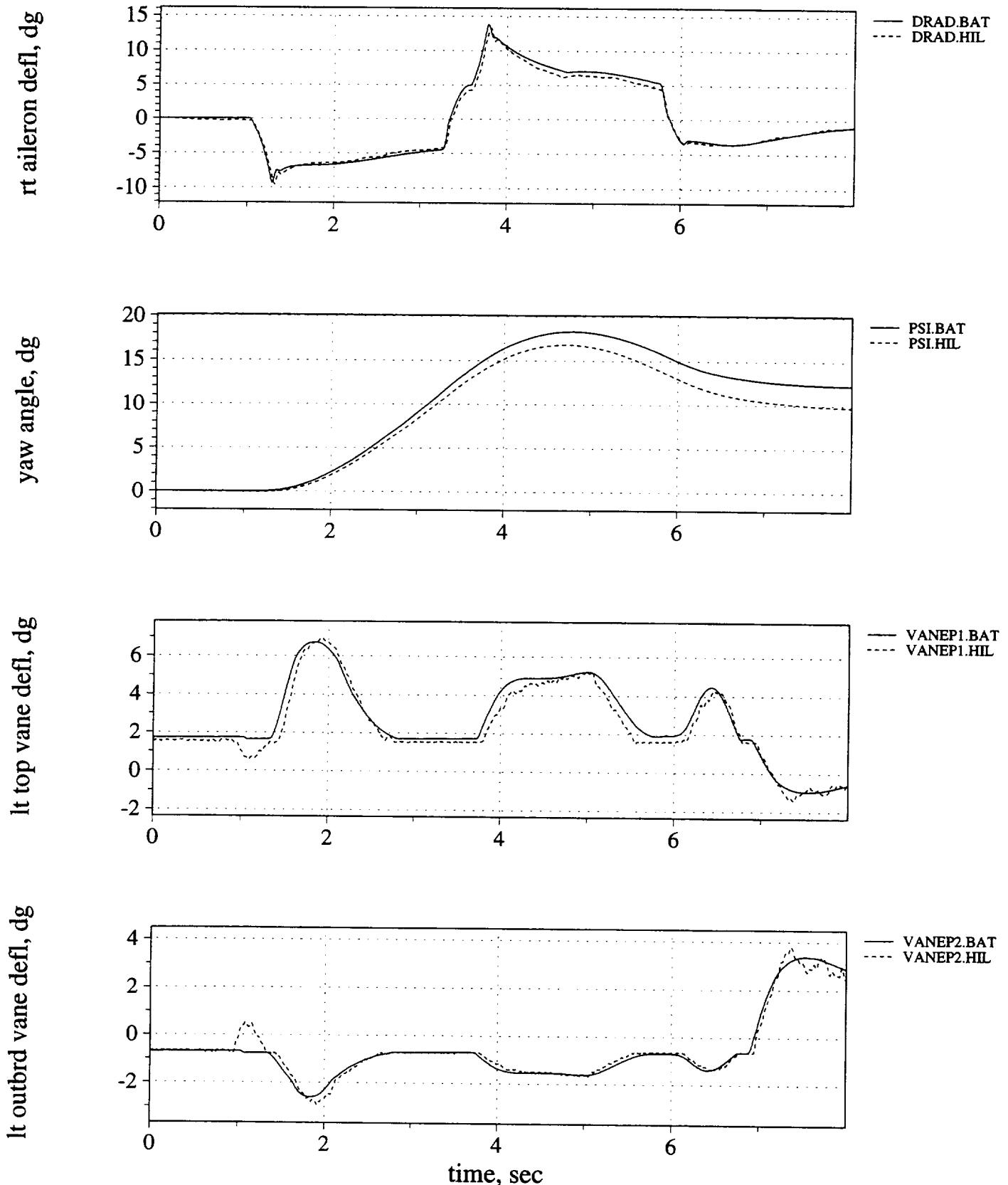


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 8/12

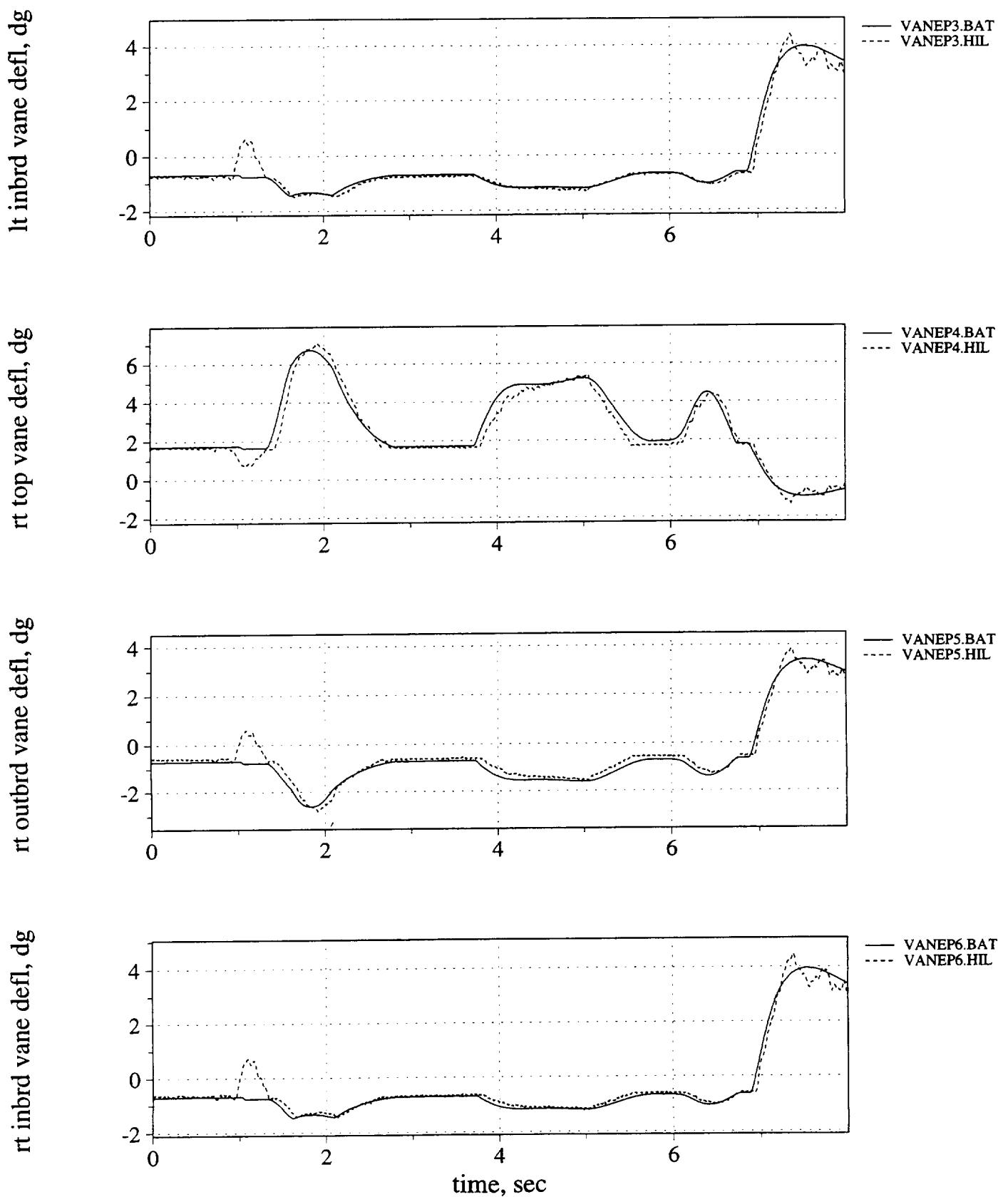


Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 9/12

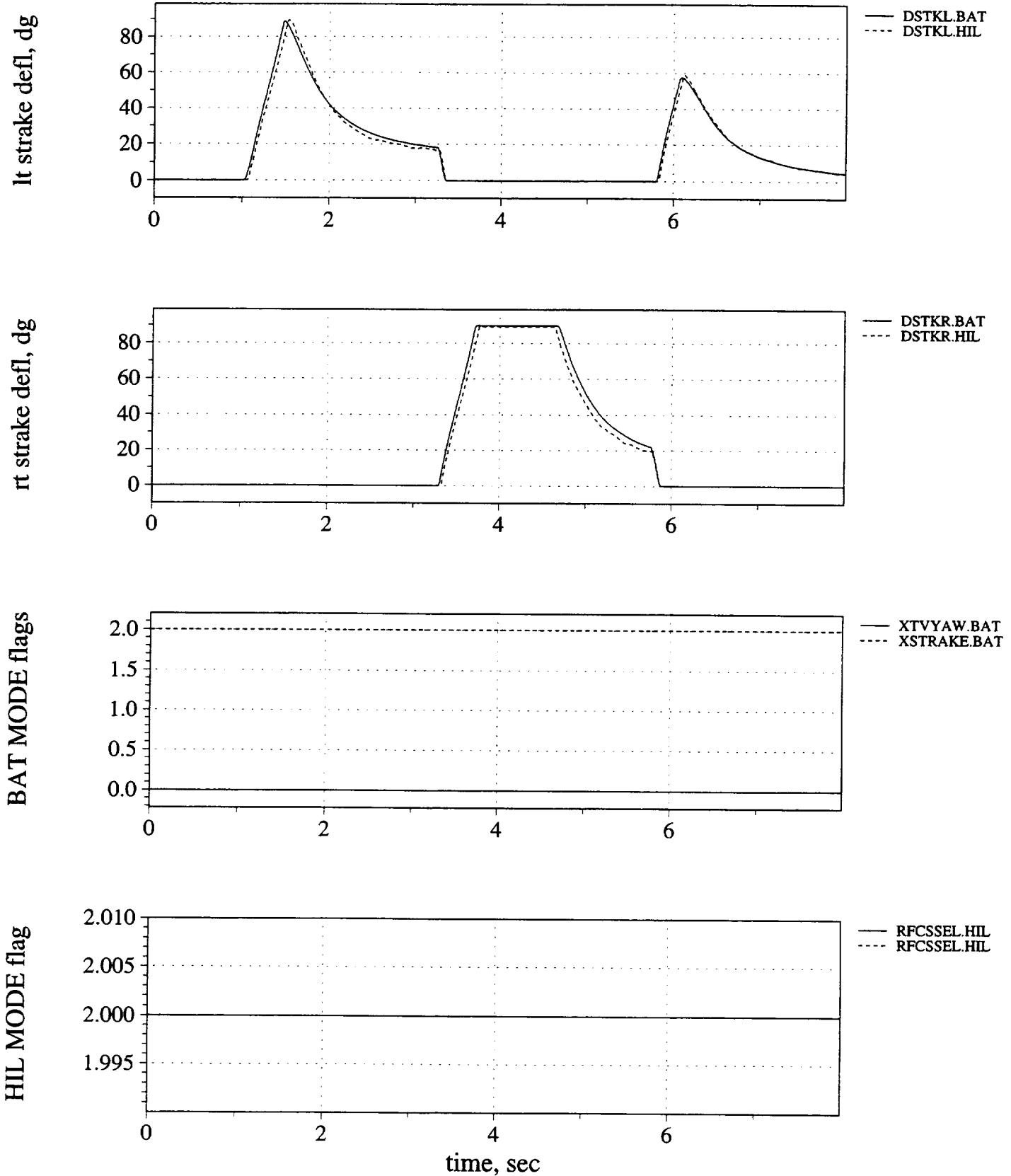


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 10/12

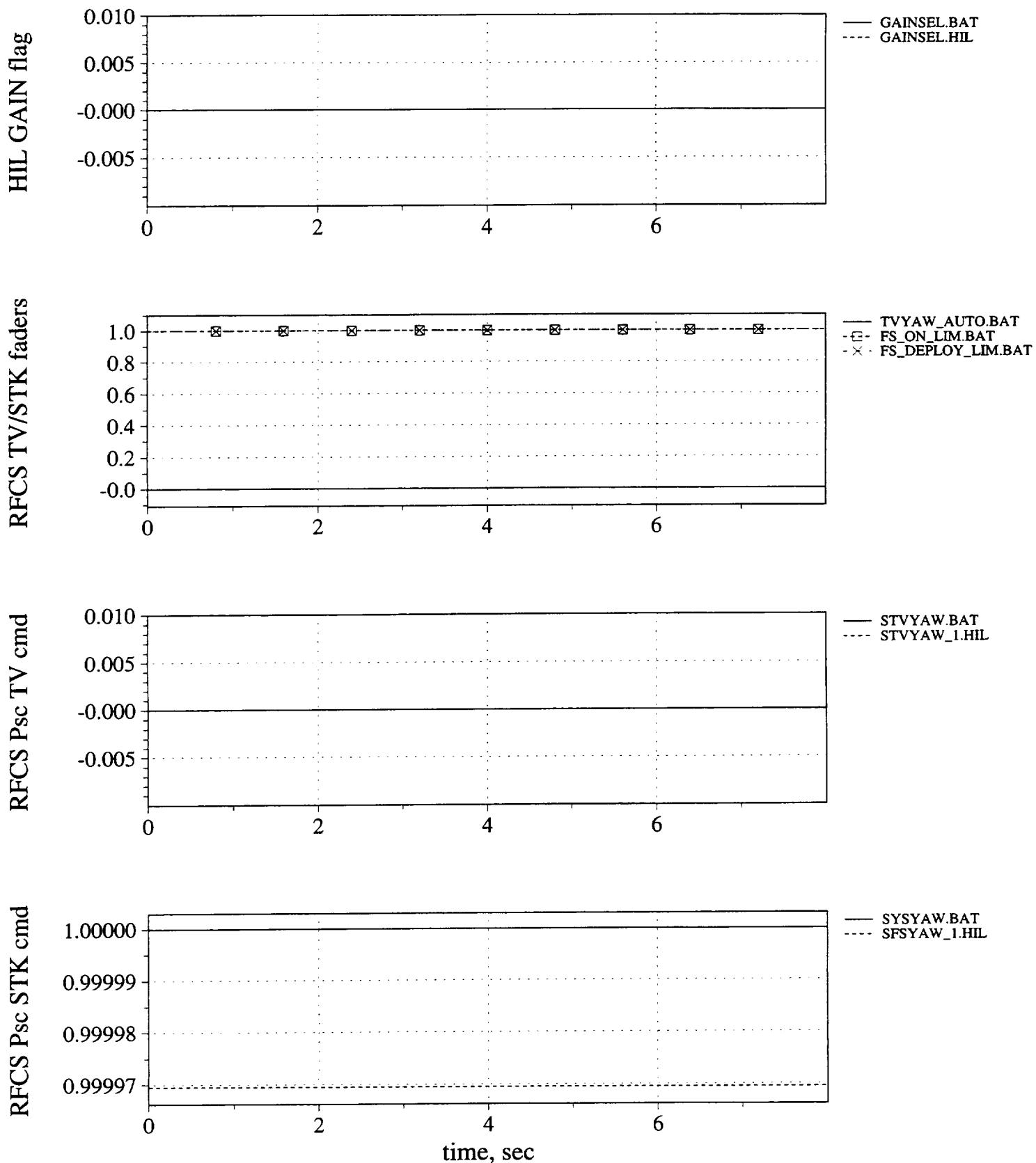


Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 11/12



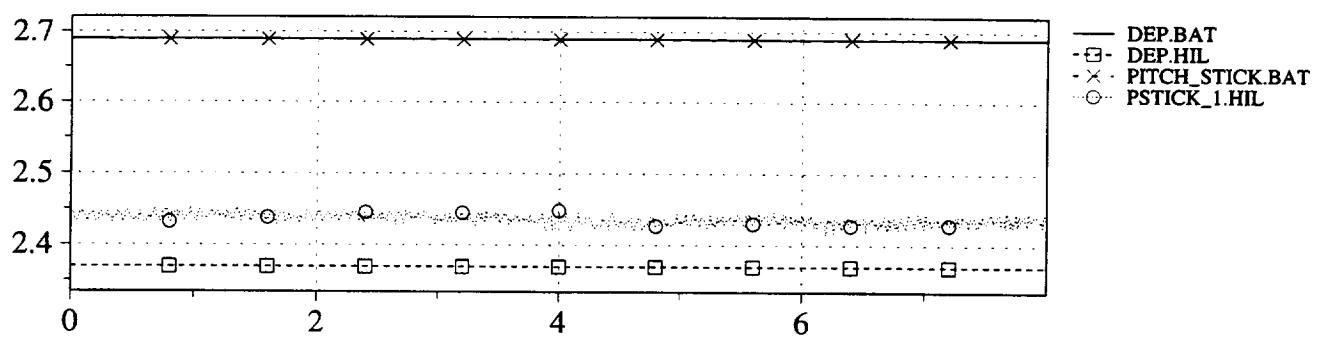
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_23_S_Mode_30alp-35k_Max_AB_2_in_ramp_dap page 12/12

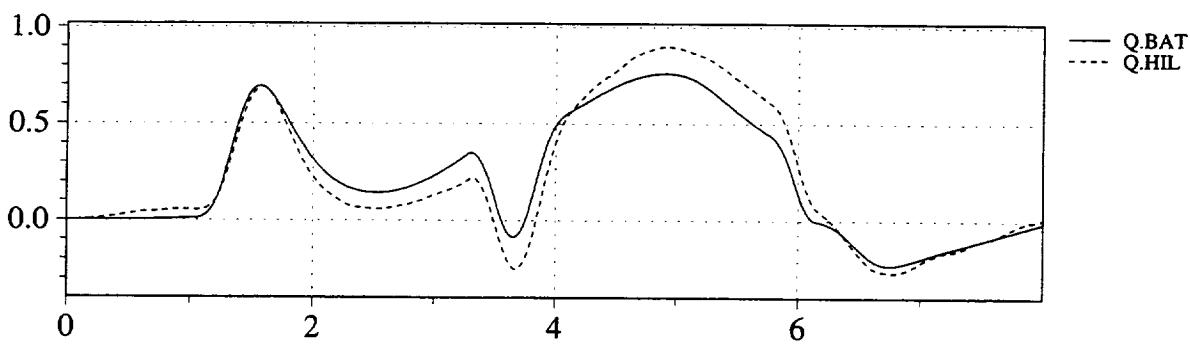


Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 1/12

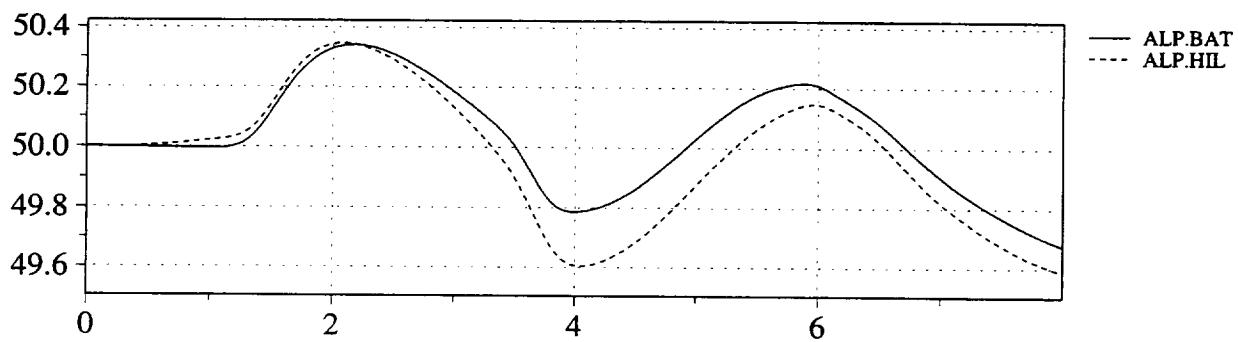
total pit stk cmd, in



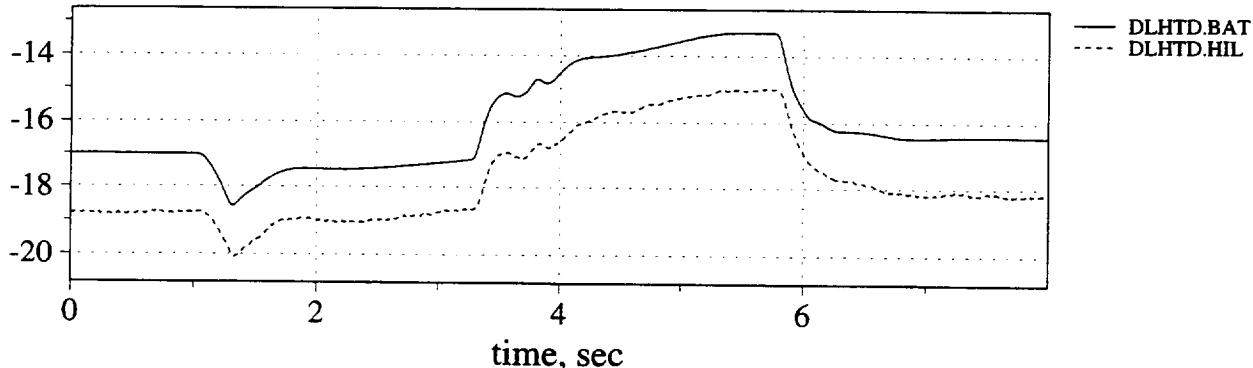
pitch rate, d/s



Angle-of-attack, dg

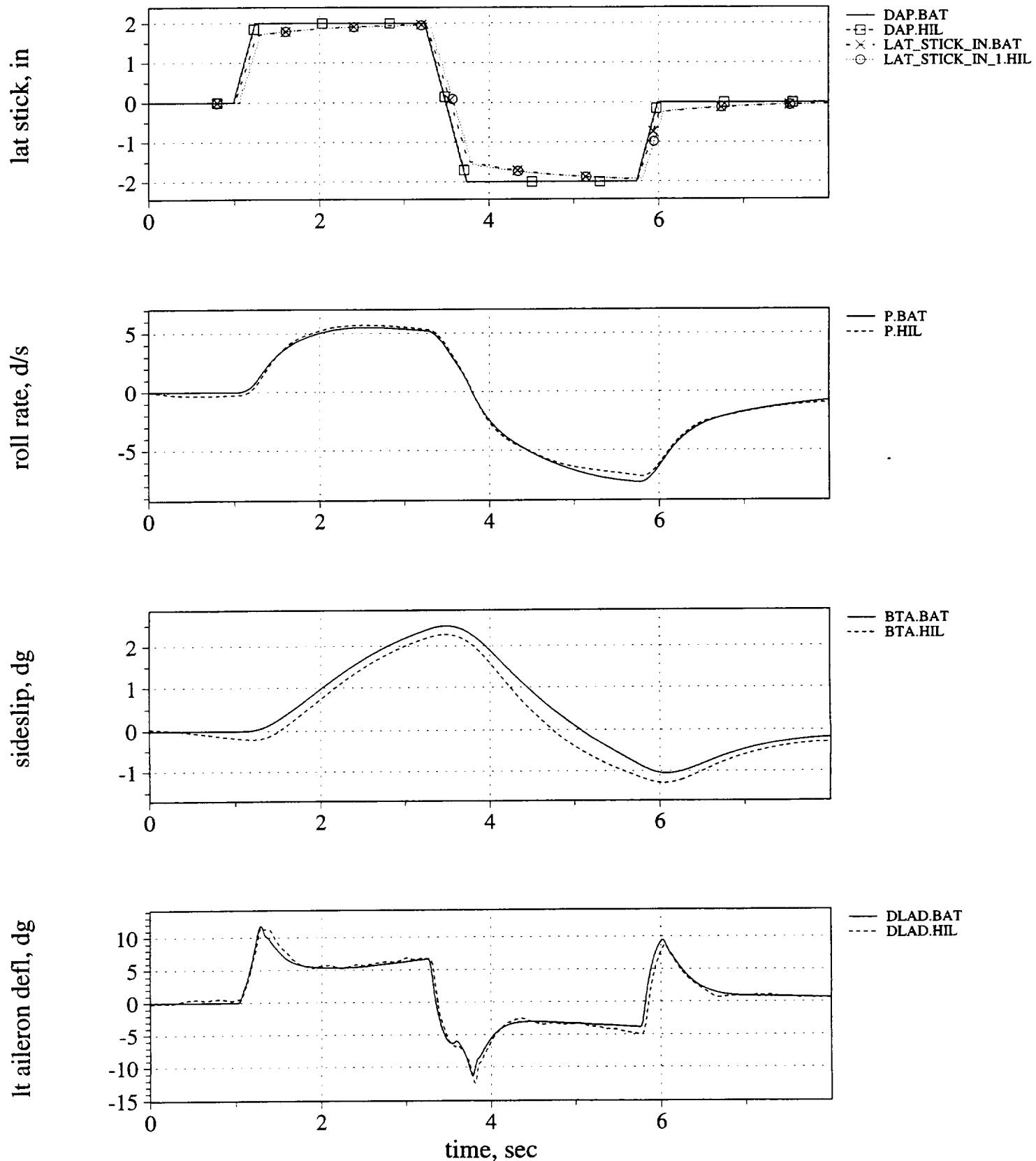


lt stab defl, dg

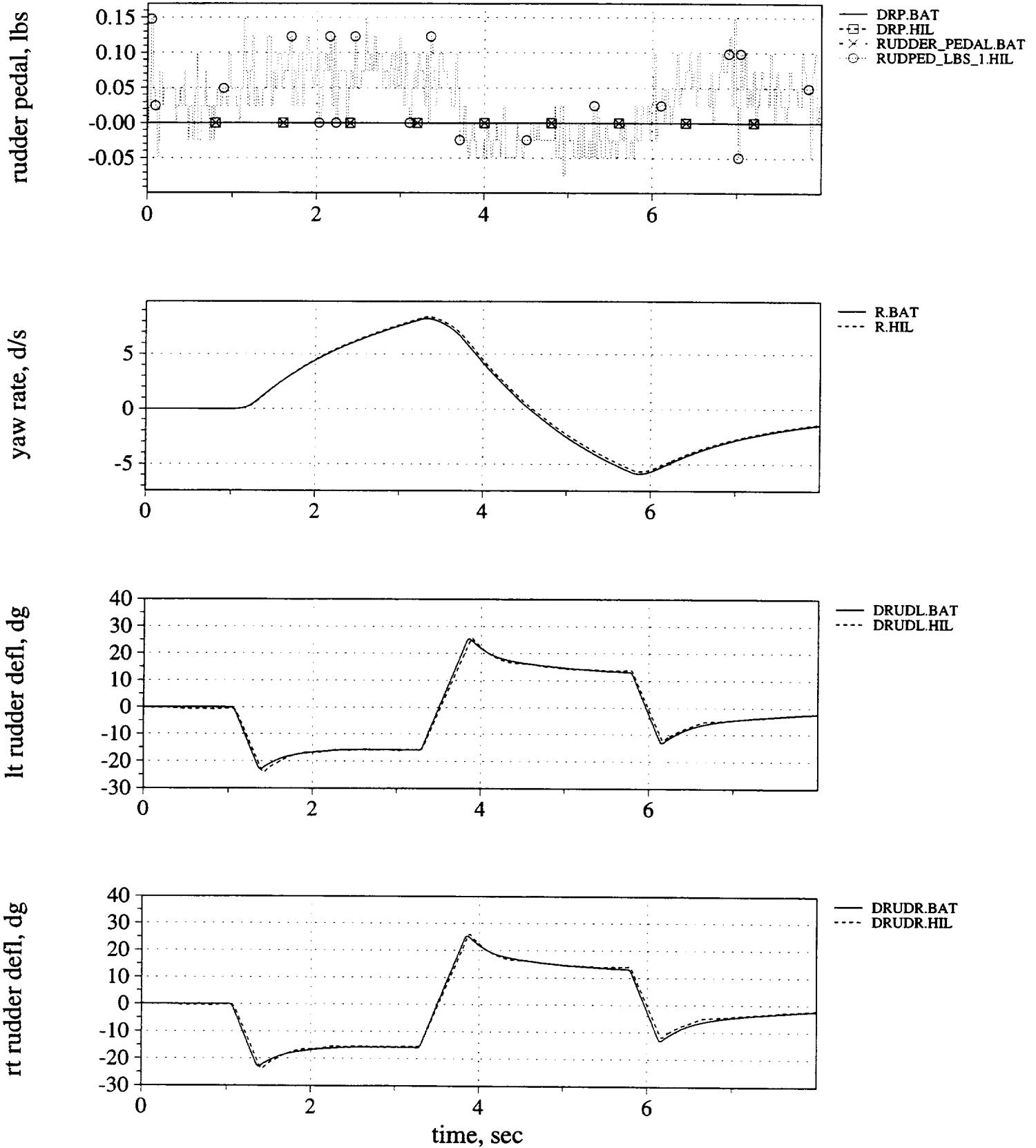


Performance Validation Checkcases

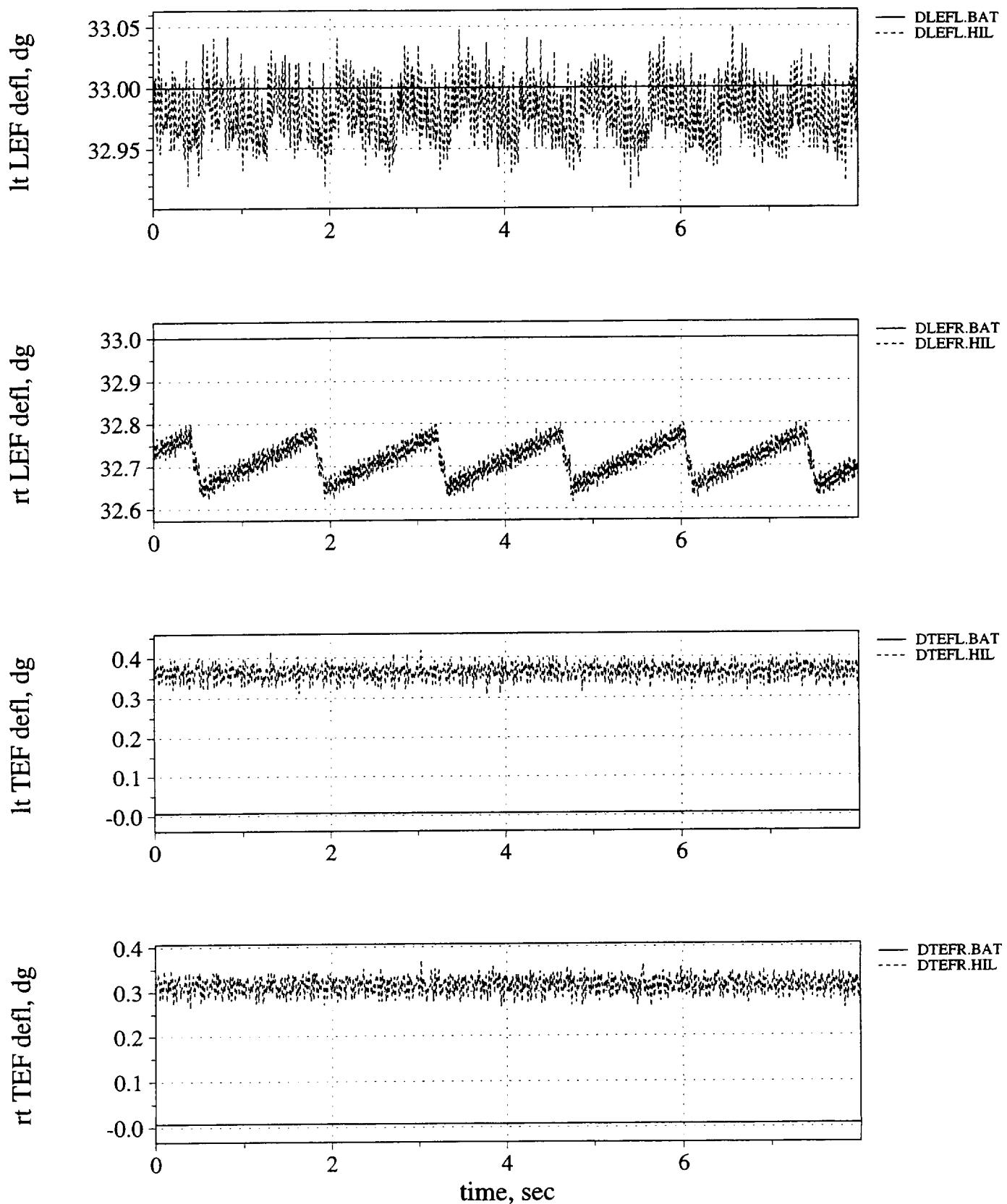
Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 2/12



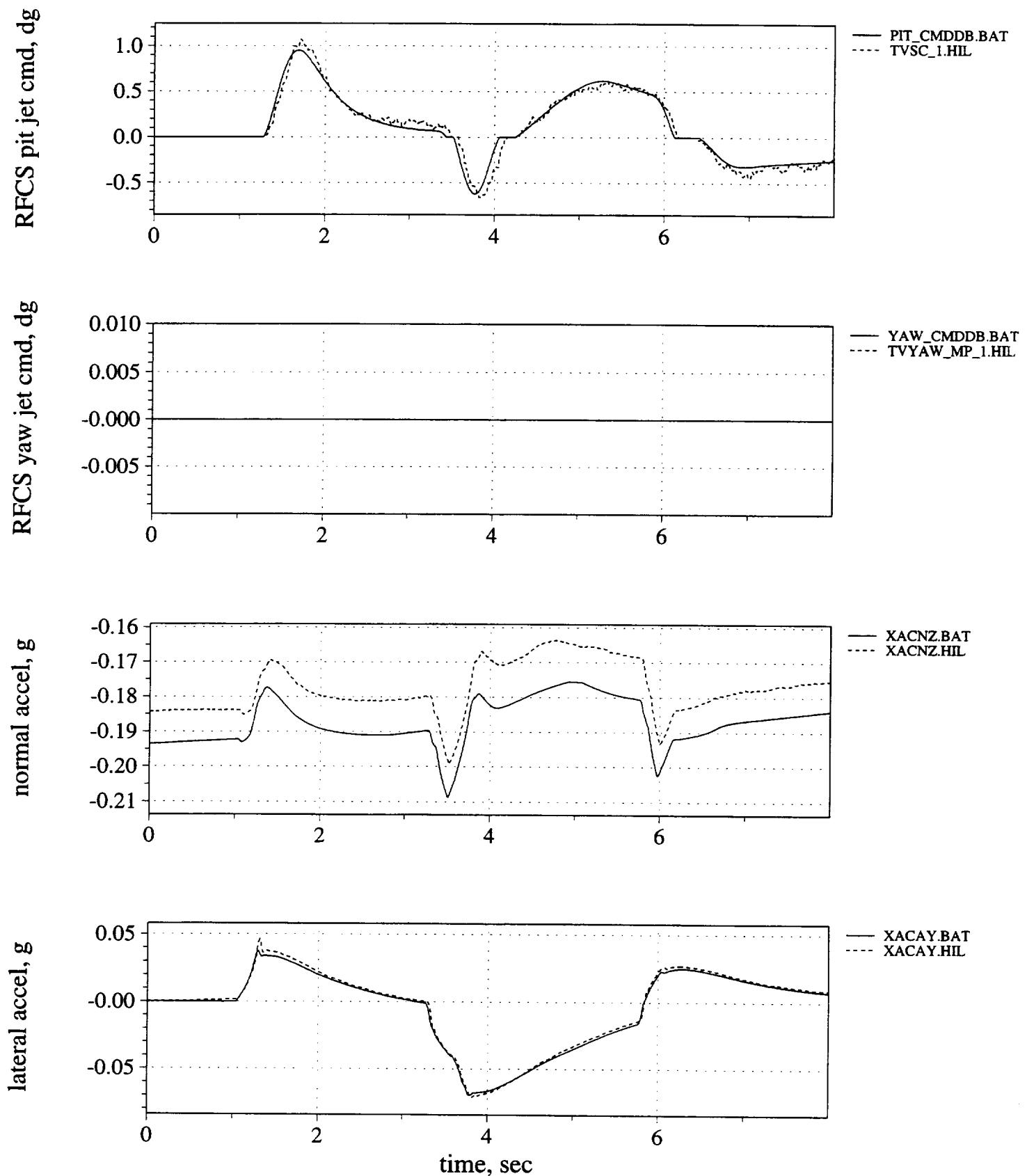
Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 3/12



Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 4/12

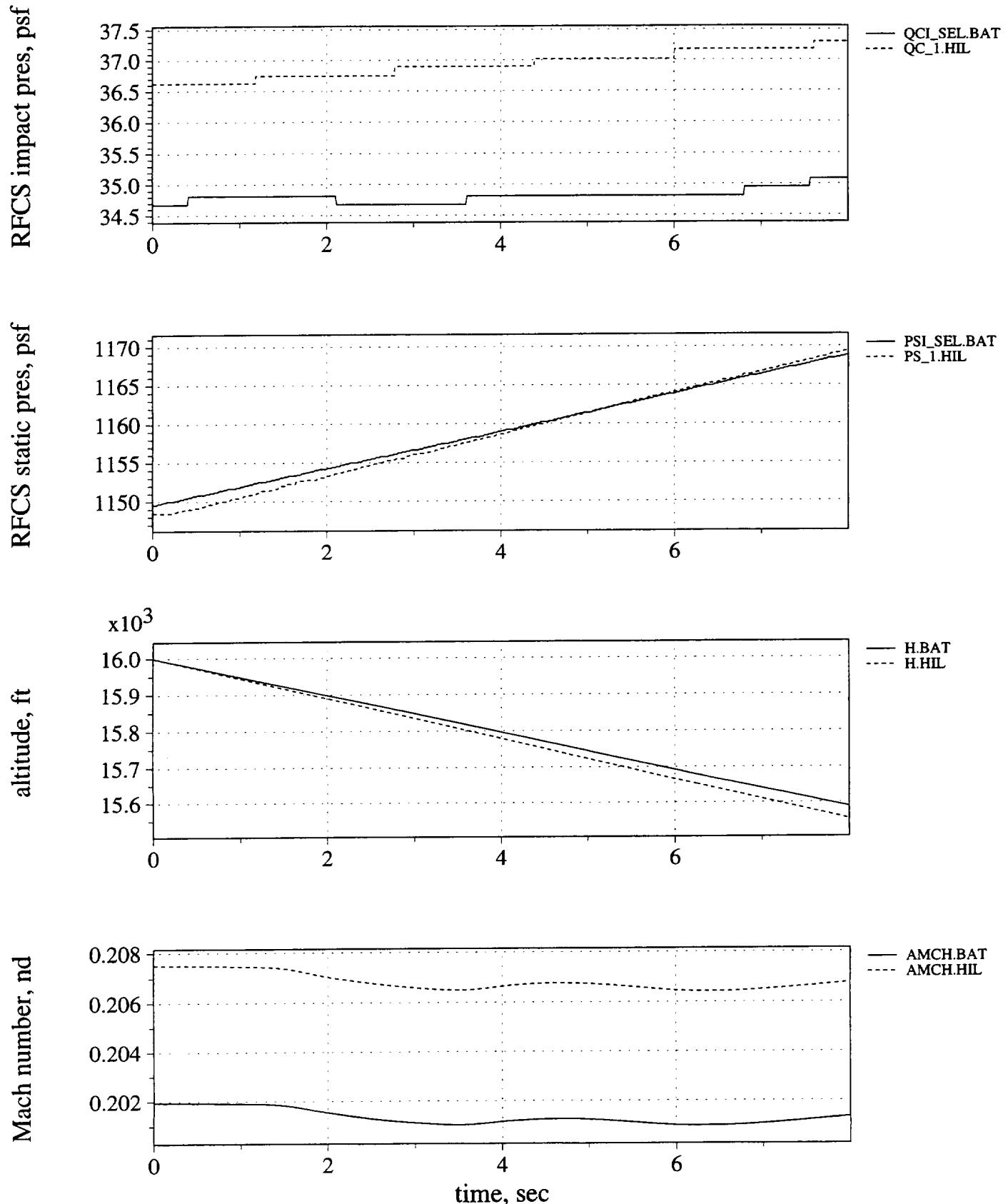


Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 5/12

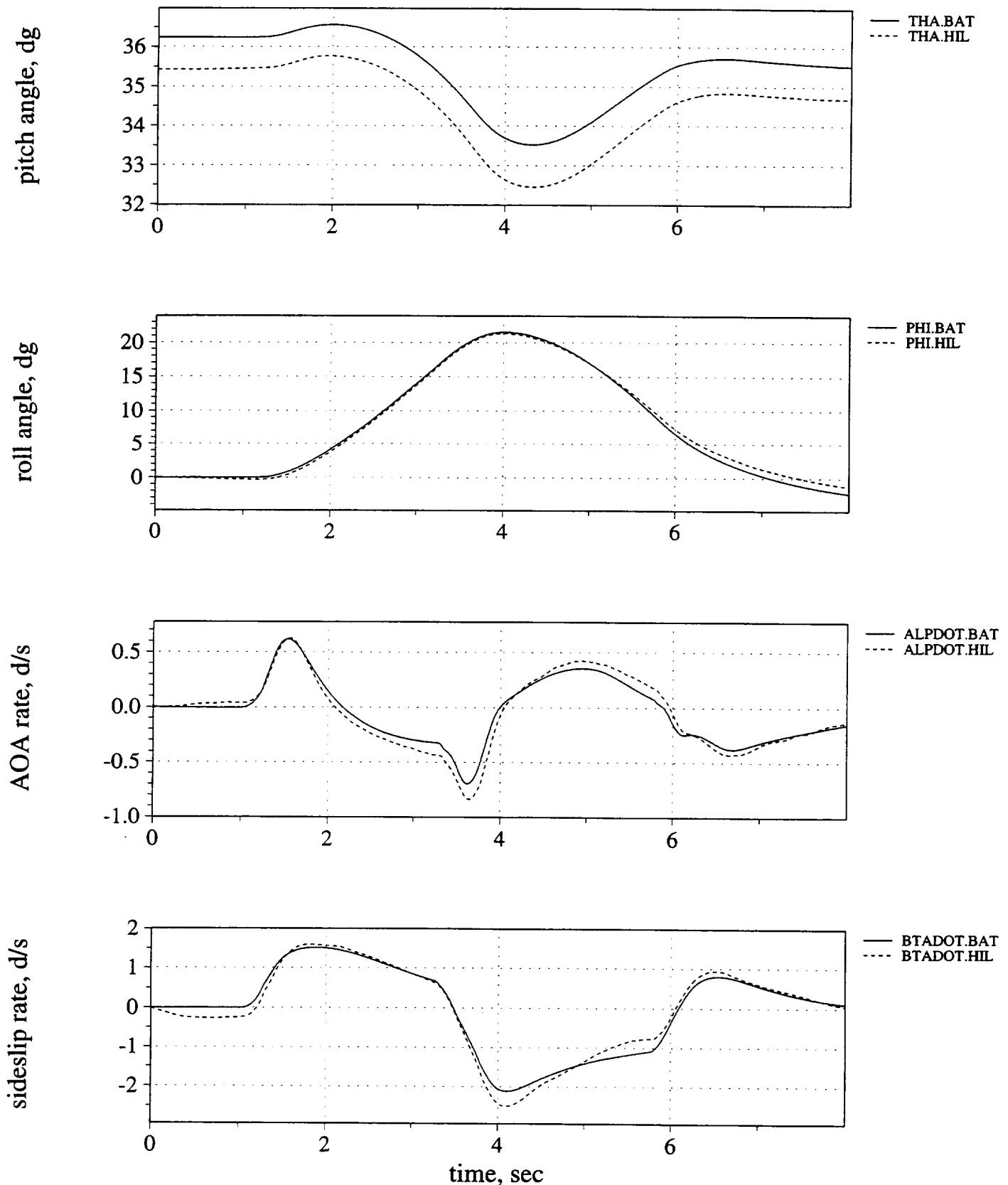


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 6/12

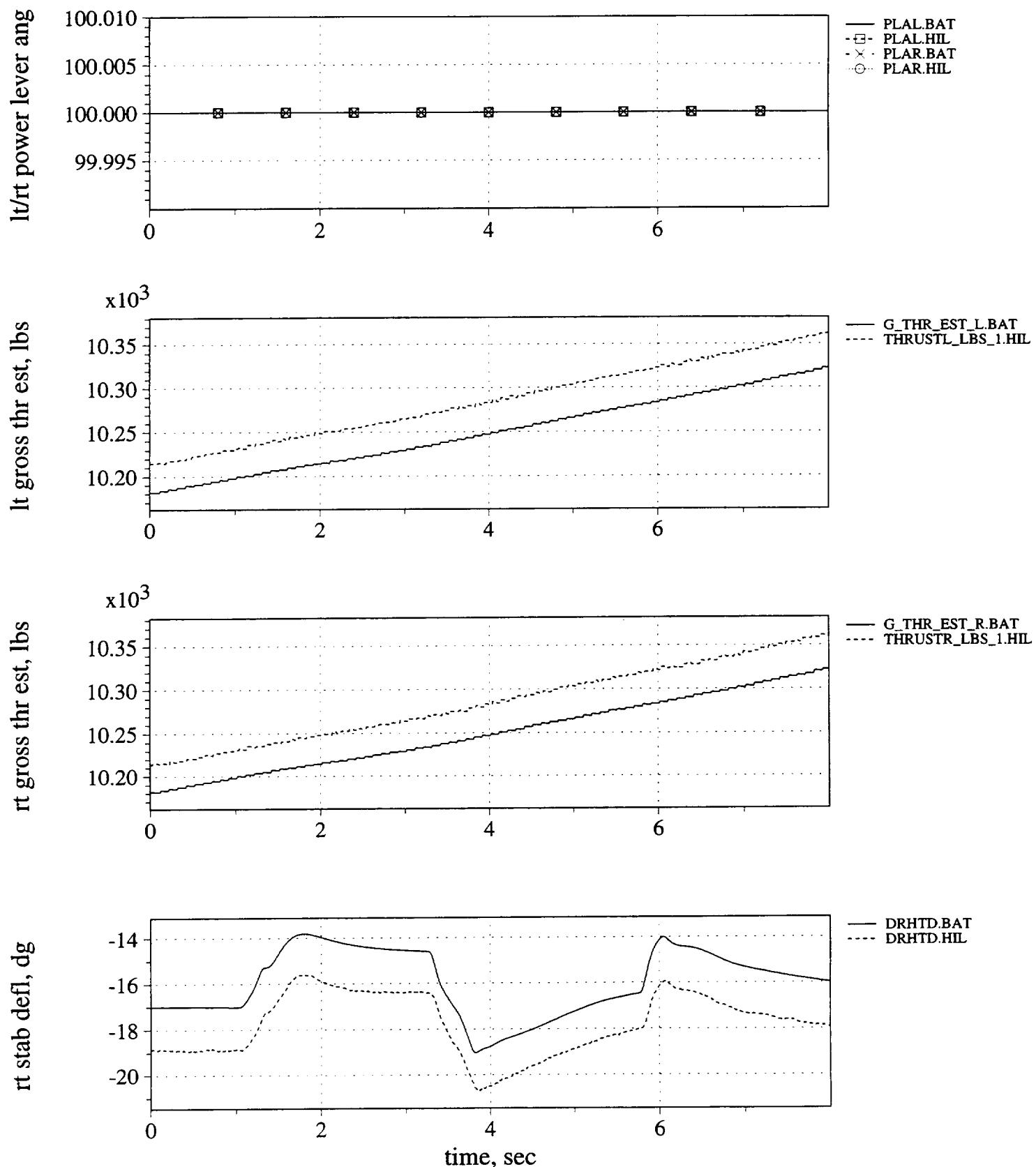


Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 7/12

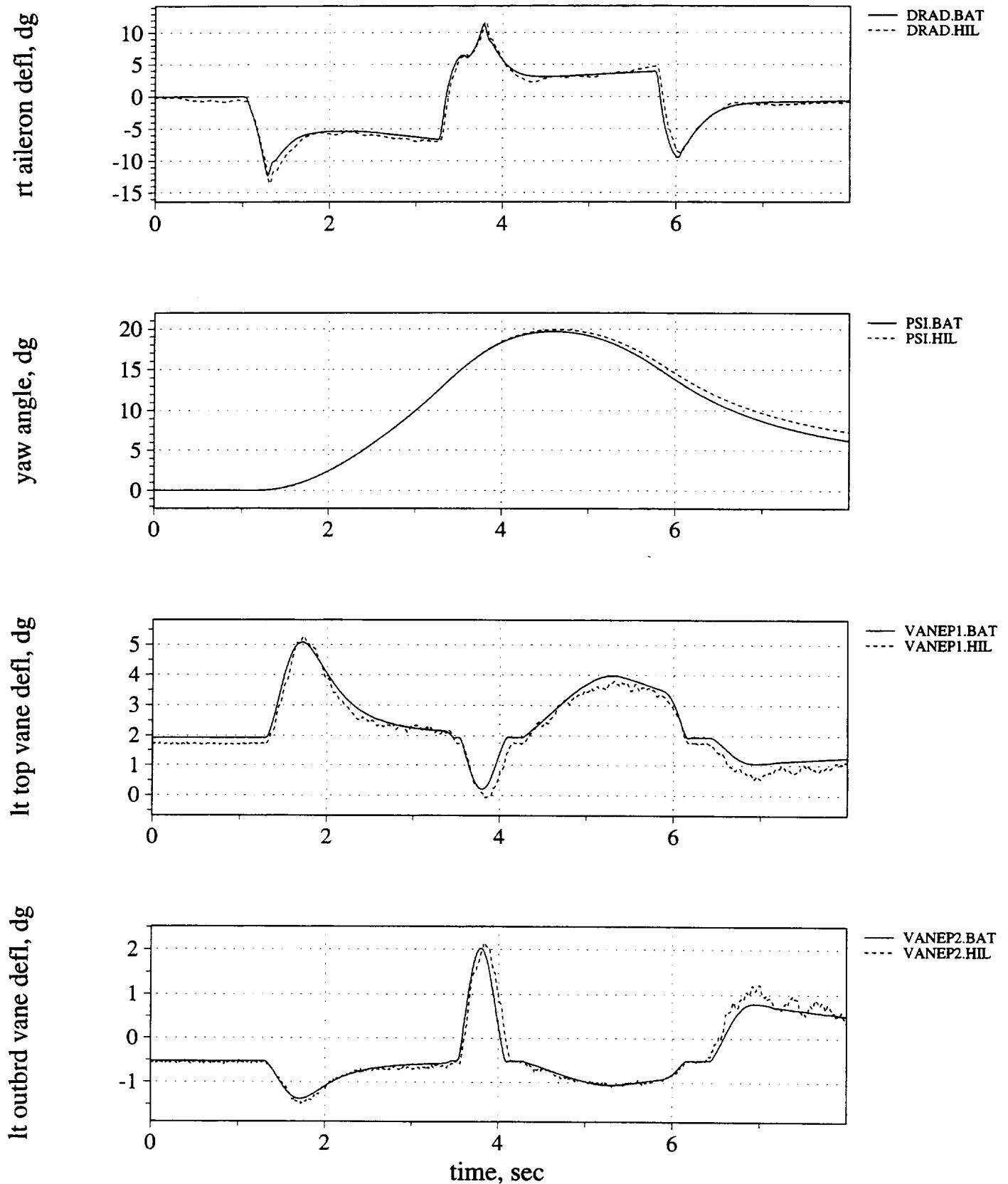


Performance Validation Checkcases

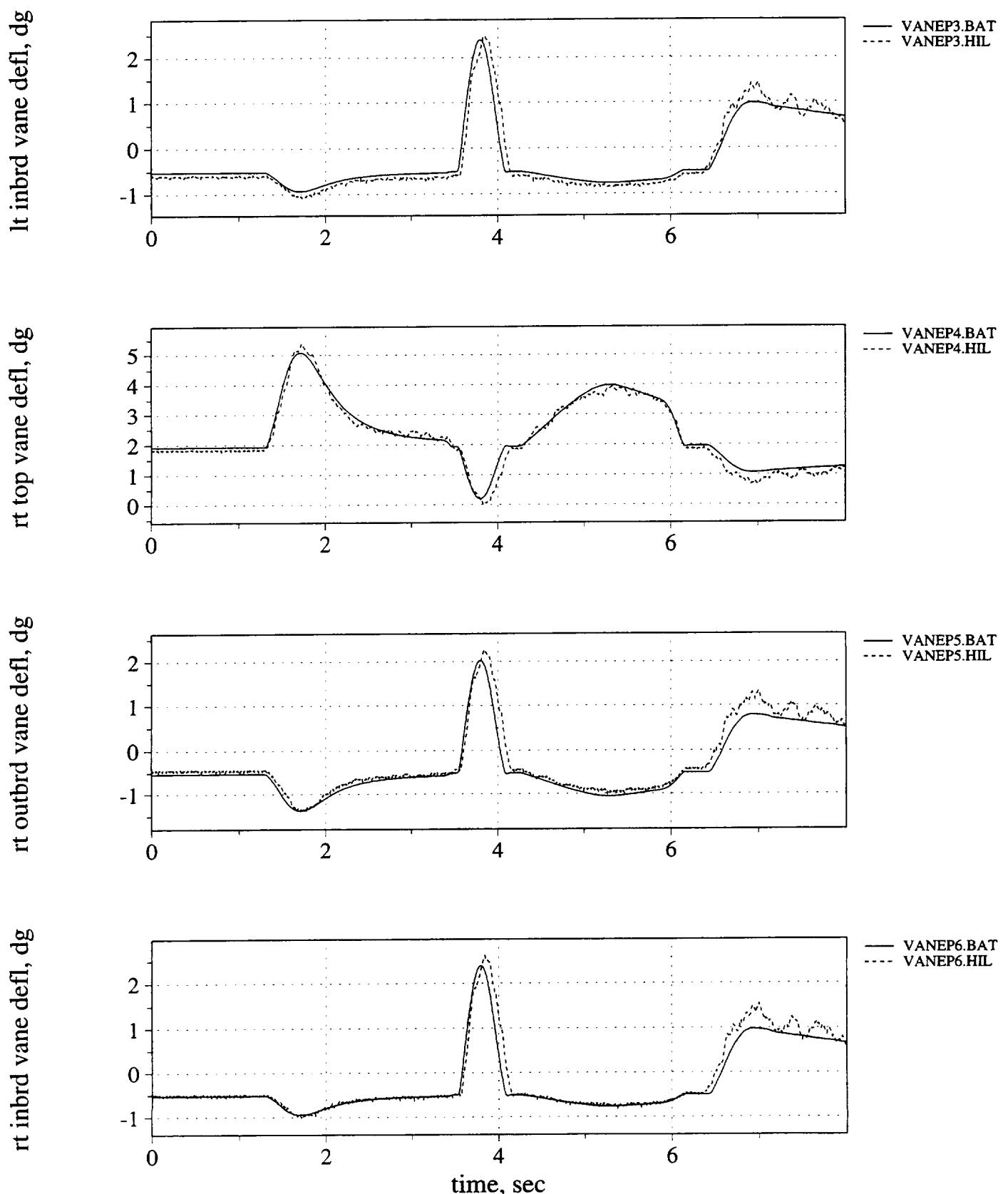
Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 8/12



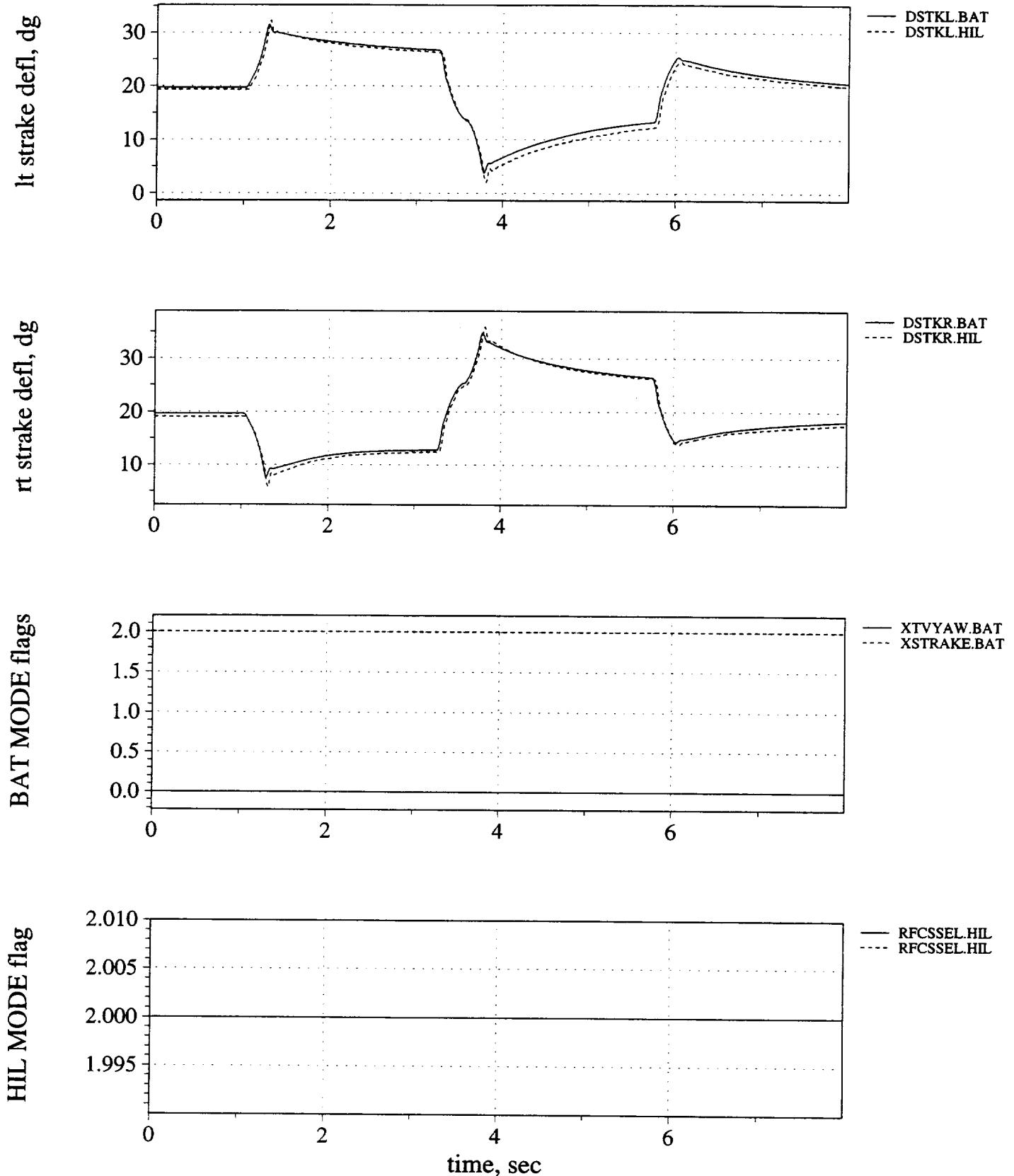
Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 9/12



Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 10/12

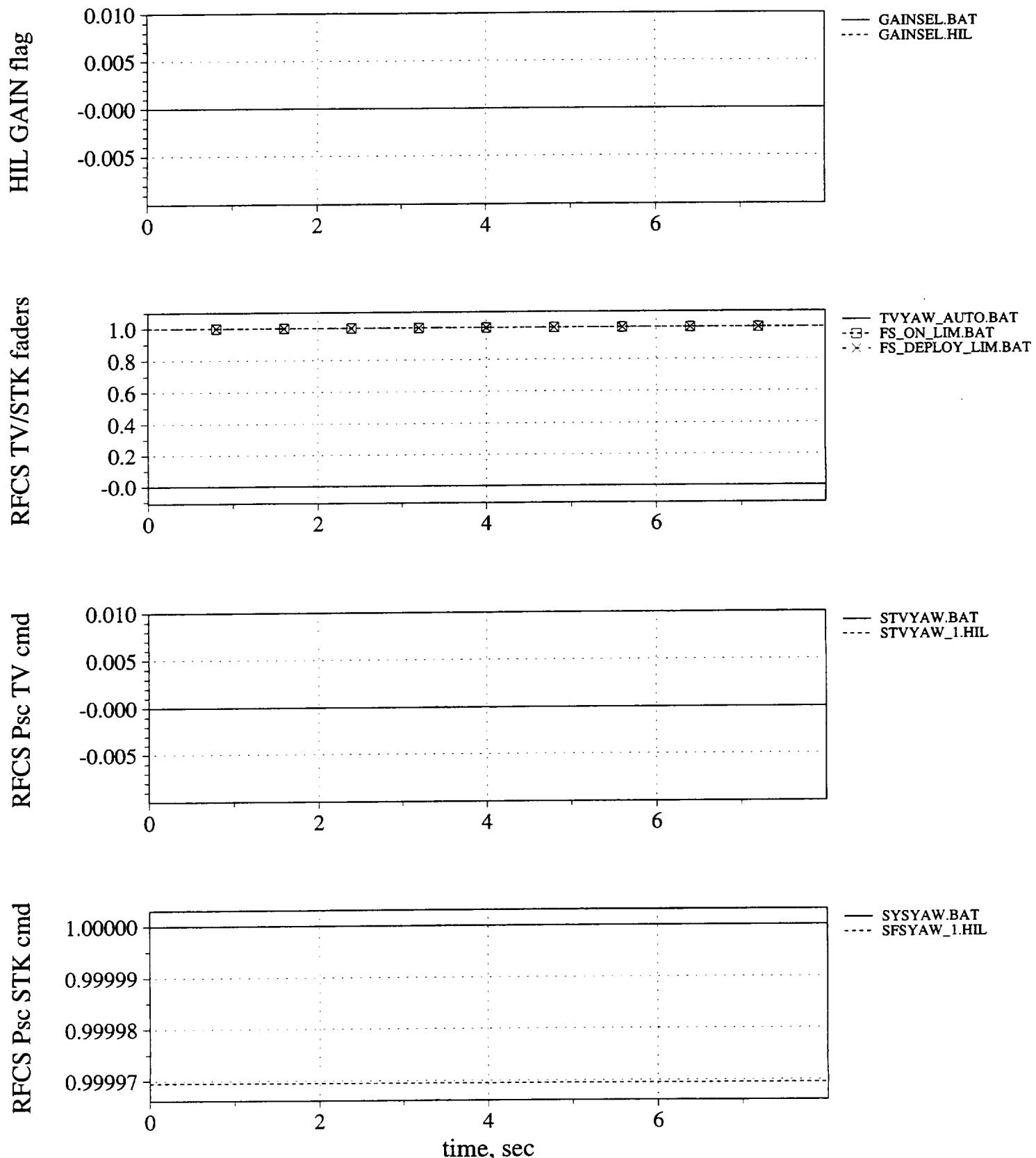


Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 11/12



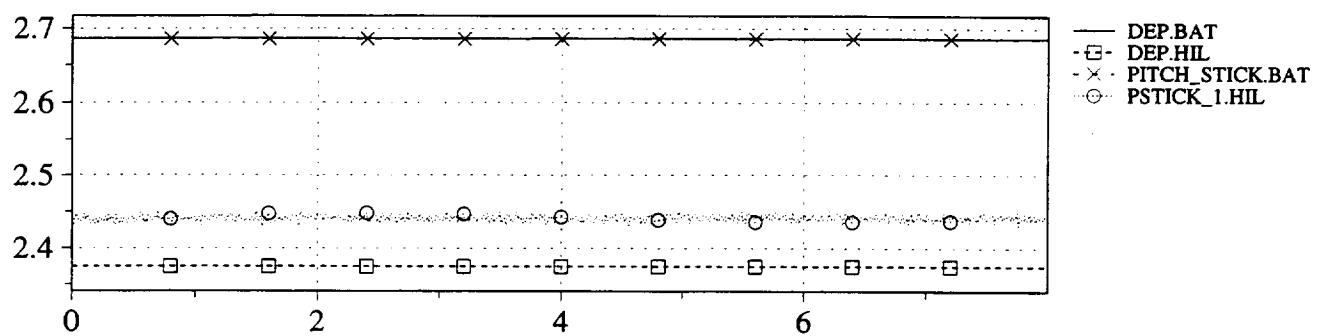
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_26_S_Mode_50alp-16k_Max_AB_2_in_ramp_dap page 12/12

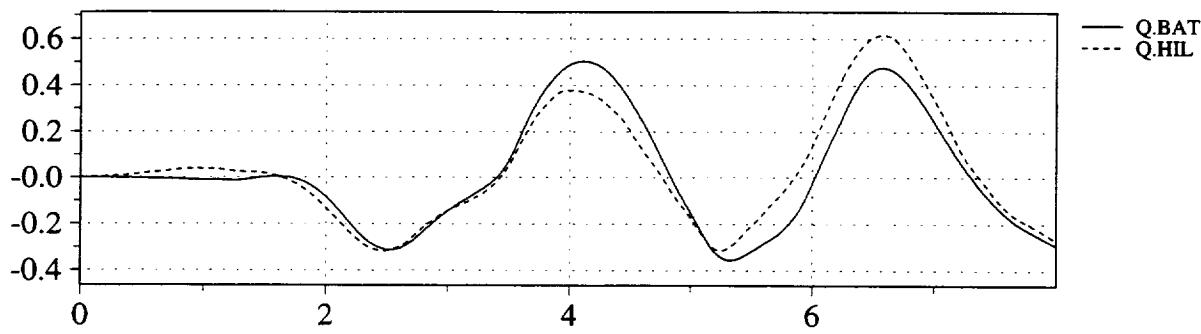


Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 1/12

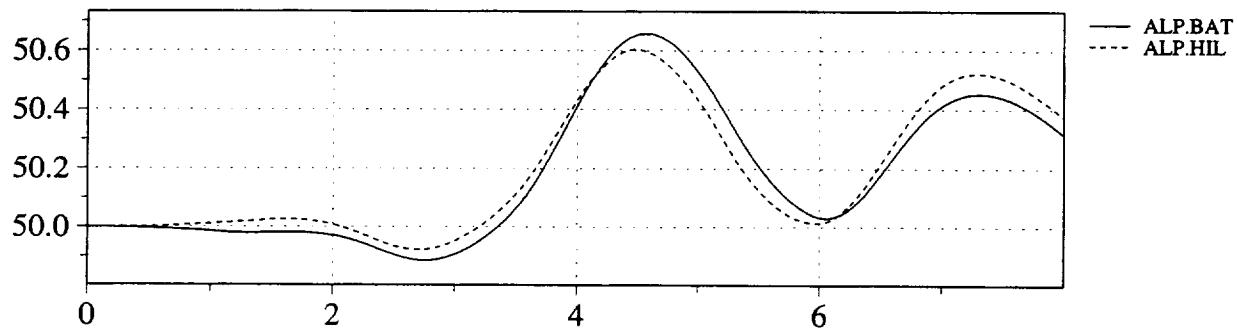
total pit stk cmd, in



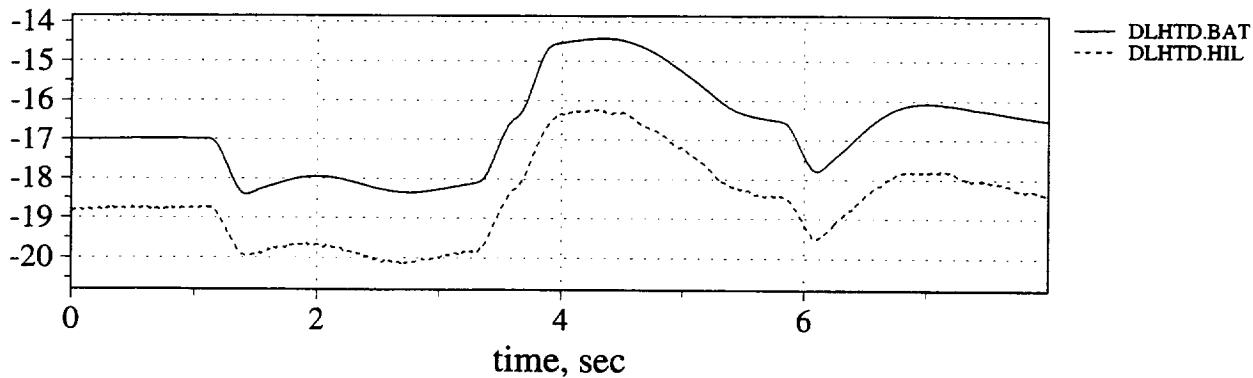
pitch rate, d/s



Angle-of-attack, dg

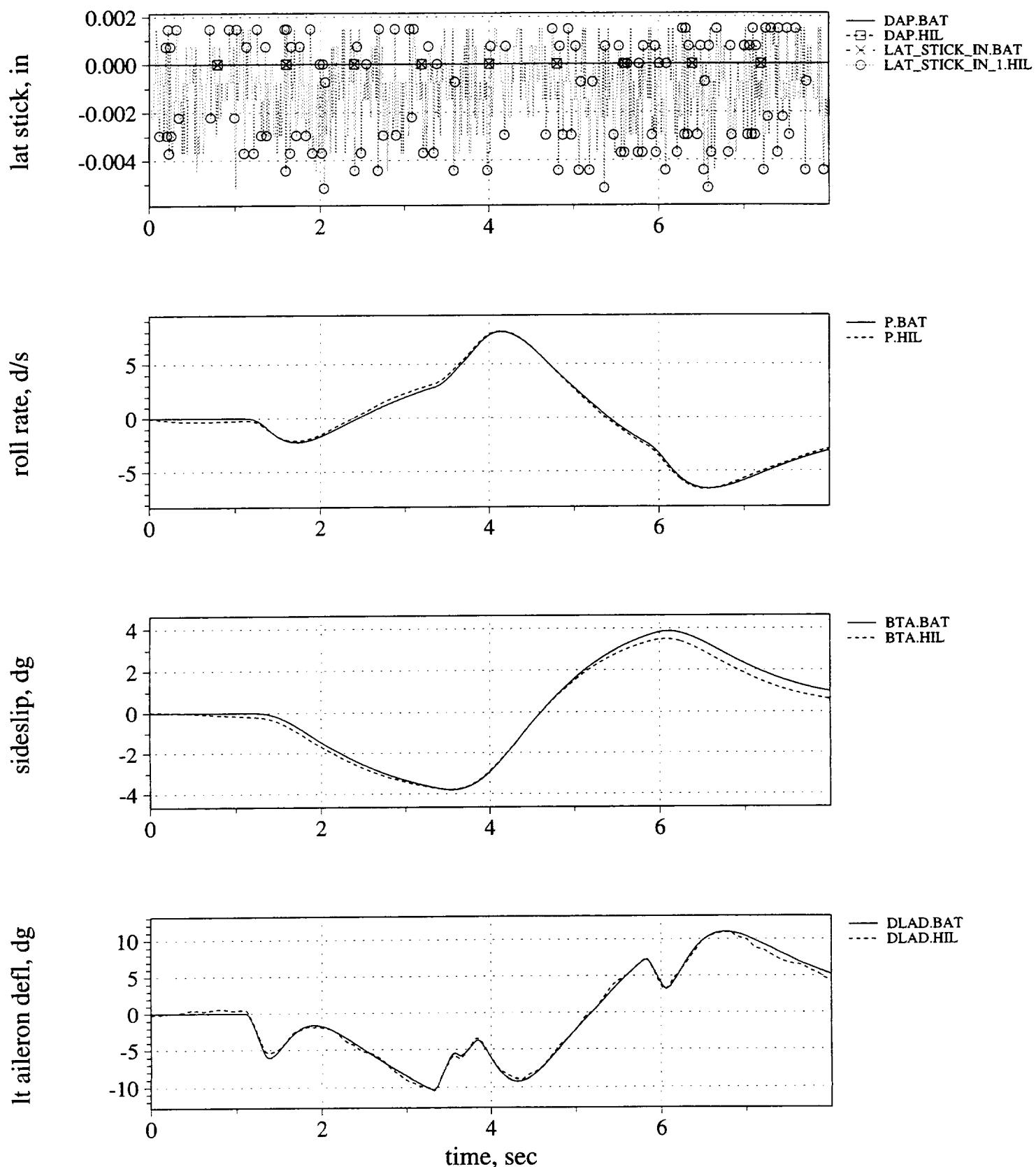


lt stab defl, dg



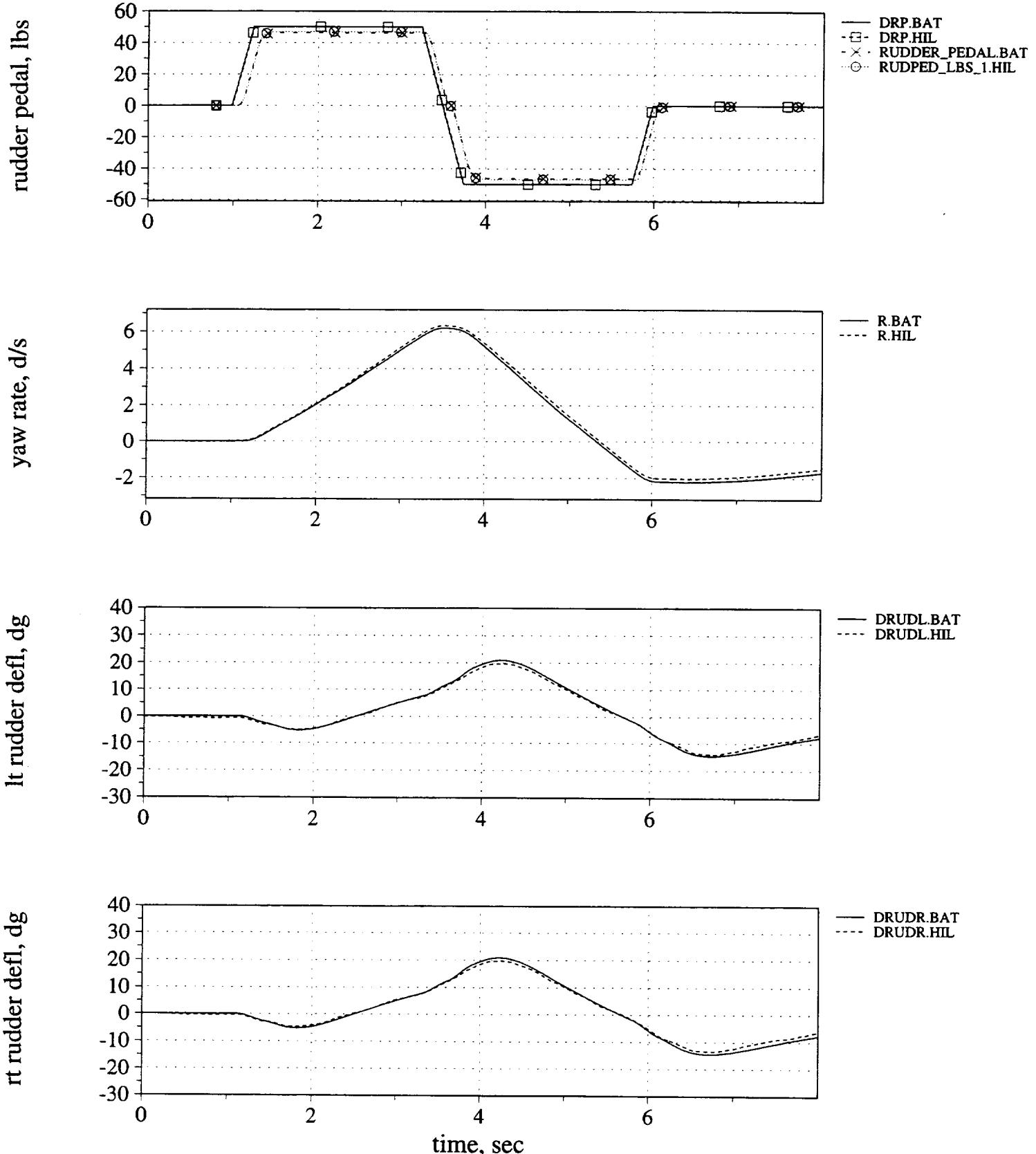
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 2/12



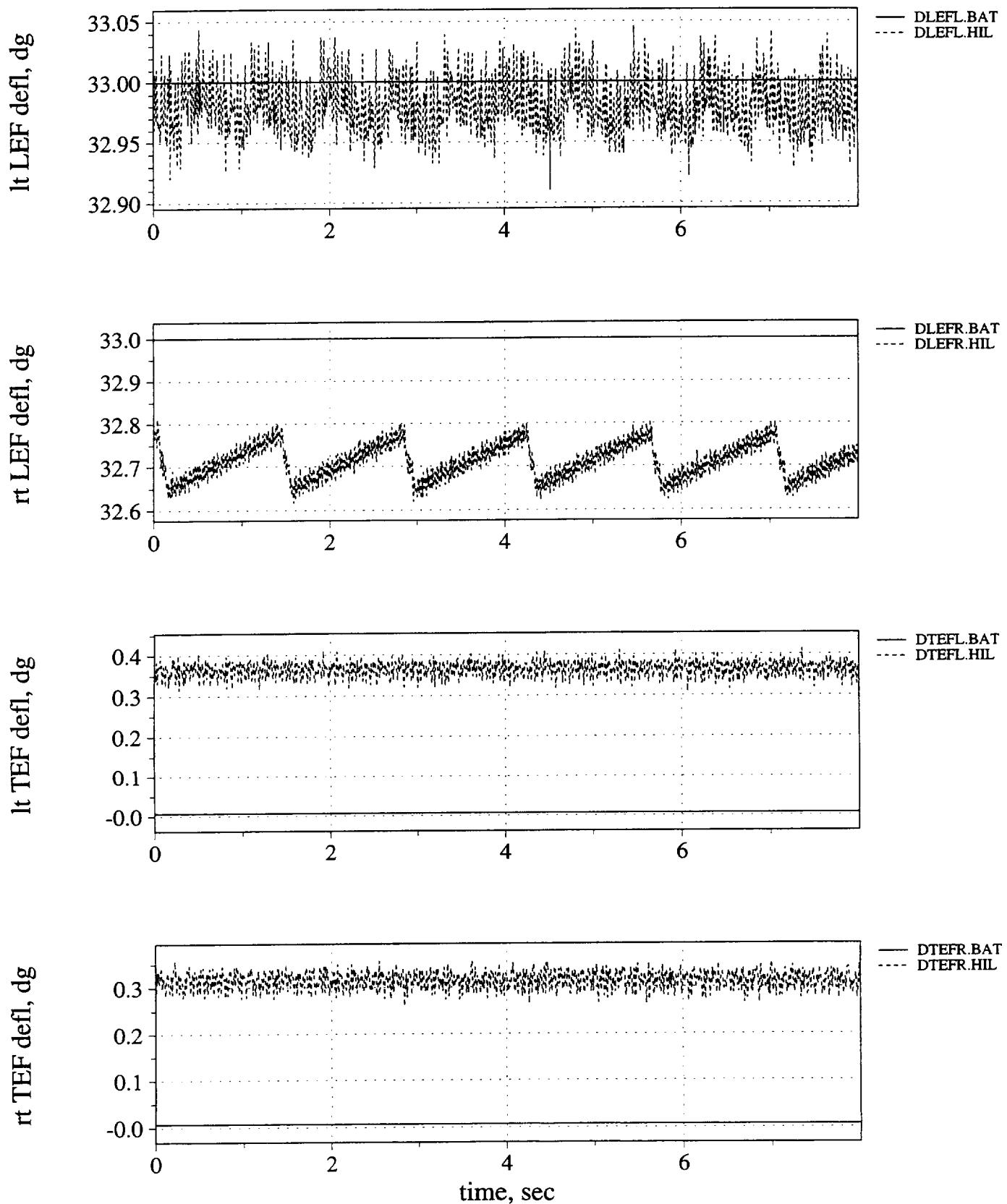
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 3/12

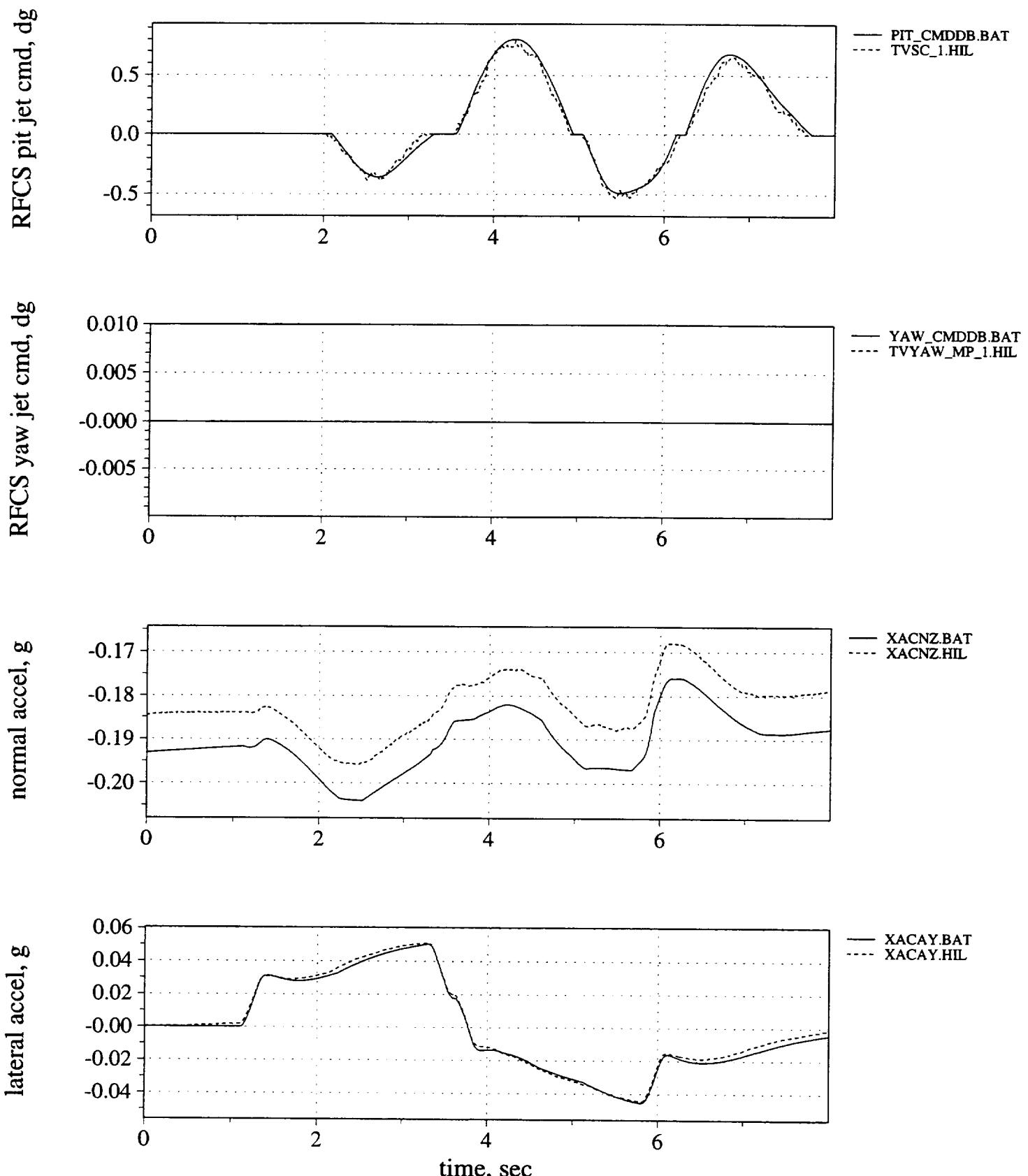


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 4/12



Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 5/12



Performance Validation Checkcases

Performance Validation Checks

Mach number, nd

AMC_HILL

time, sec

6

4

2

0

0.202
0.204
0.206
0.208

altitude, ft

H_BAT

6

4

2

0

15.6
15.7
15.8
15.9
16.0
 $\times 10^3$

RFCS static pres, psf

PS_SEL_BAT

6

4

2

0

1150
1155
1160
1165
1170

RFCS impact pres, psf

QCI_SEL_BAT

6

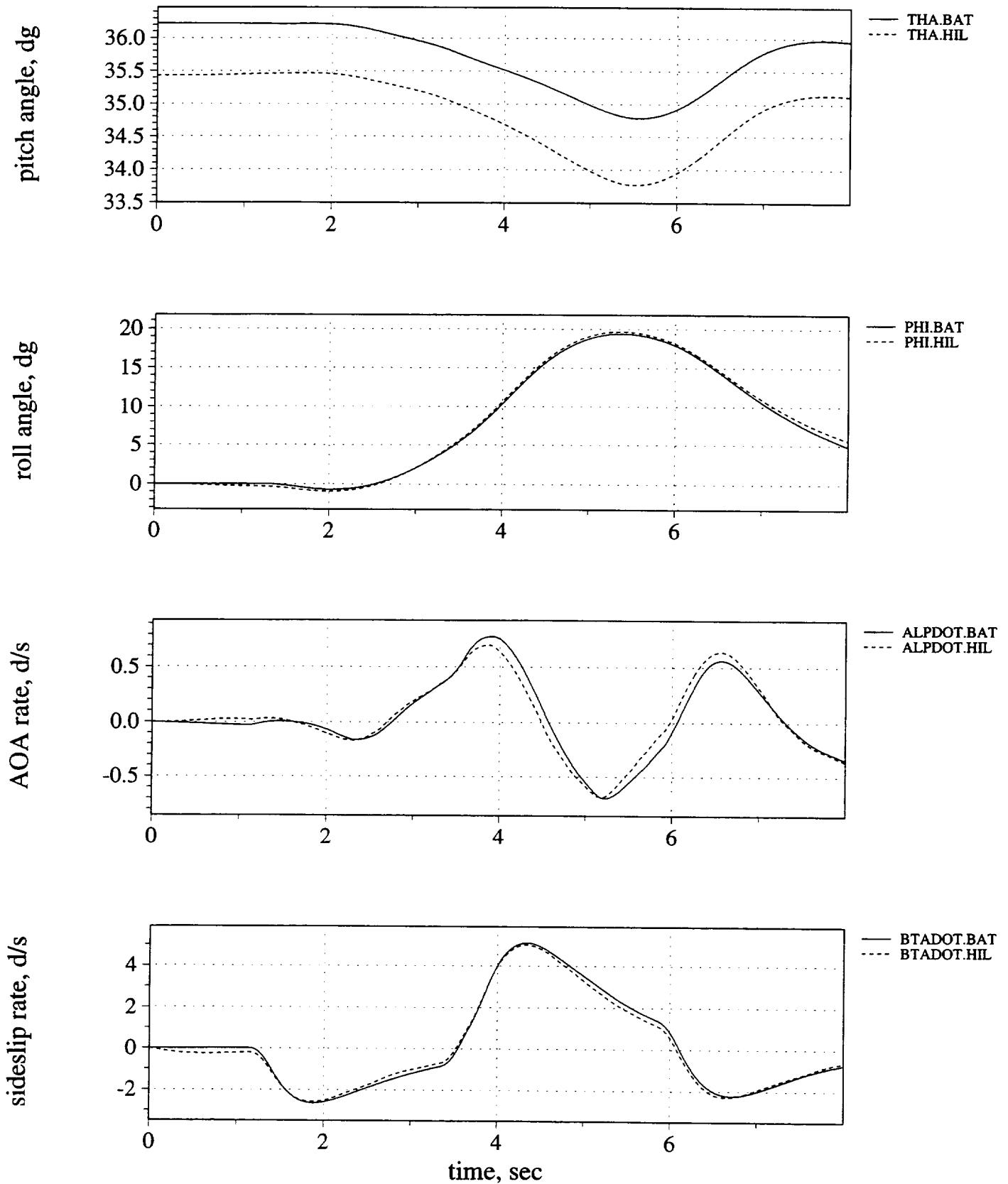
4

2

0

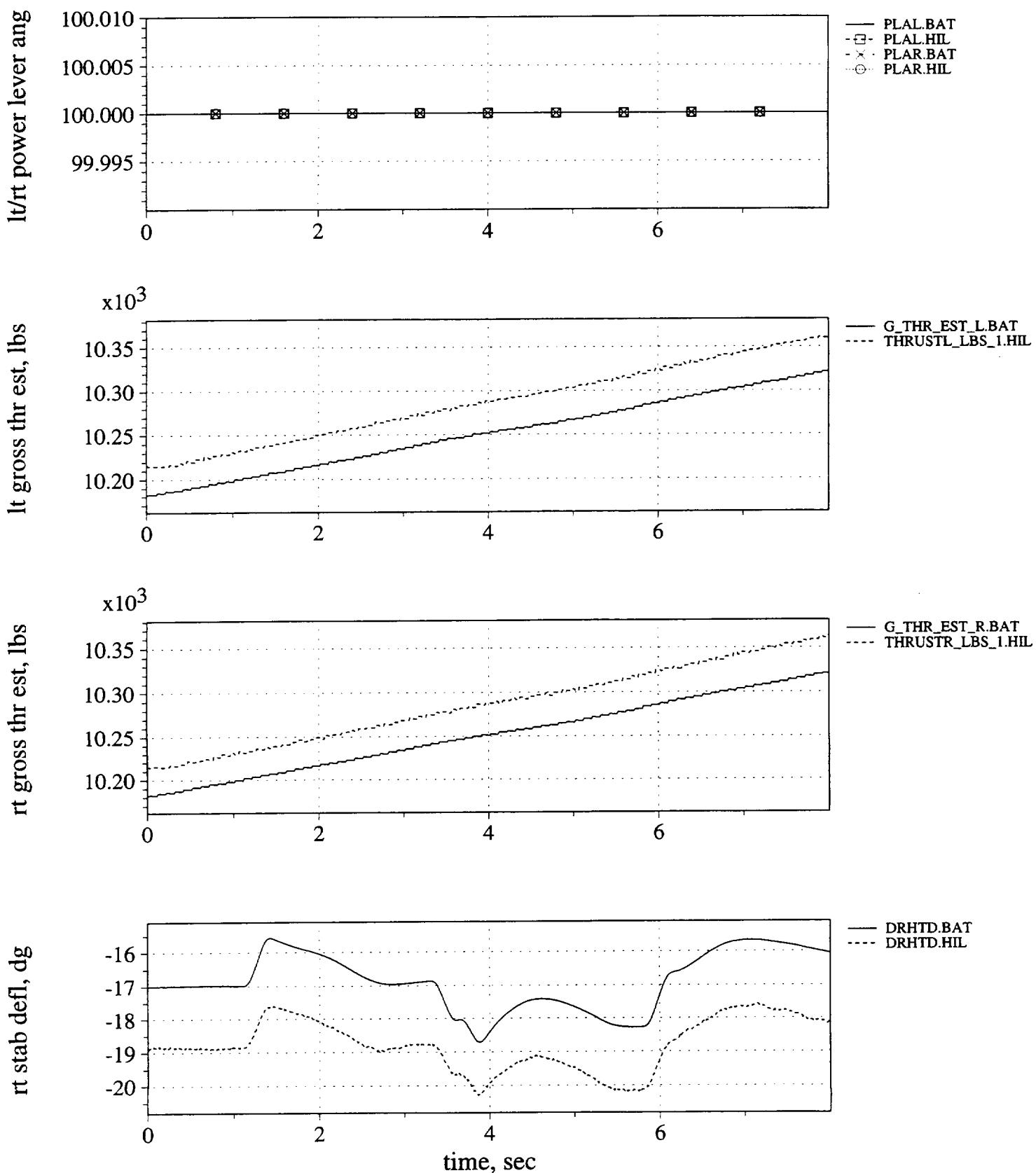
34.5
35.0
35.5
36.0
36.5
37.0
37.5

Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 7/12



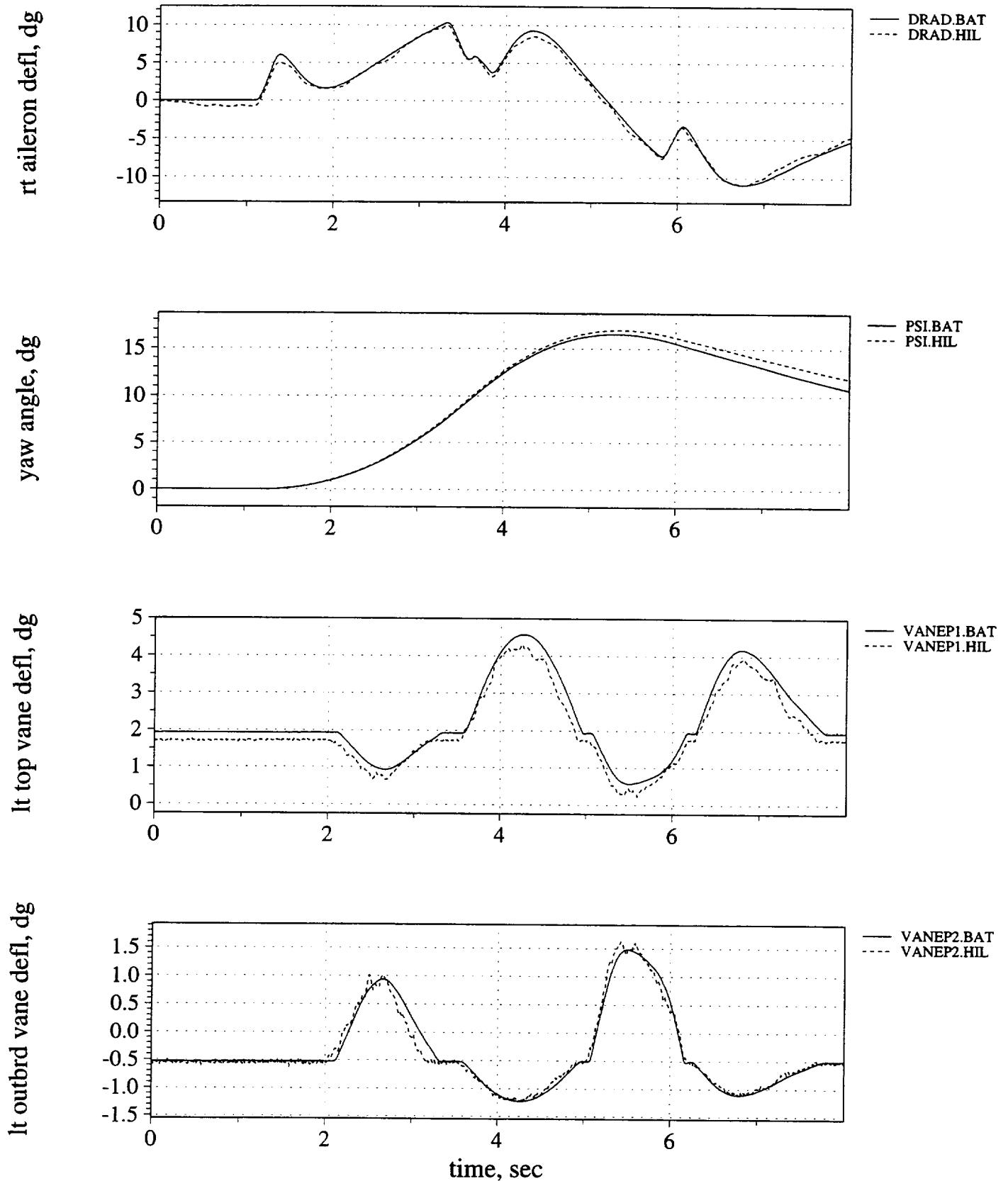
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 8/12



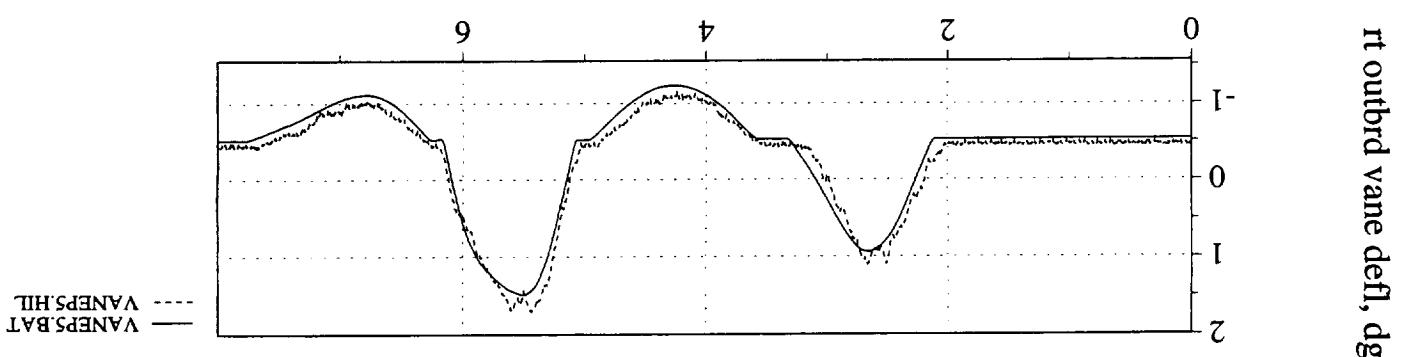
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 9/12

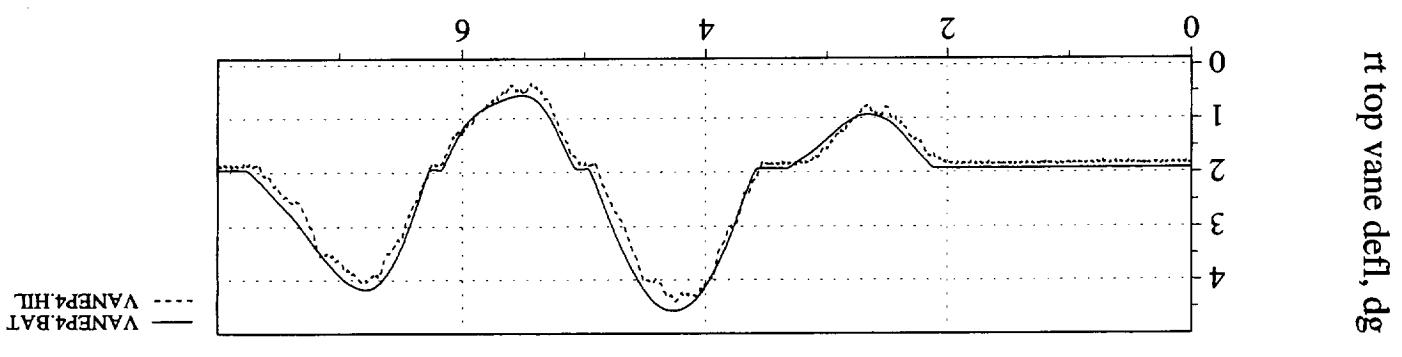


Performance Validation Checkcases

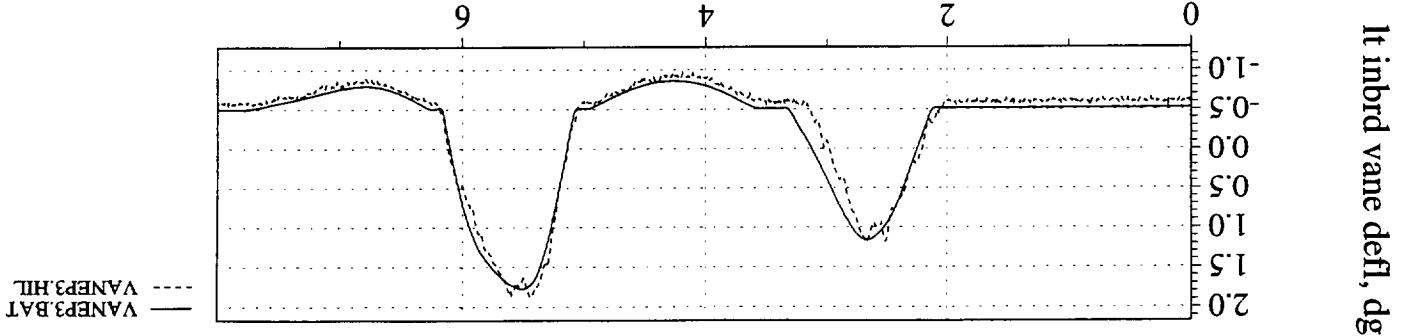
rt inbnd vane defl, dg



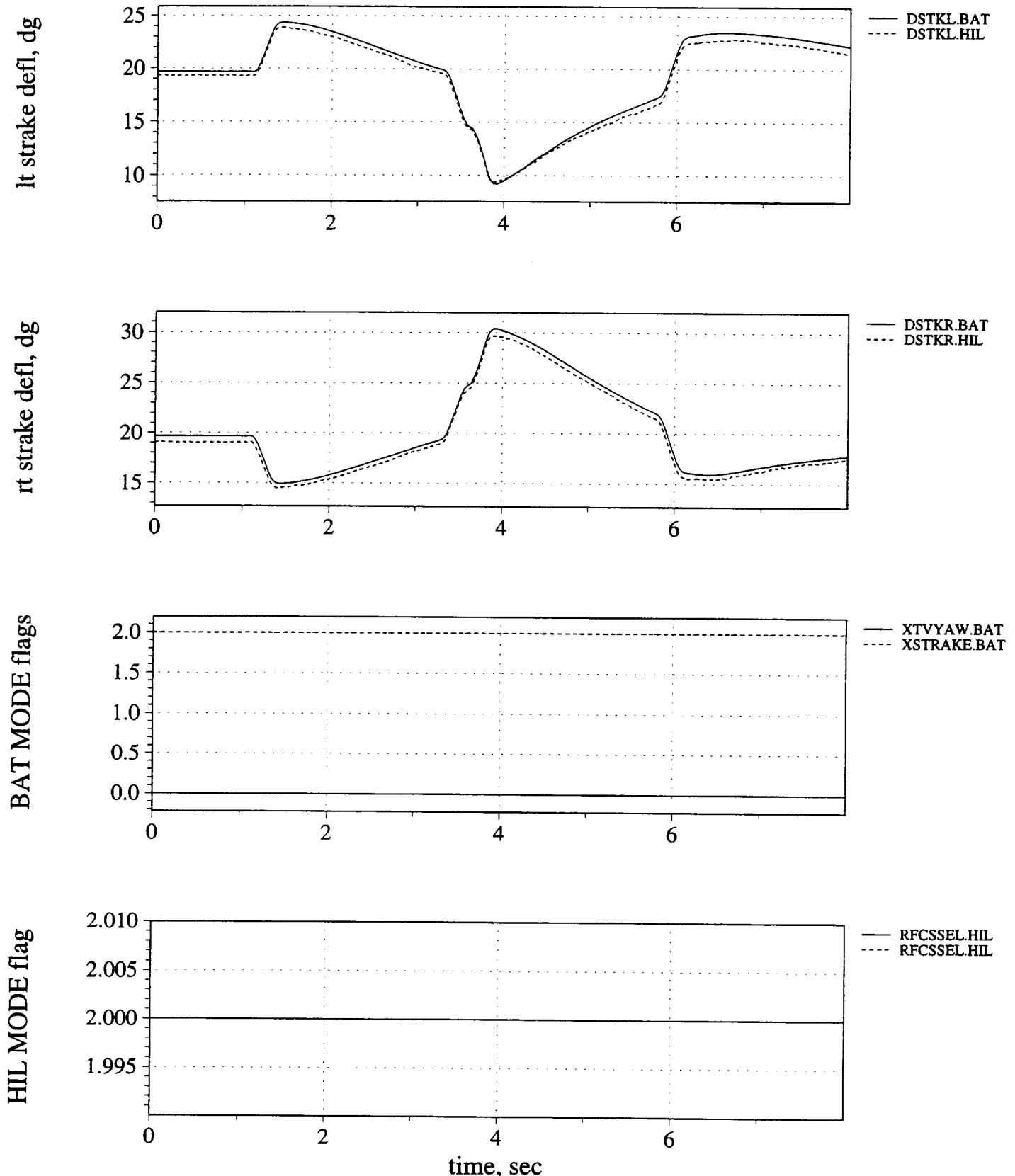
rt top vane defl, dg



It inbnd vane defl, dg

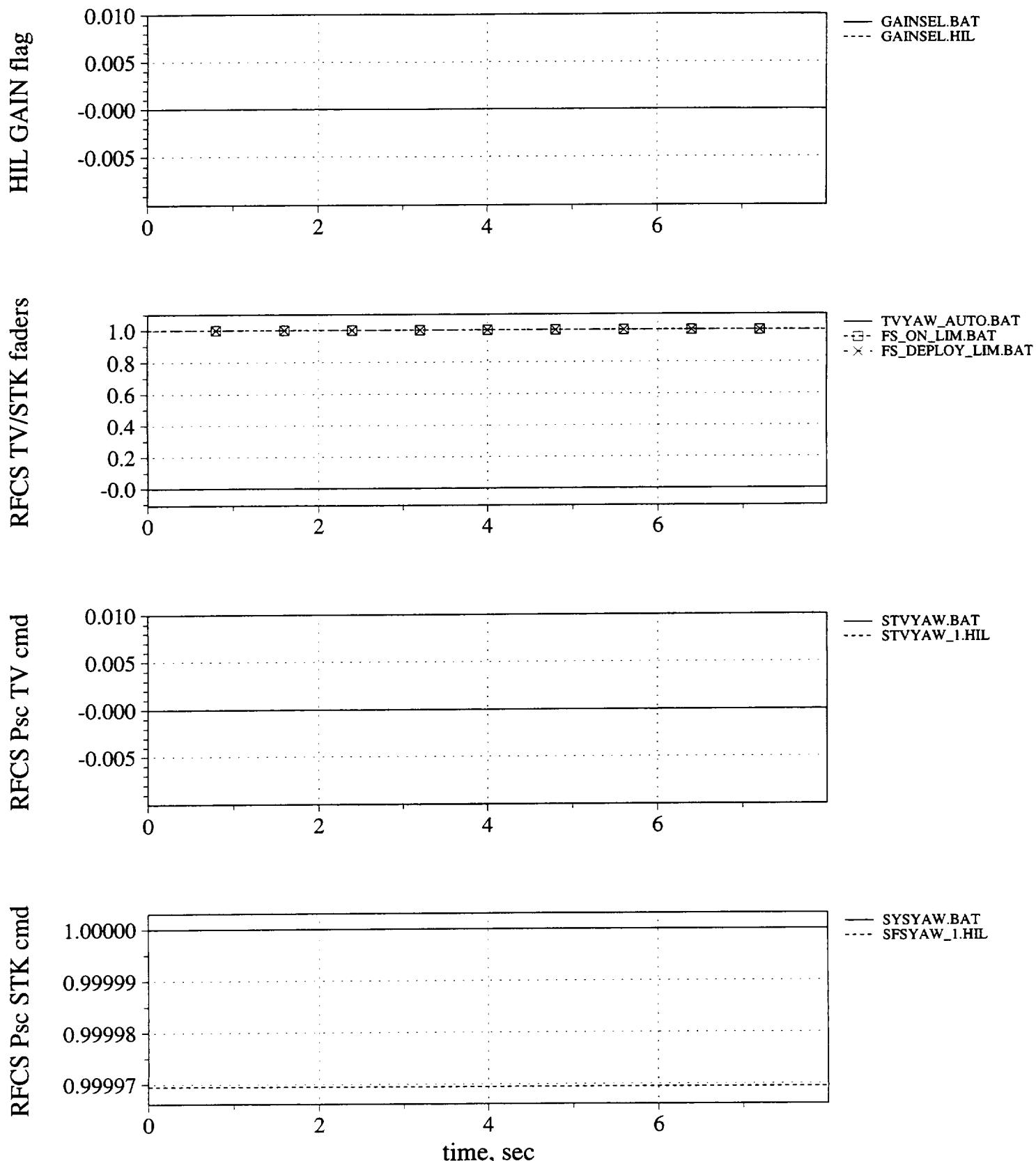


Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 11/12



Performance Validation Checkcases

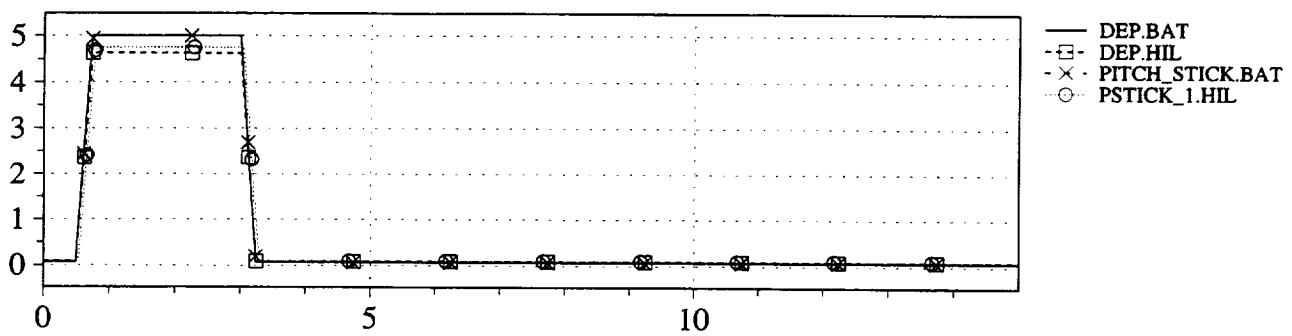
Appendix B - Batch versus HIL Version 154.0
CASE_27_S_Mode_50alp-16k_Max_AB_2_in_ramp_drp page 12/12



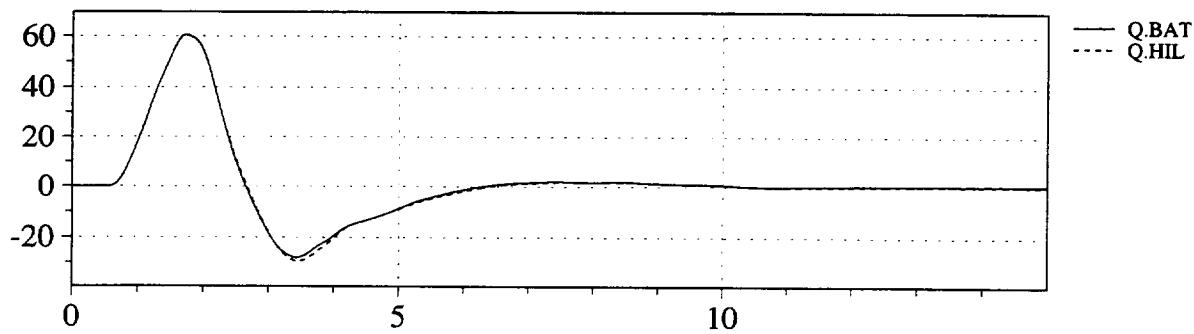
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 1/12

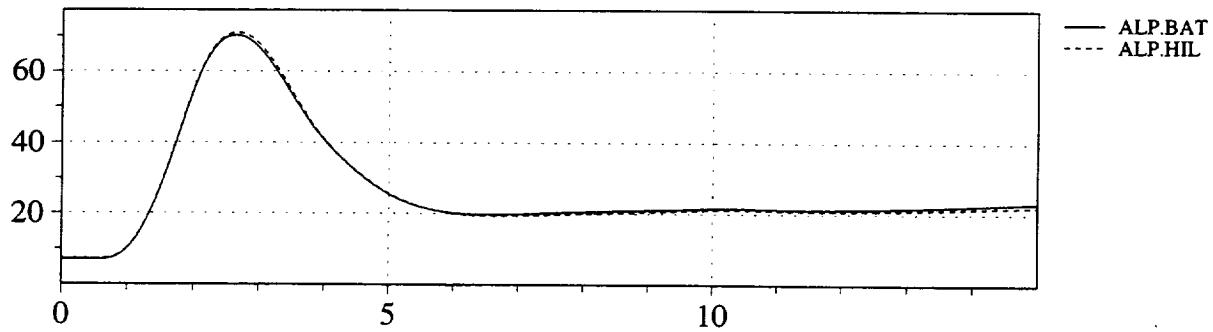
total pit stk cmd, in



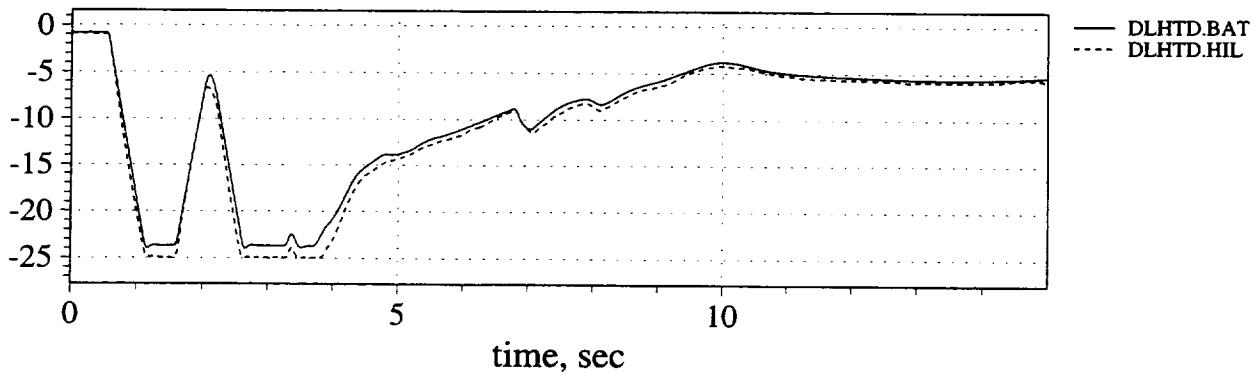
pitch rate, d/s



Angle-of-attack, dg



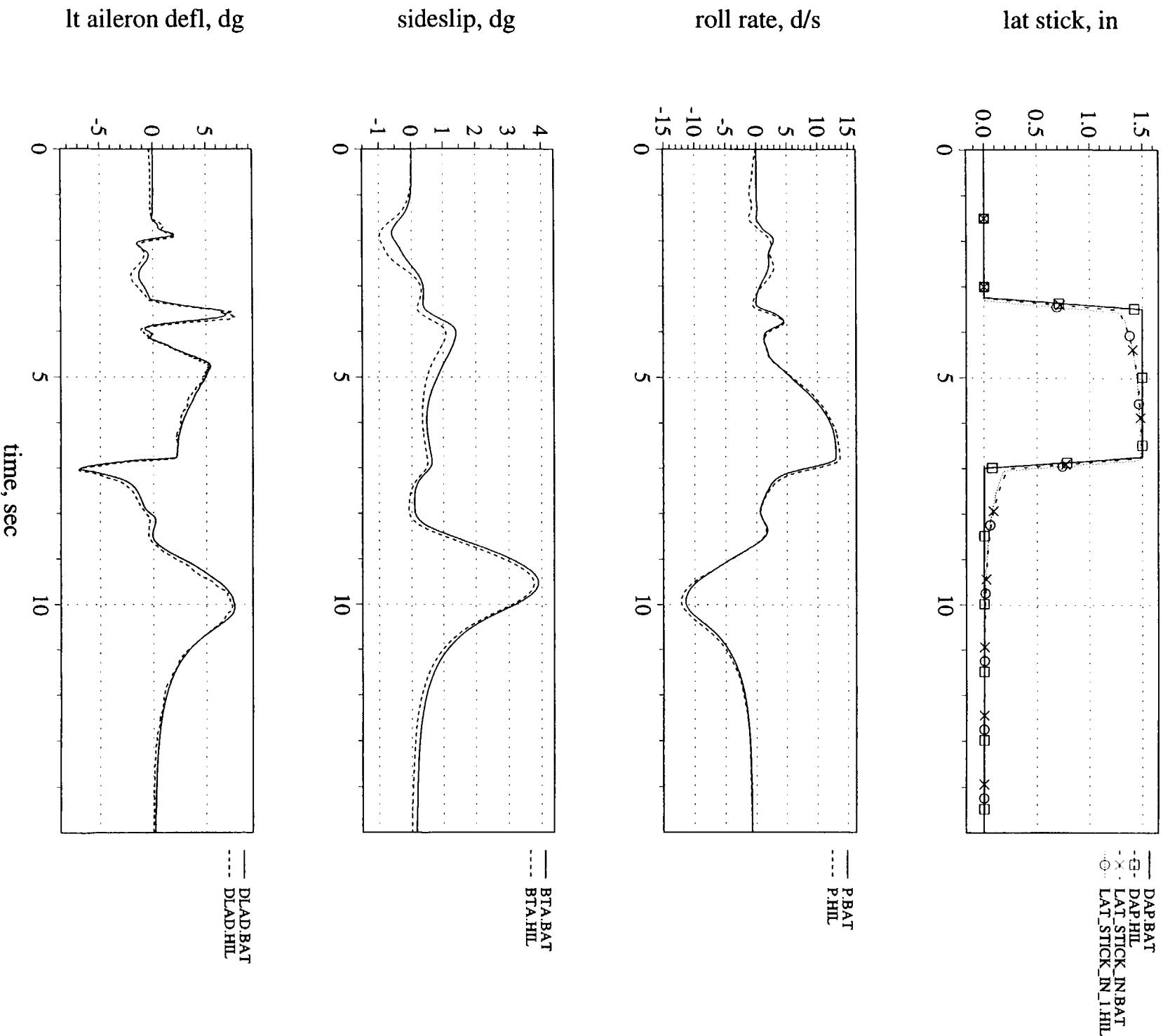
lt stab defl, dg



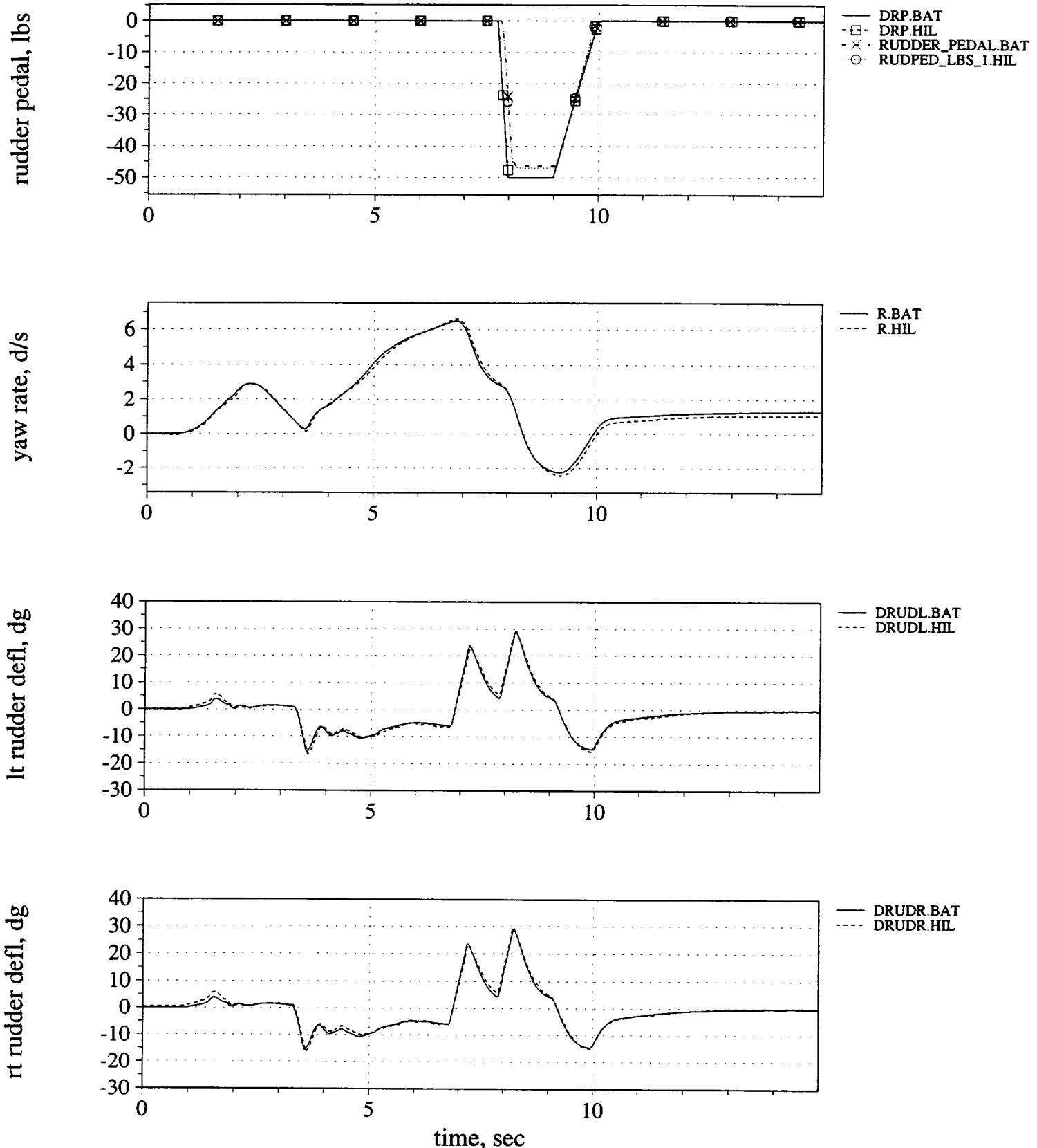
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

CASE_30_S_Mode_.68-40k_trim--3_axis_input page 2/12



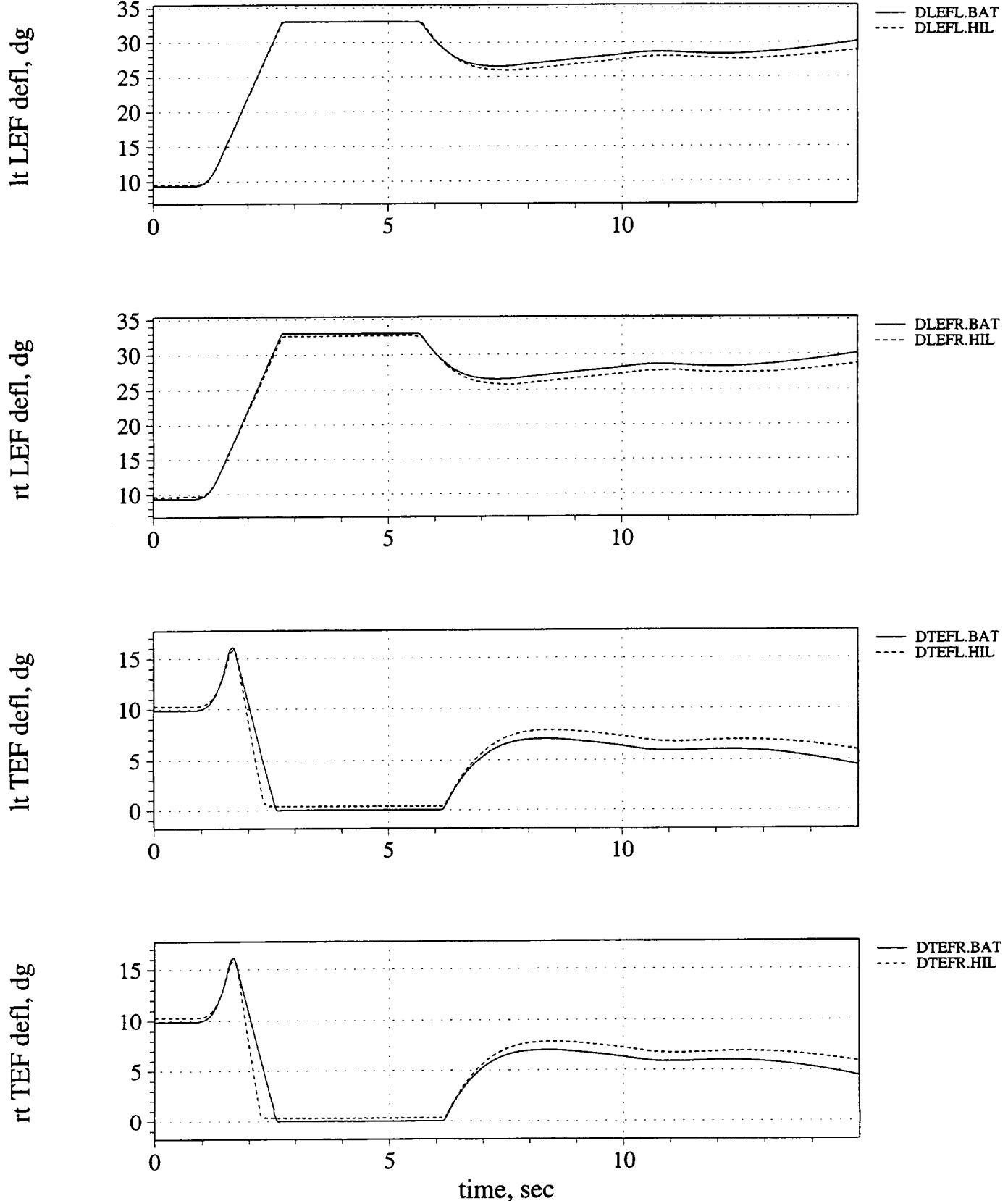
Appendix B - Batch versus HIL Version 154.0
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 3/12



Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

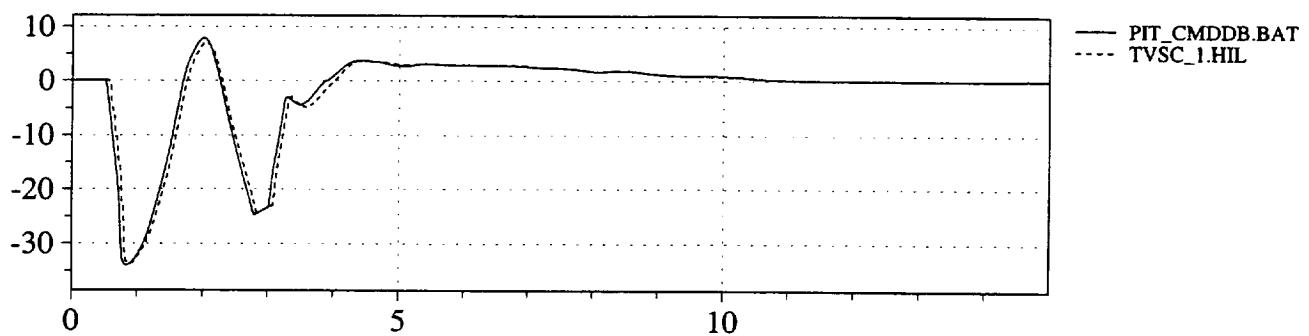
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 4/12



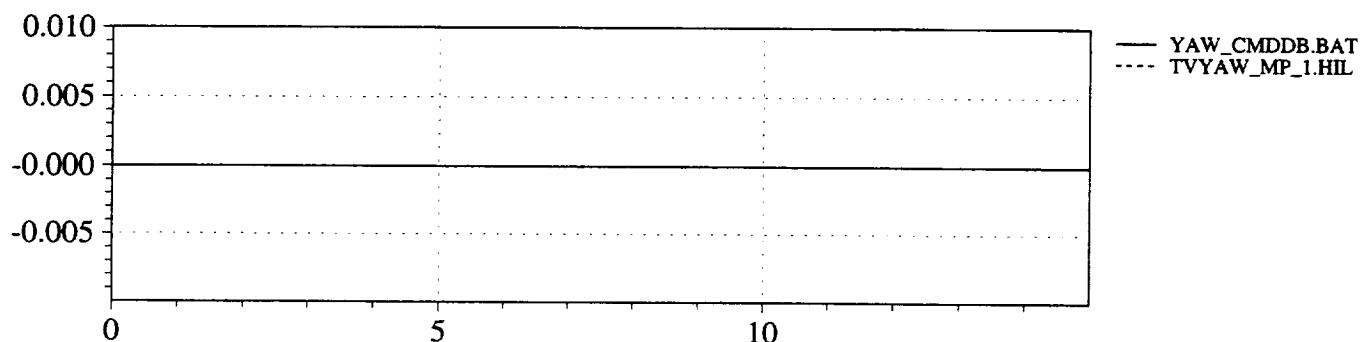
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 5/12

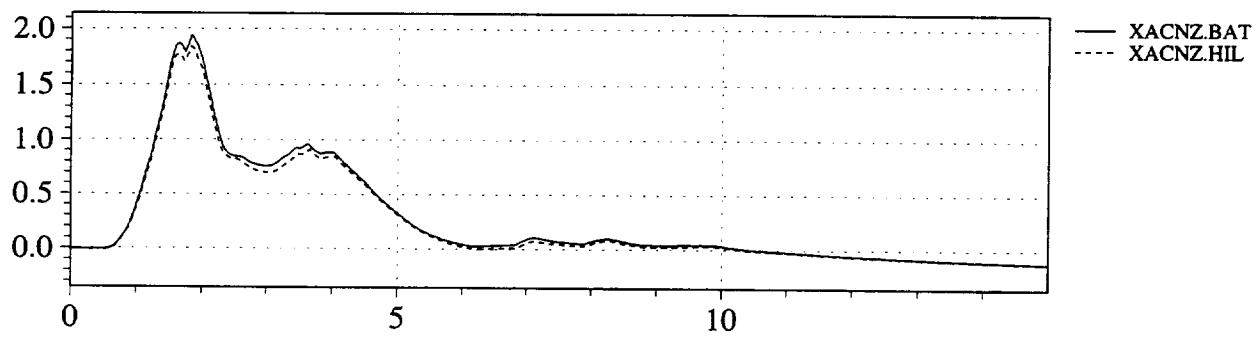
RFCS pit jet cmd, dg



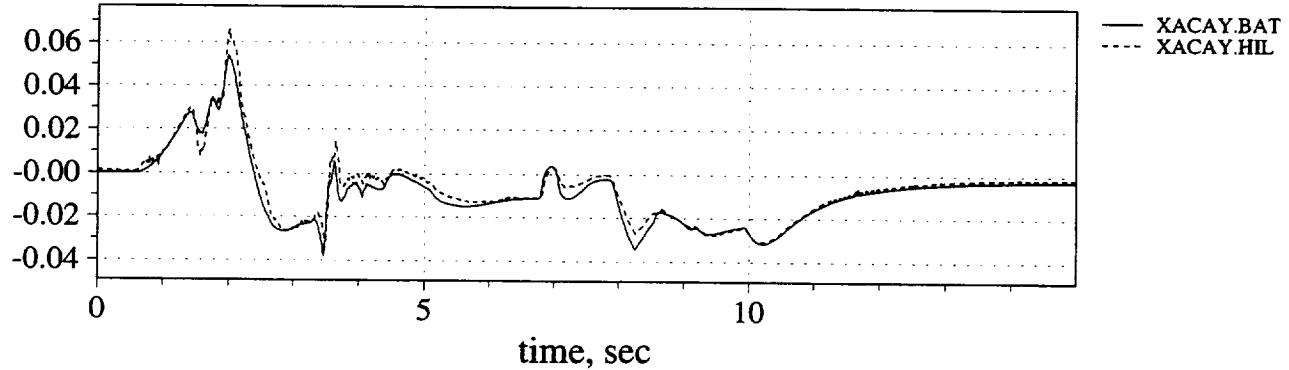
RFCS yaw jet cmd, dg



normal accel, g



lateral accel, g

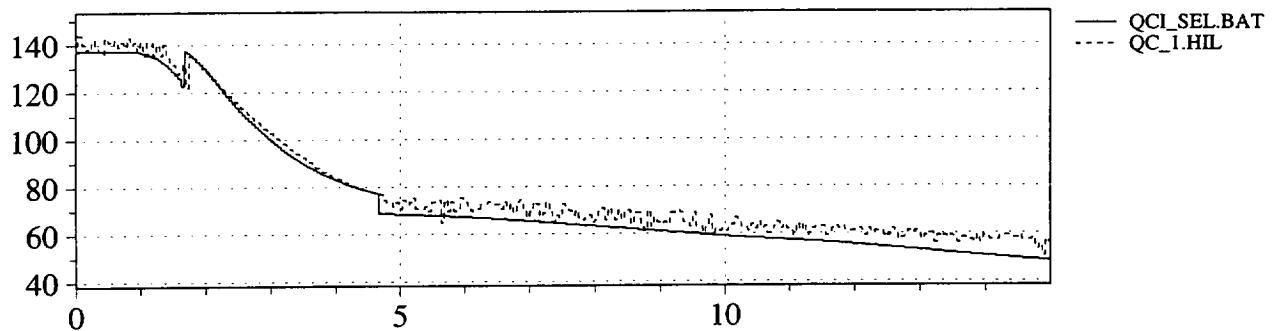


Performance Validation Checkcases

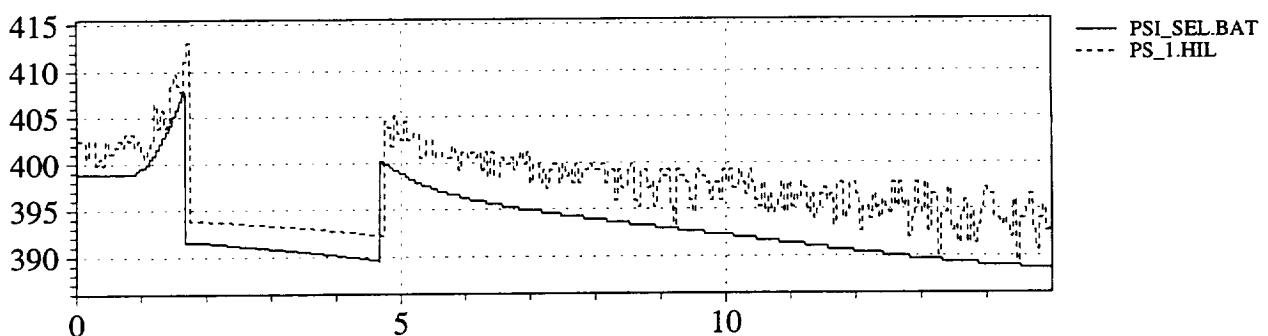
Appendix B - Batch versus HIL Version 154.0

CASE_30_S_Mode_.68-40k_trim--3_axis_input page 6/12

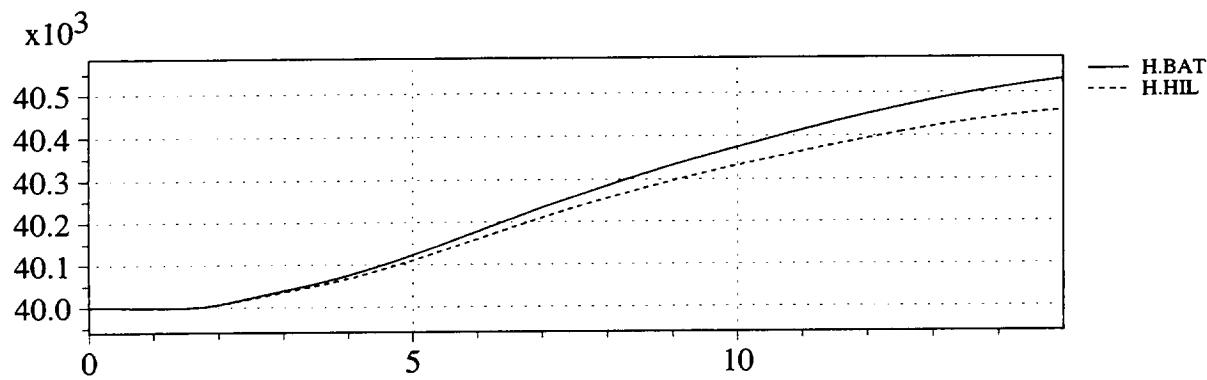
RFCS impact pres, psf



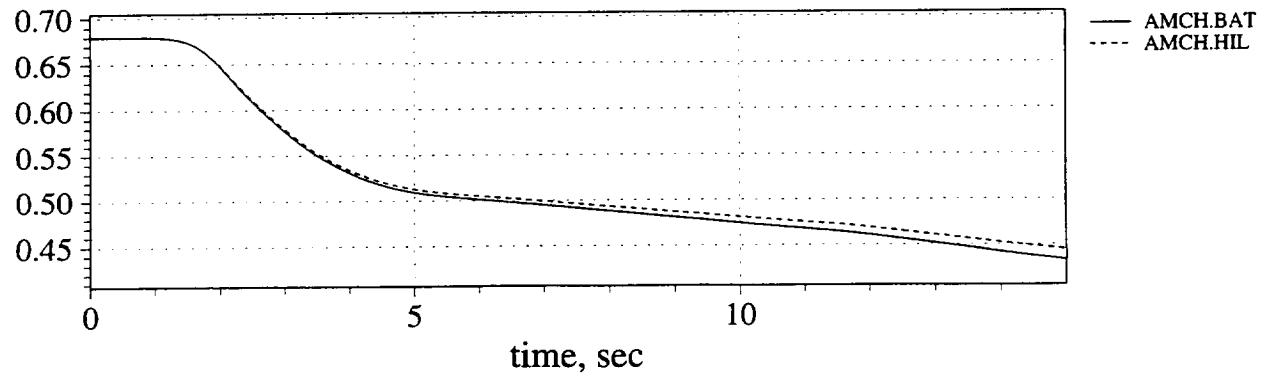
RFCS static pres, psf



altitude, ft

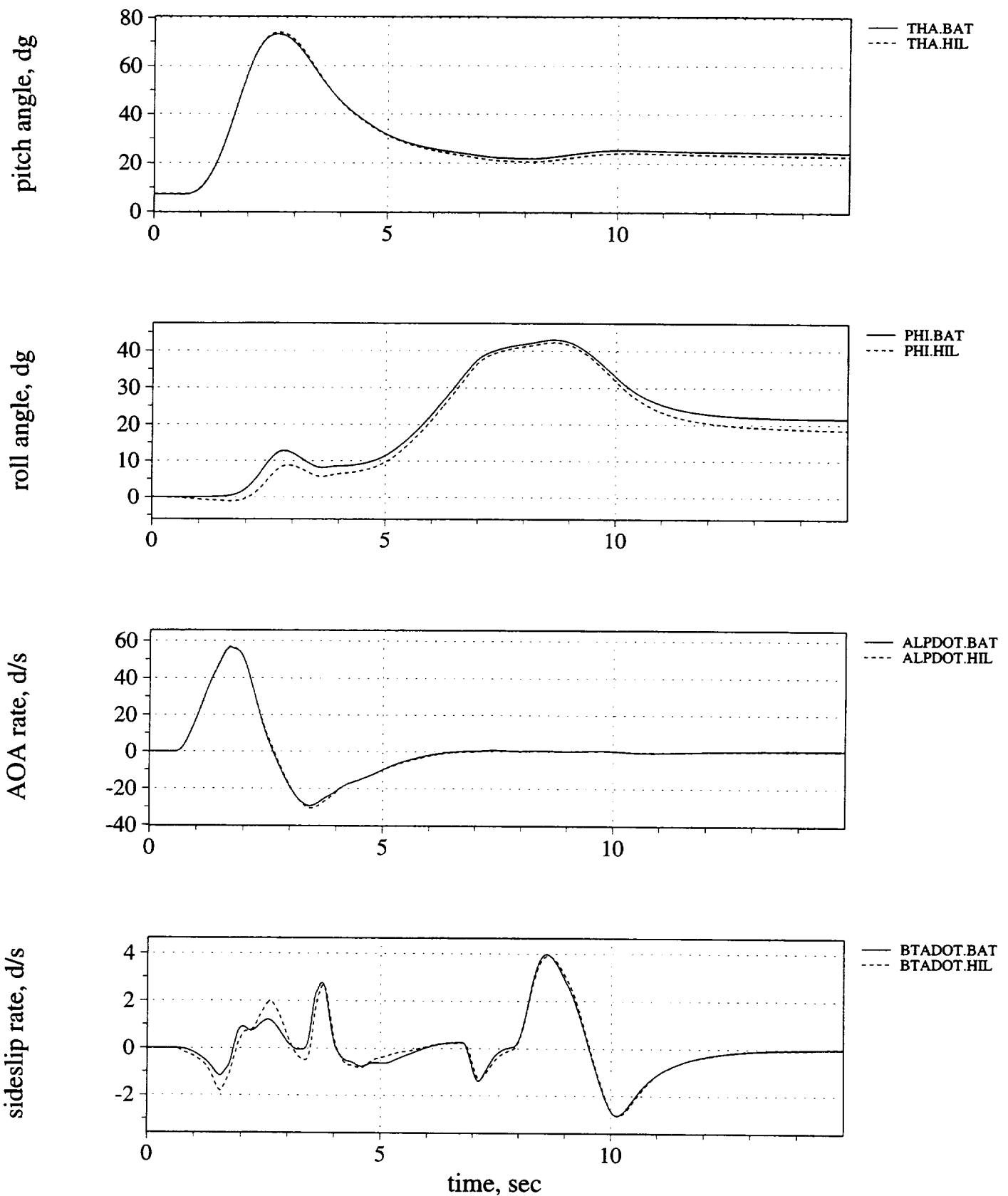


Mach number, nd



Appendix B - Batch versus HIL Version 154.0

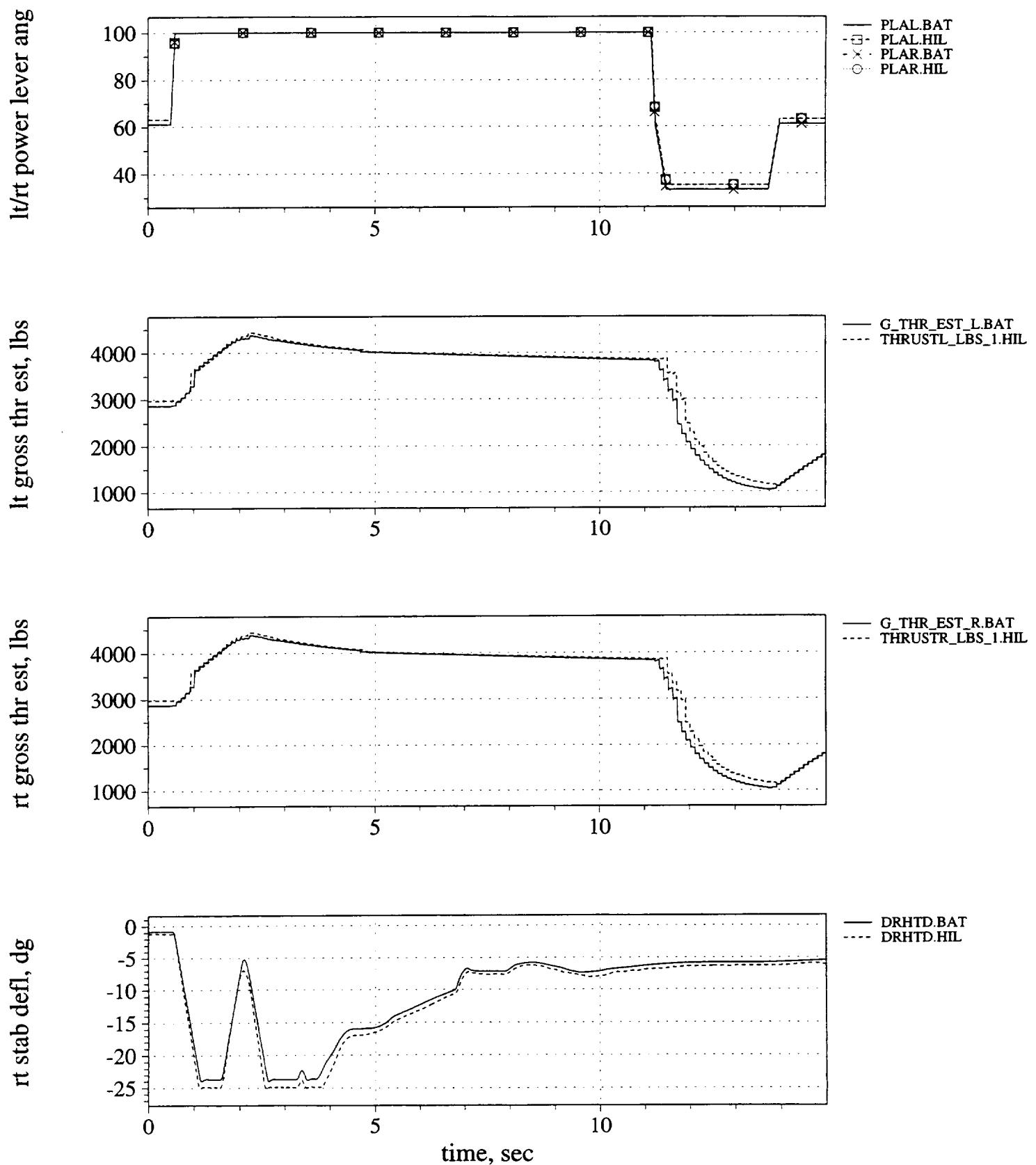
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 7/12



Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

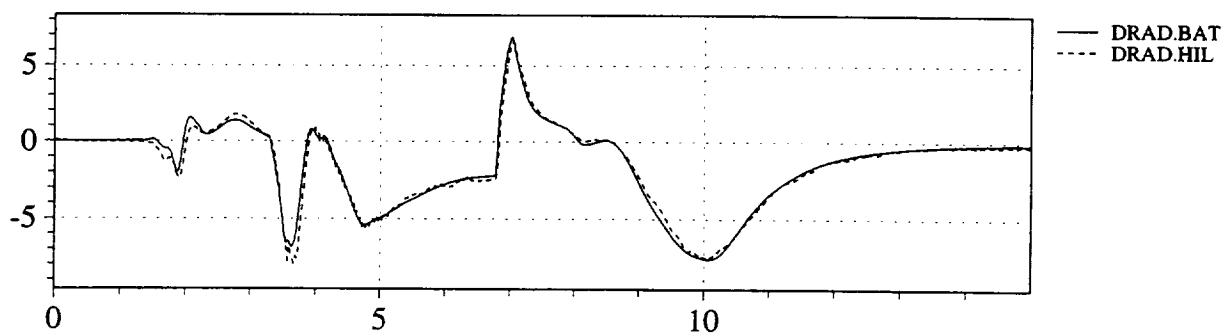
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 8/12



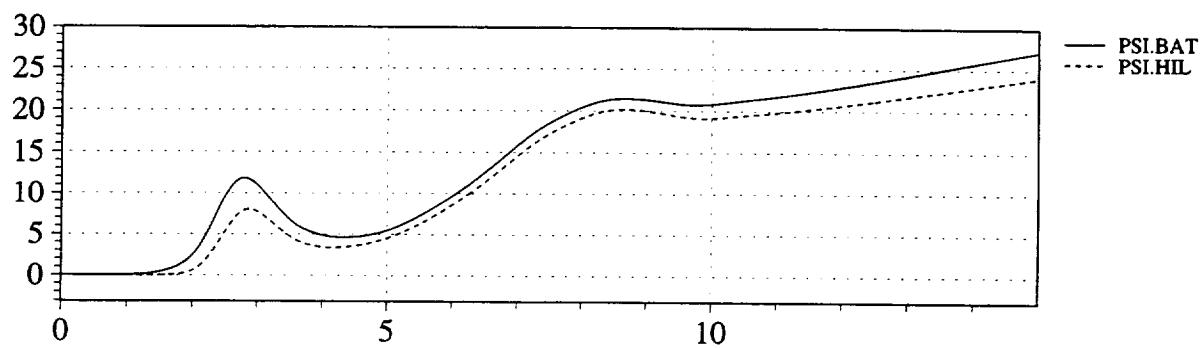
Appendix B - Batch versus HIL Version 154.0

CASE_30_S_Mode_.68-40k_trim--3_axis_input page 9/12

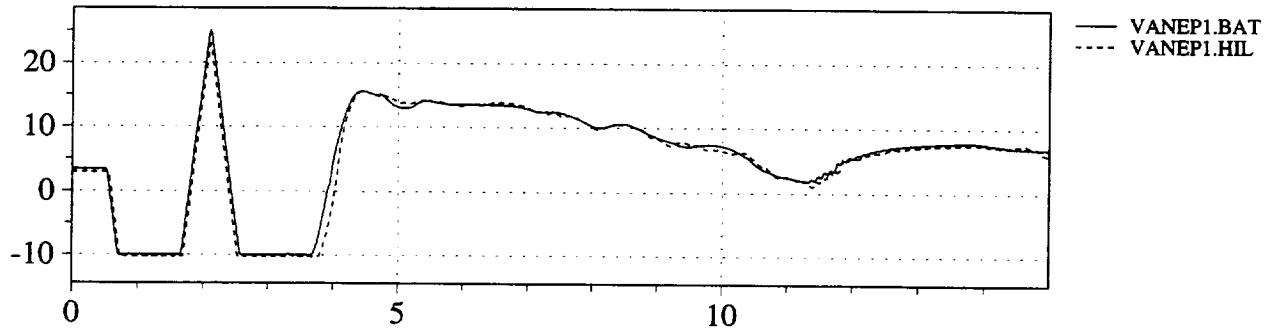
rt aileron defl, dg



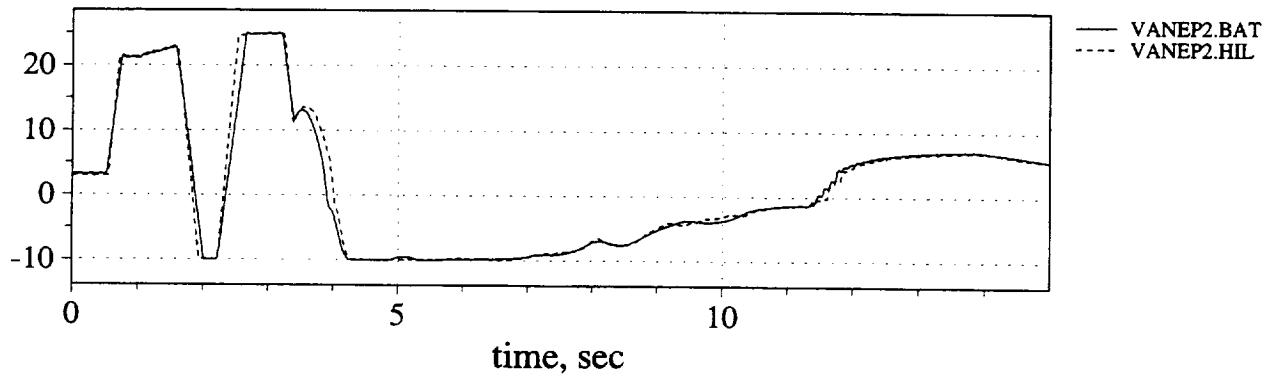
yaw angle, dg



lt top vane defl, dg



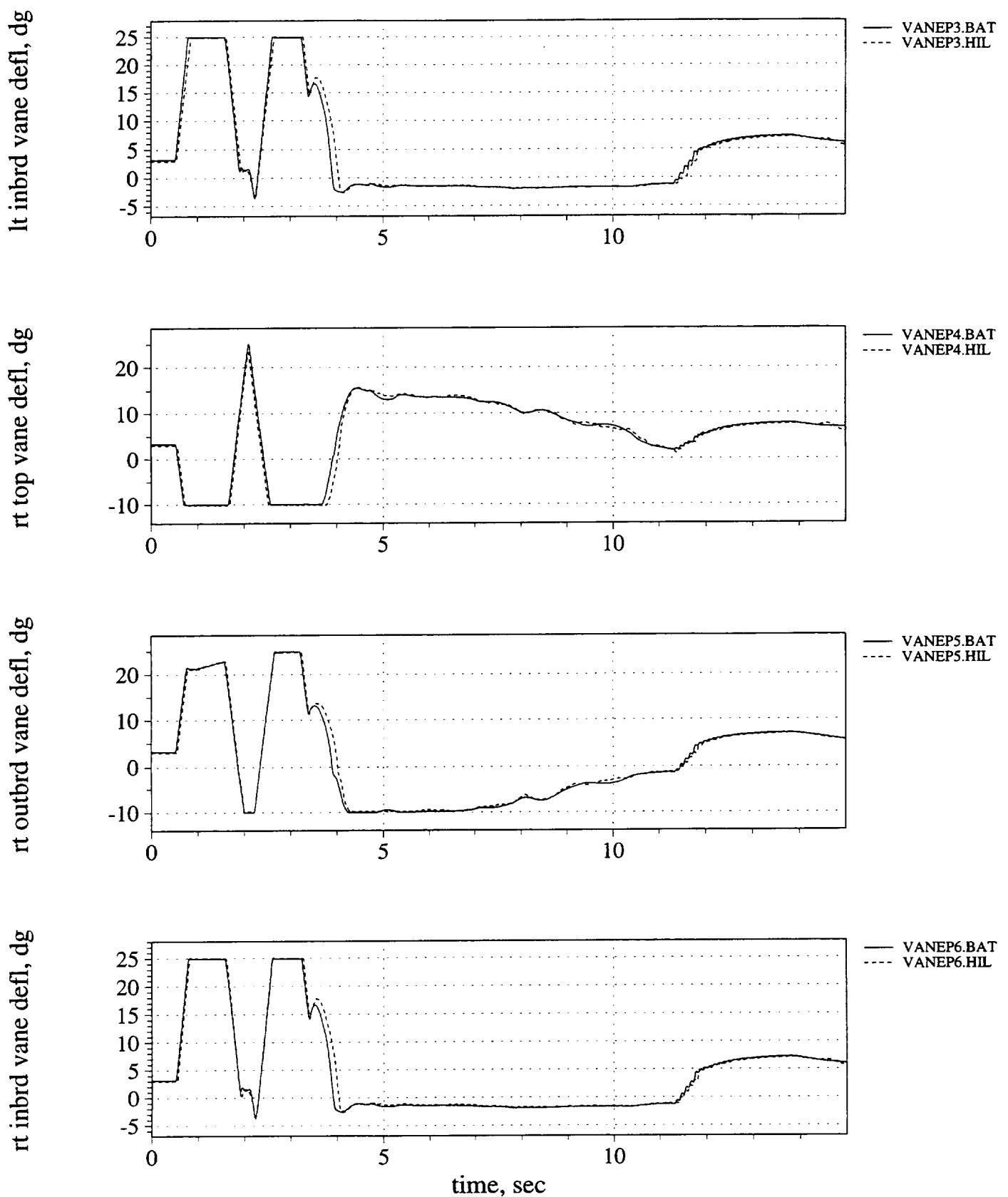
lt outbrd vane defl, dg



time, sec

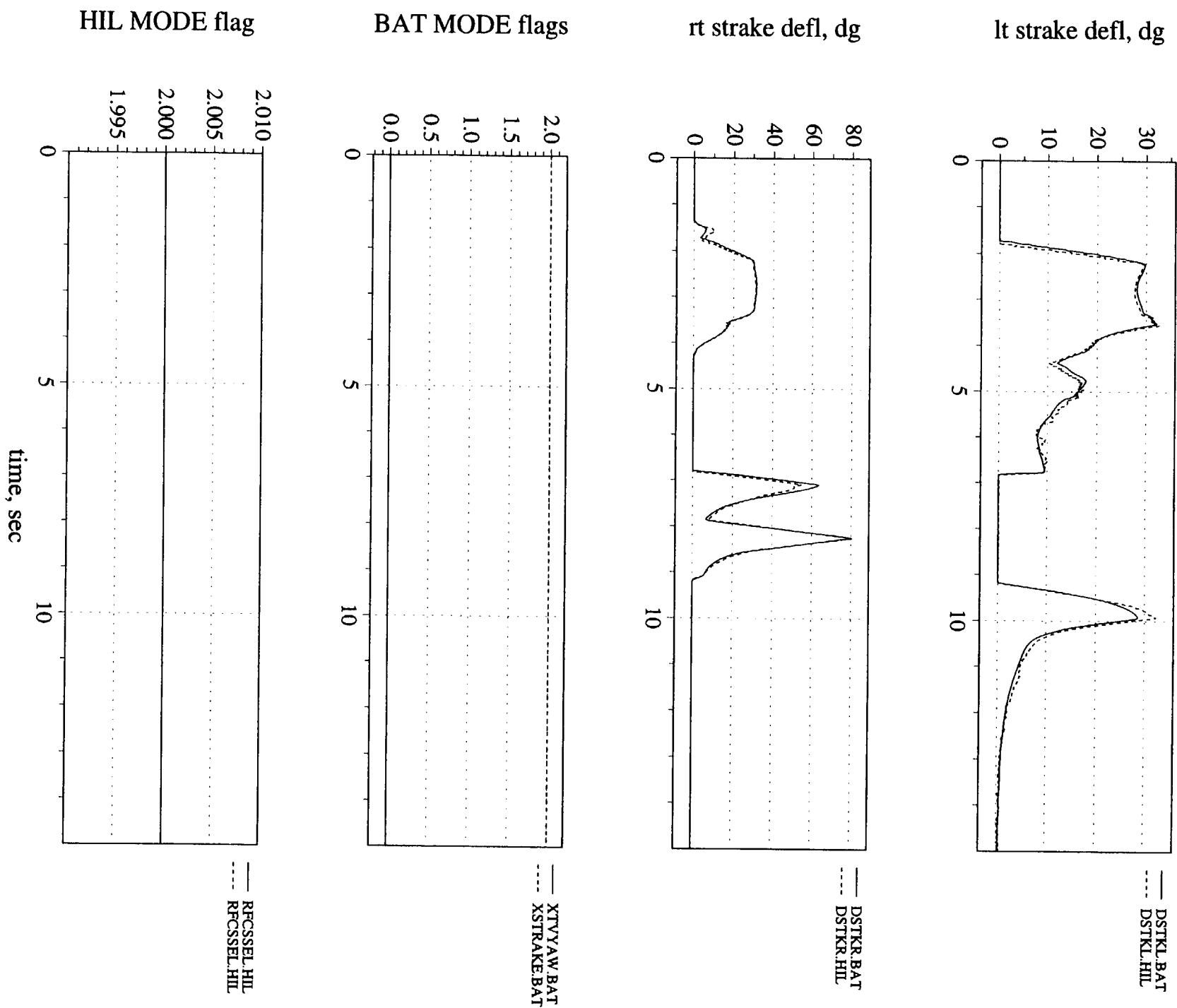
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_30_S_Mode_.68-40k_trim-3_axis_input page 10/12



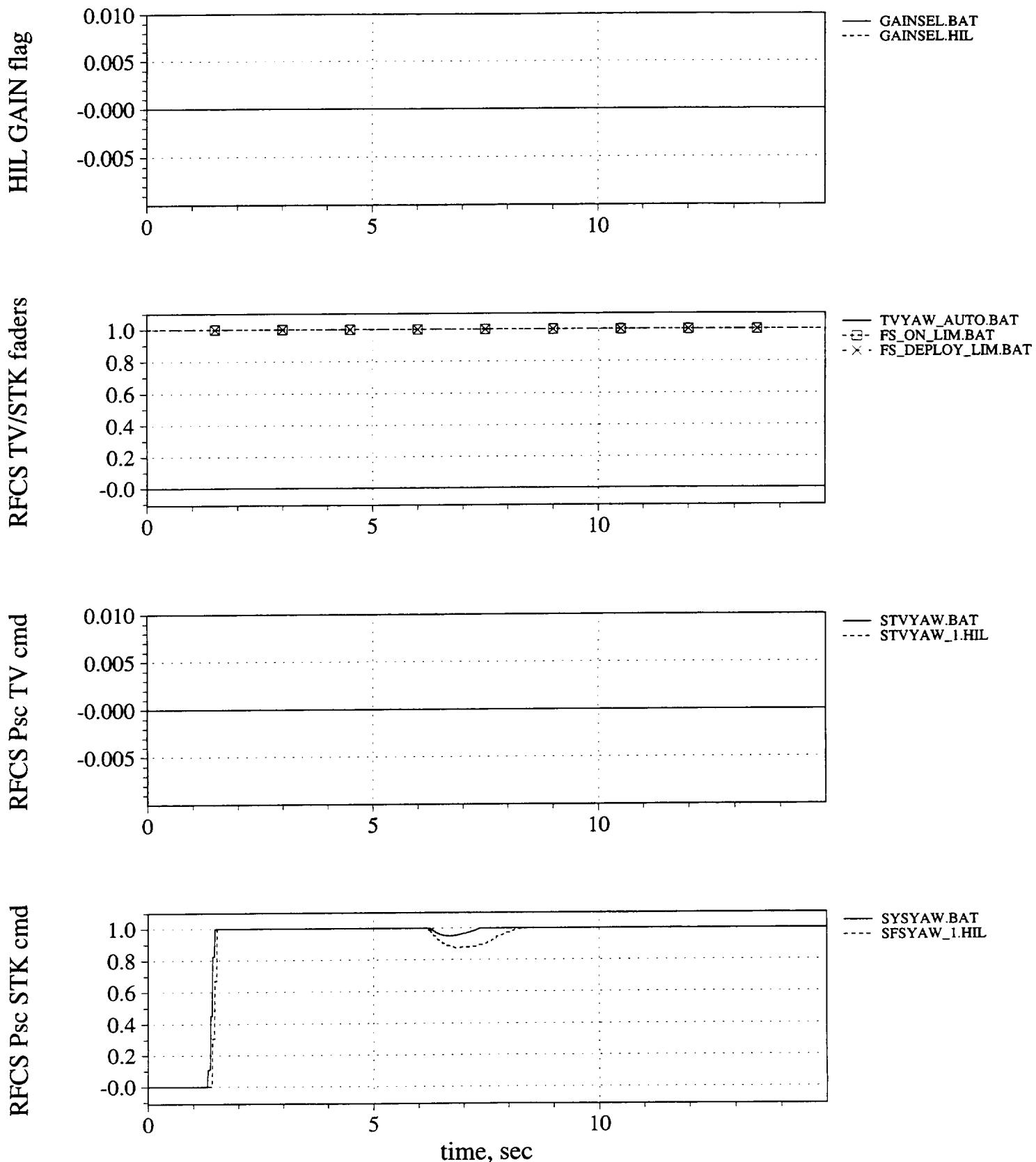
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_30_S_Mode_.68-40k_trim-3_axis_input page 11/12



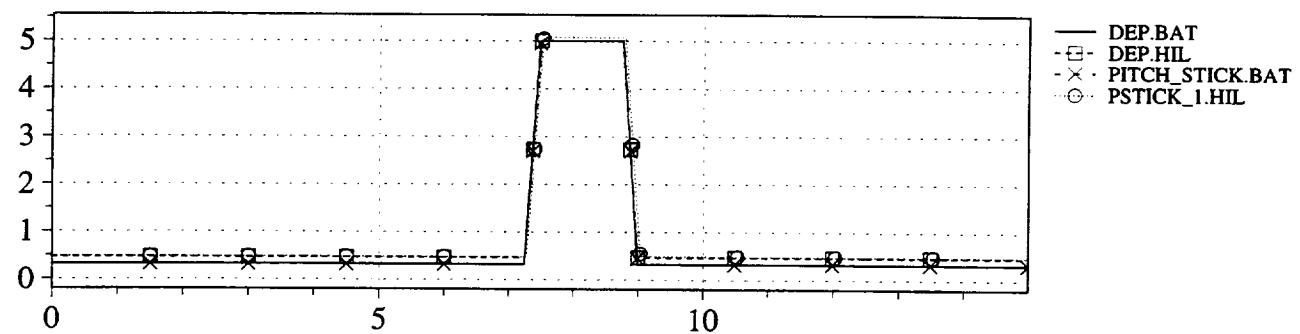
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_30_S_Mode_.68-40k_trim--3_axis_input page 12/12

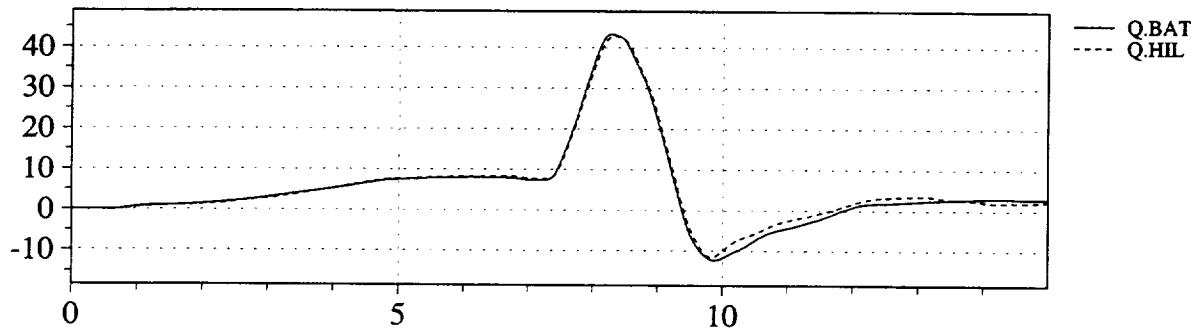


Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 1/12

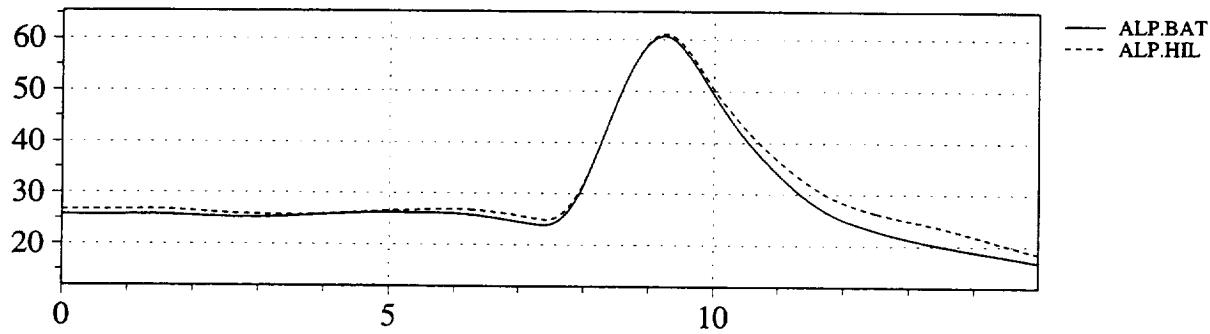
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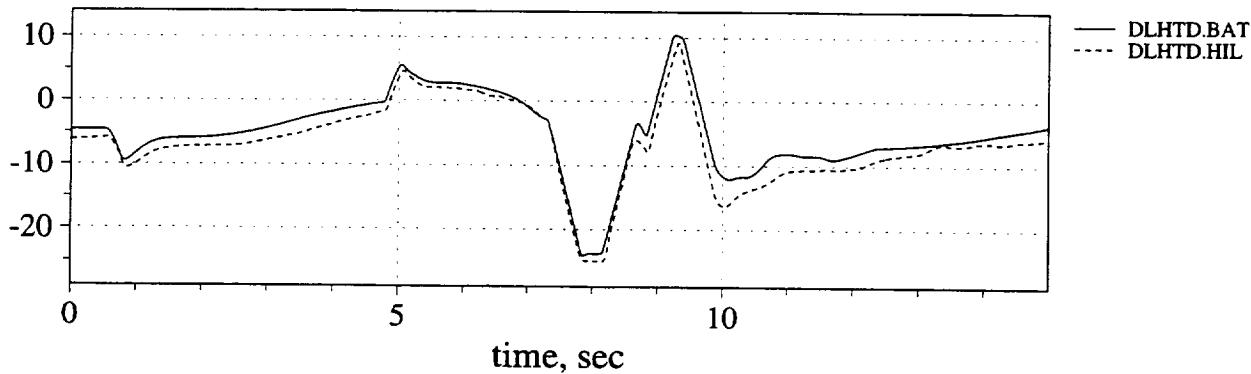
pitch rate, d/s



Angle-of-attack, dg



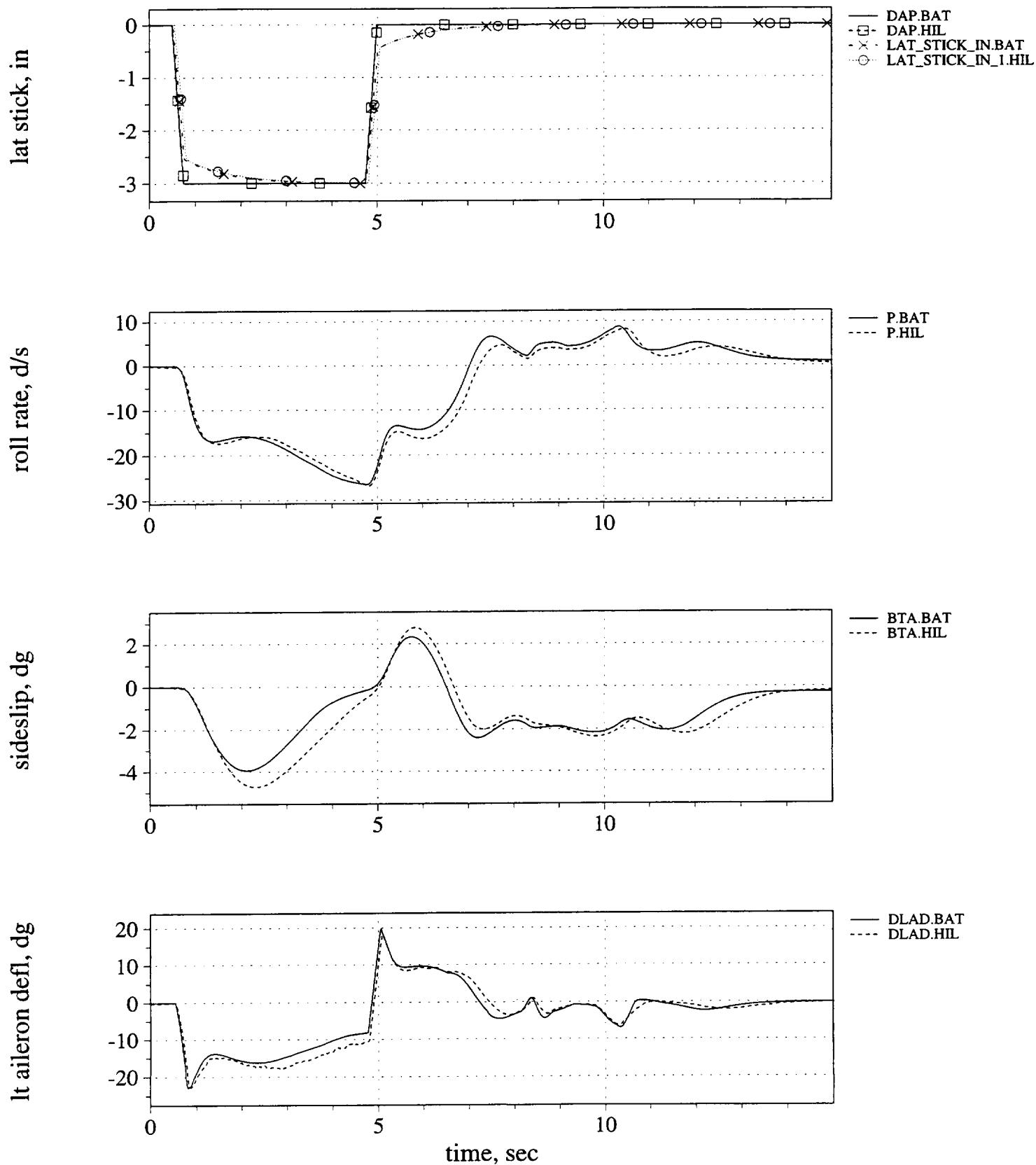
lt stab defl, dg



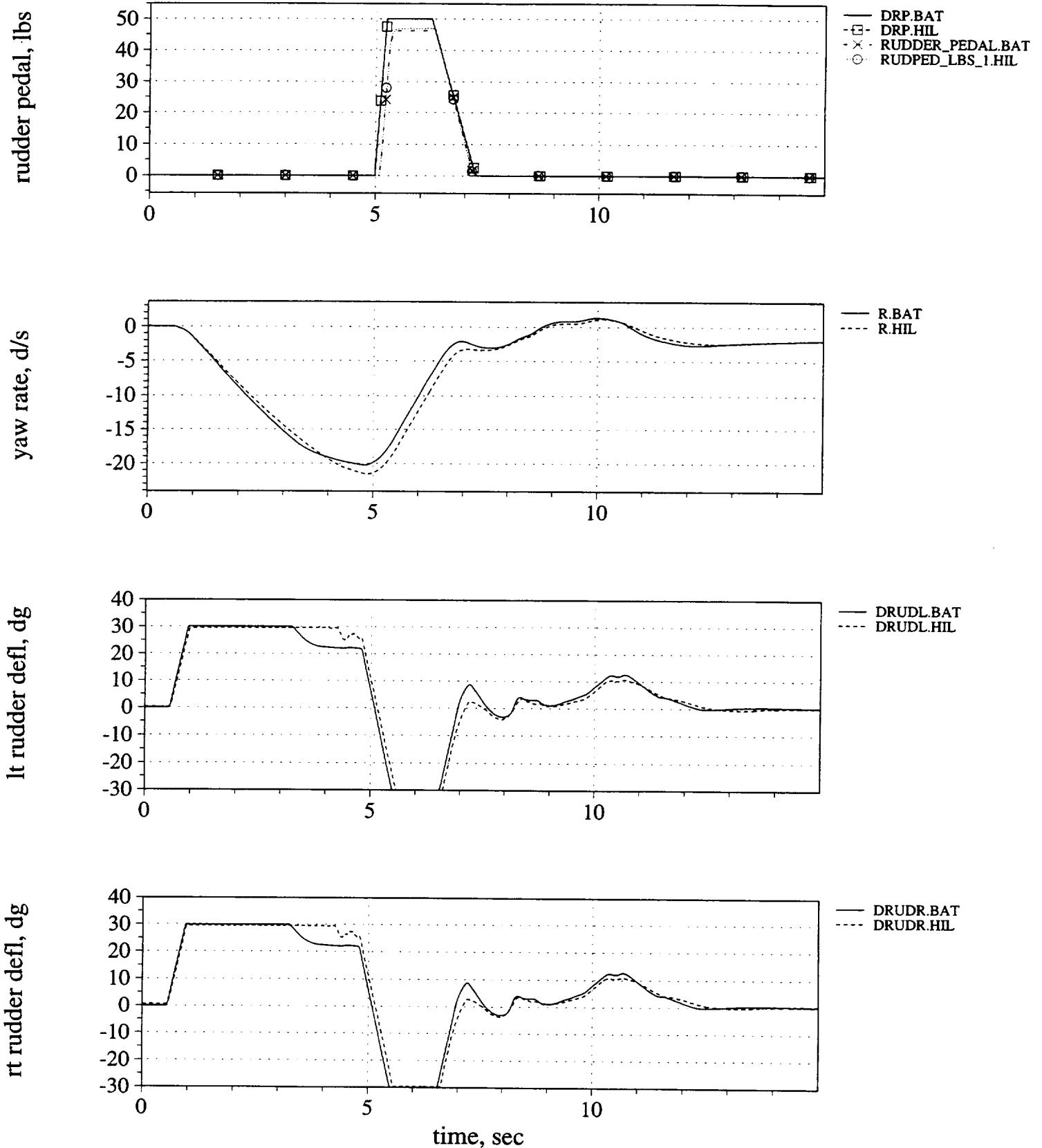
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

CASE_33_S_Mode_.35-25k_trim--3_axis_input page 2/12

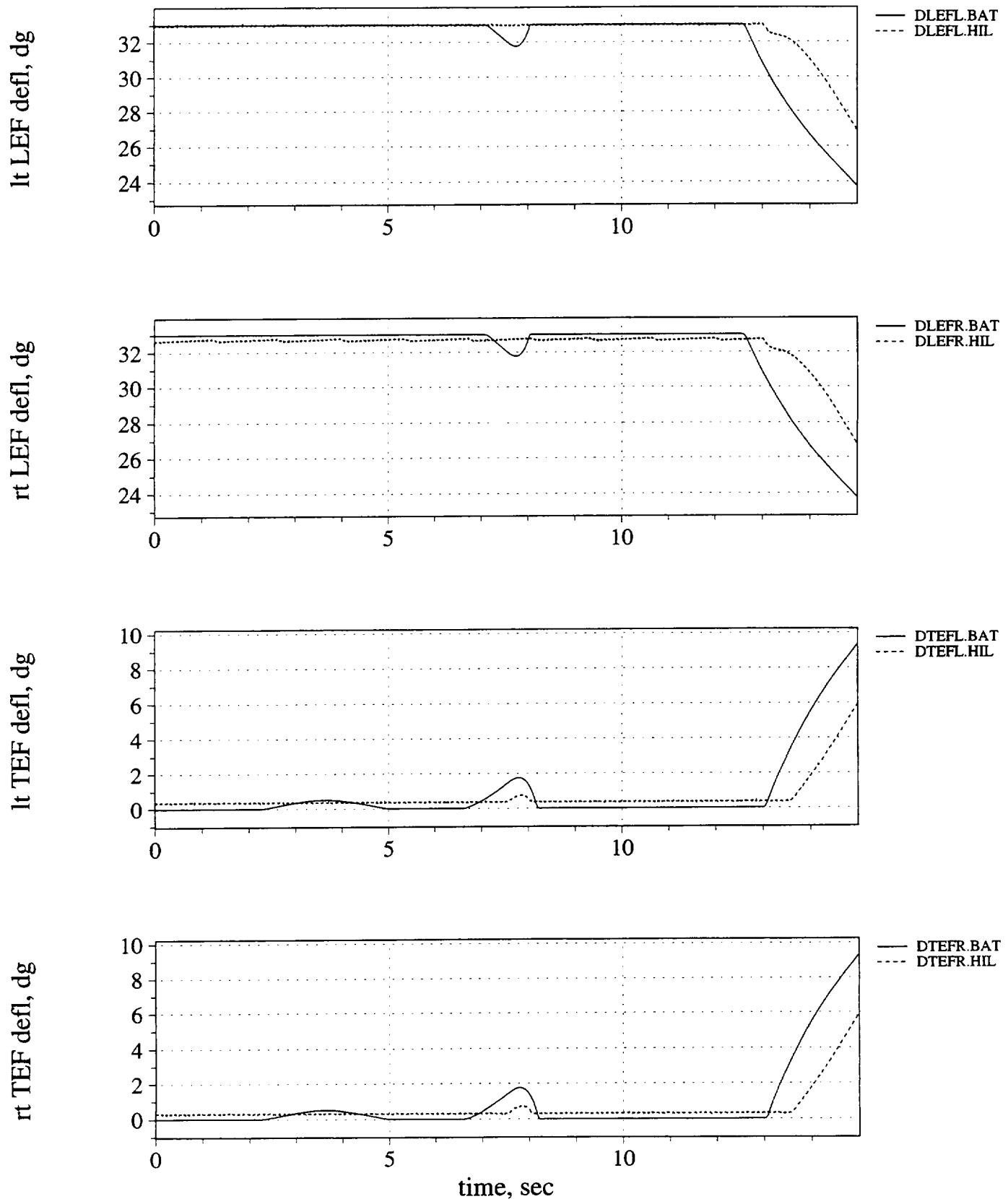


Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 3/12



Performance Validation Checkcases

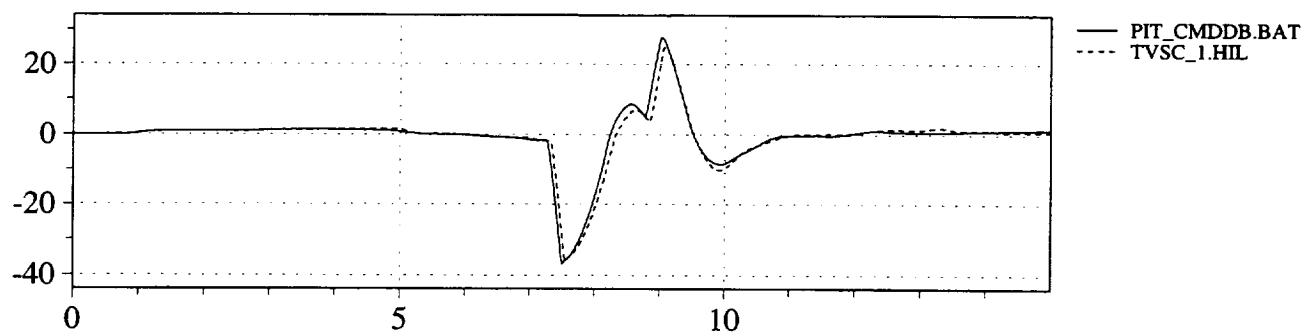
Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 4/12



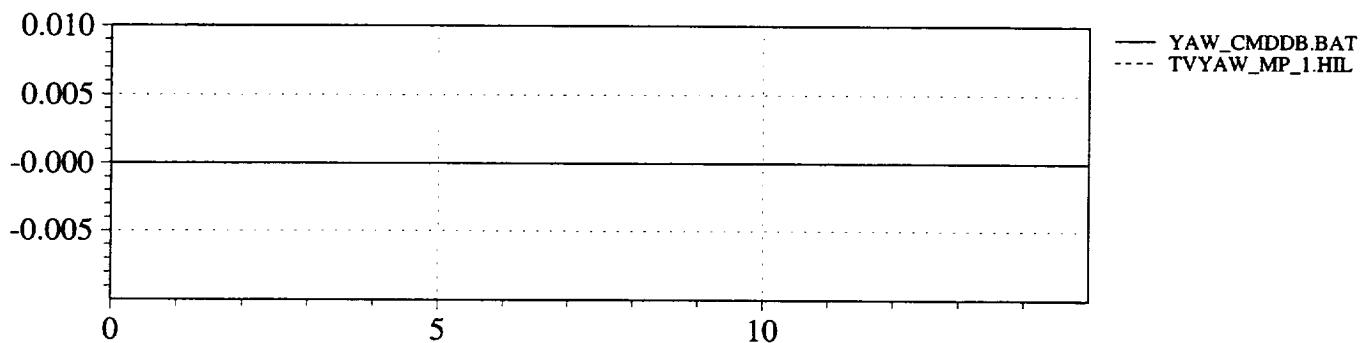
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 5/12

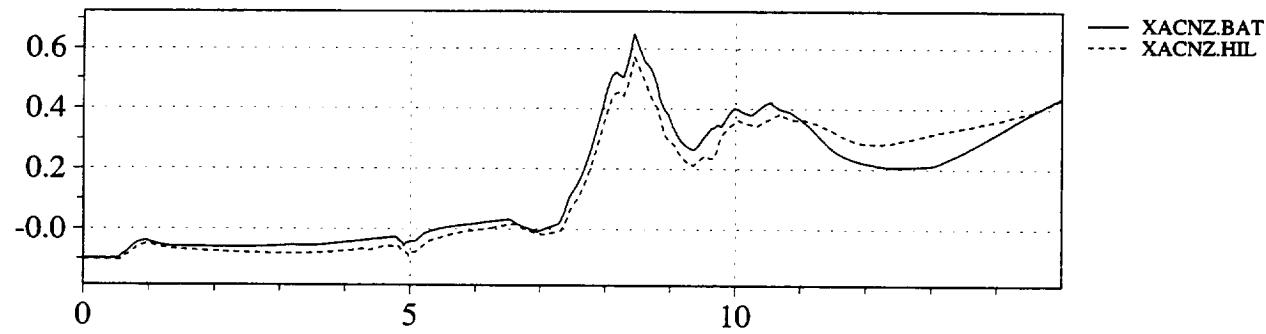
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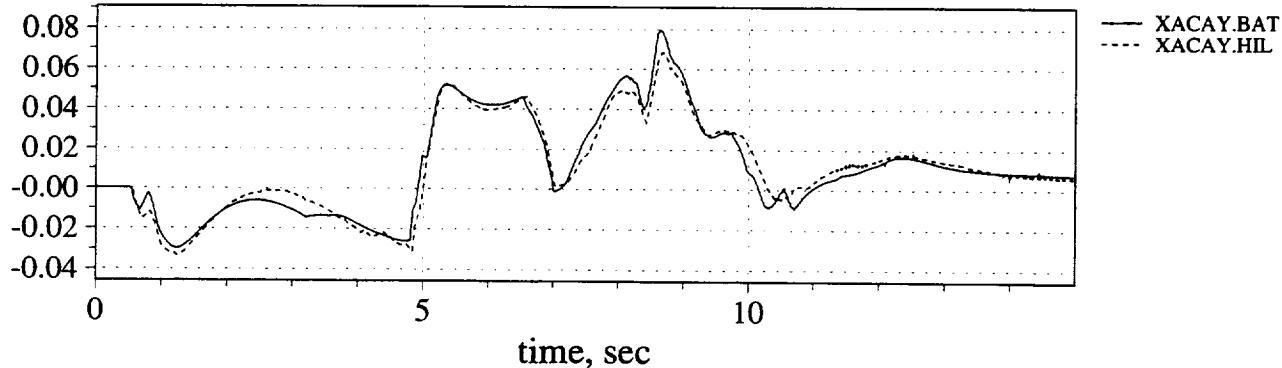
RFCS yaw jet cmd, dg



normal accel, g



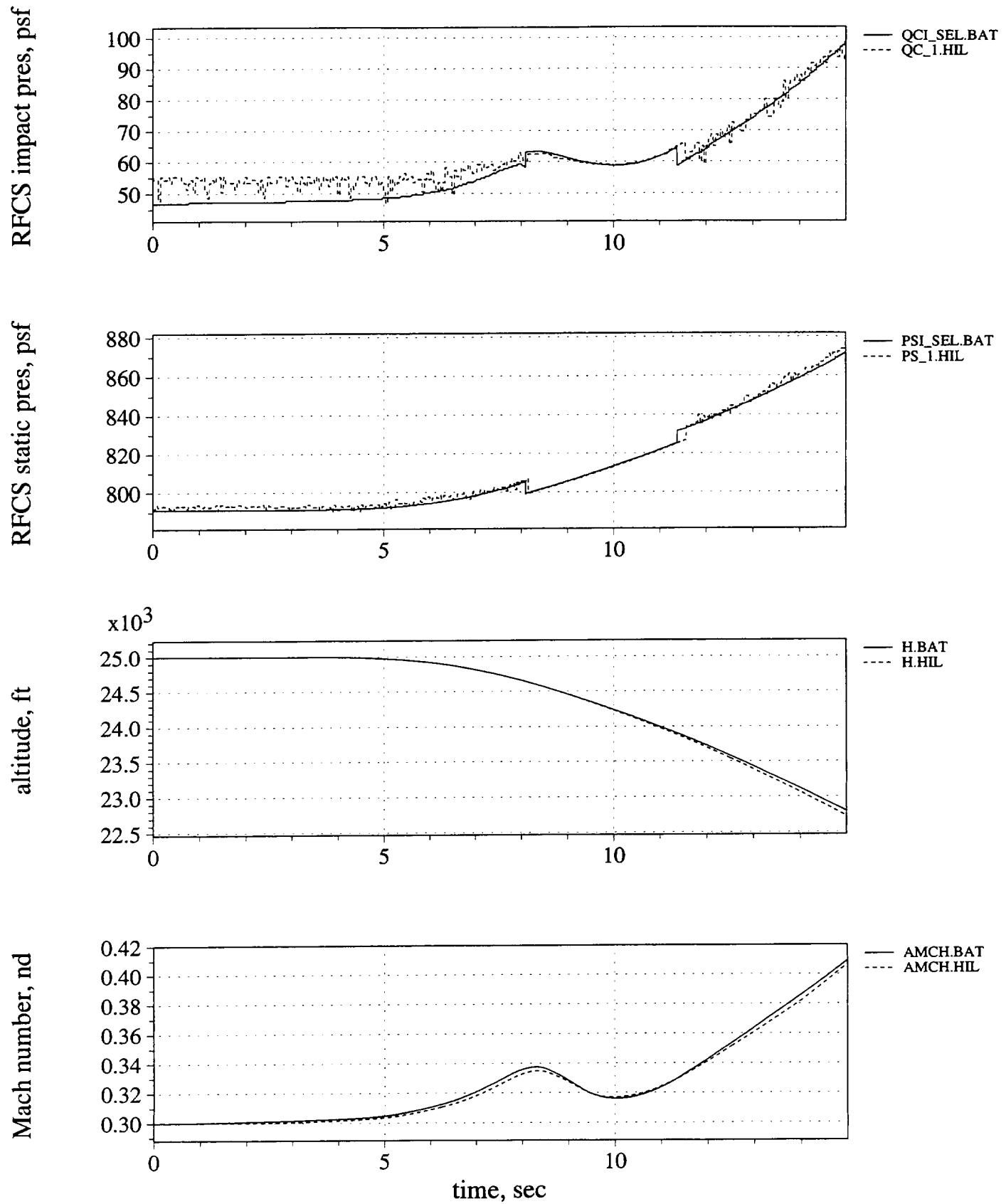
lateral accel, g



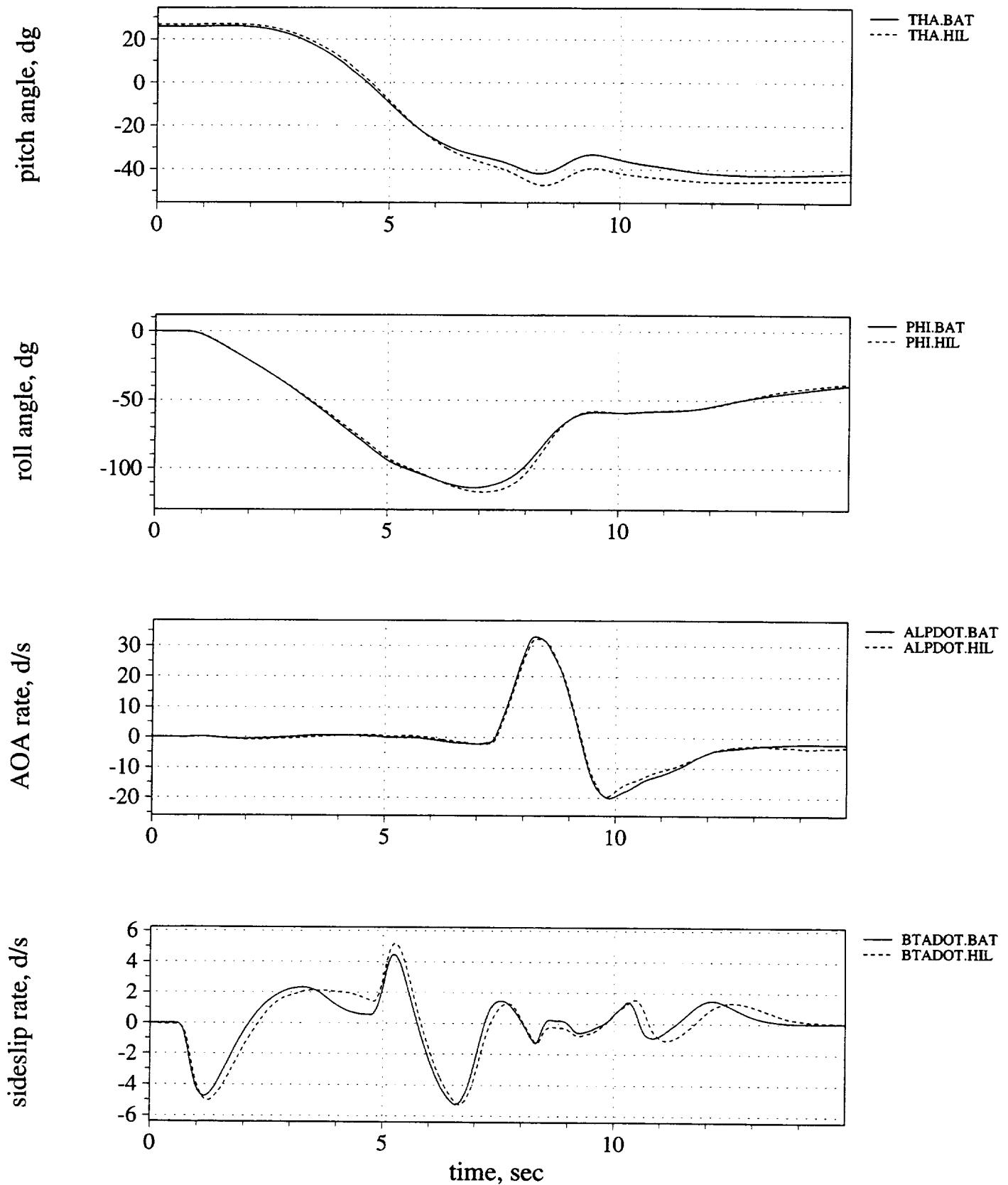
Performance Validation Checkcases

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Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 6/12

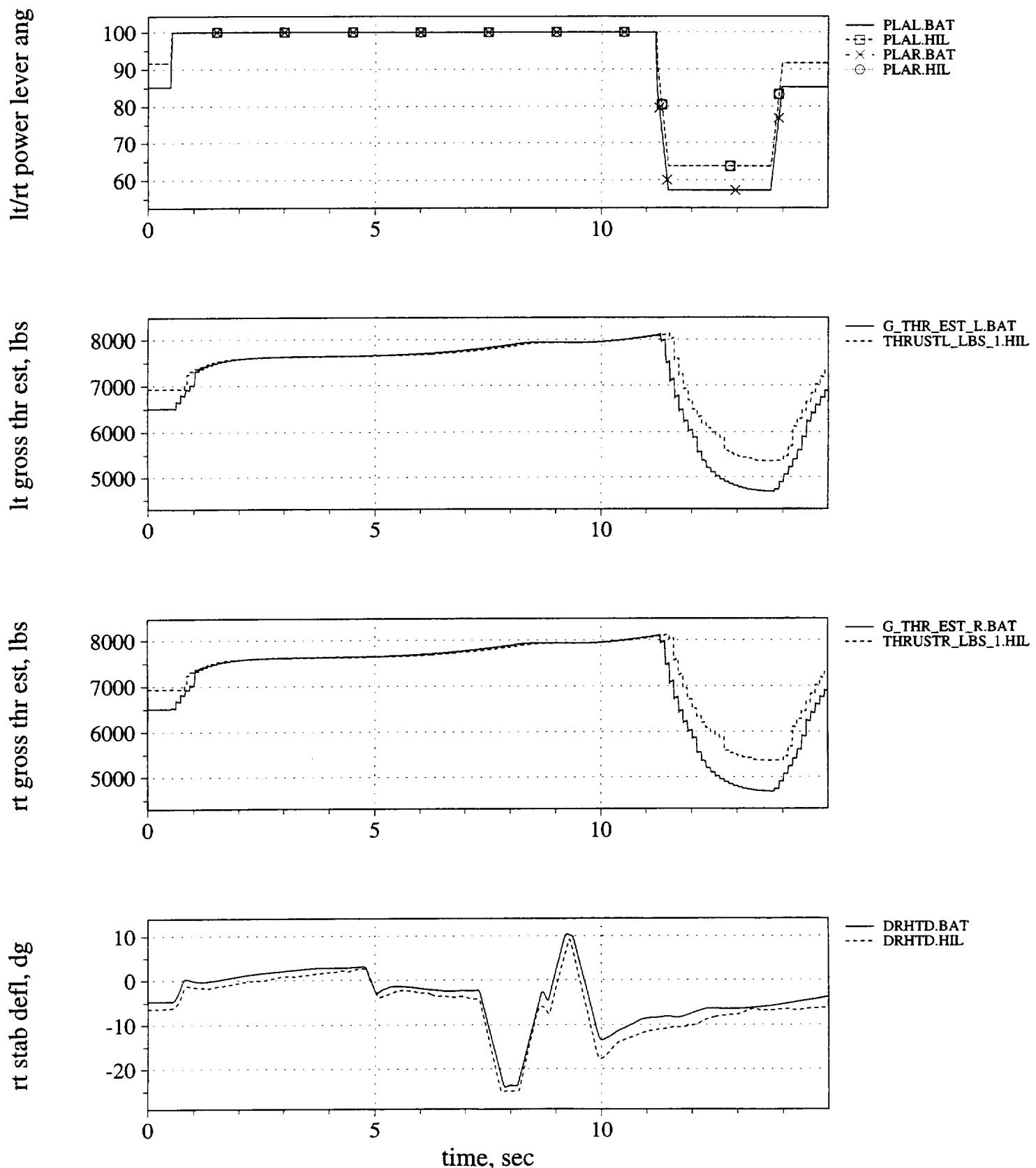


Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 7/12

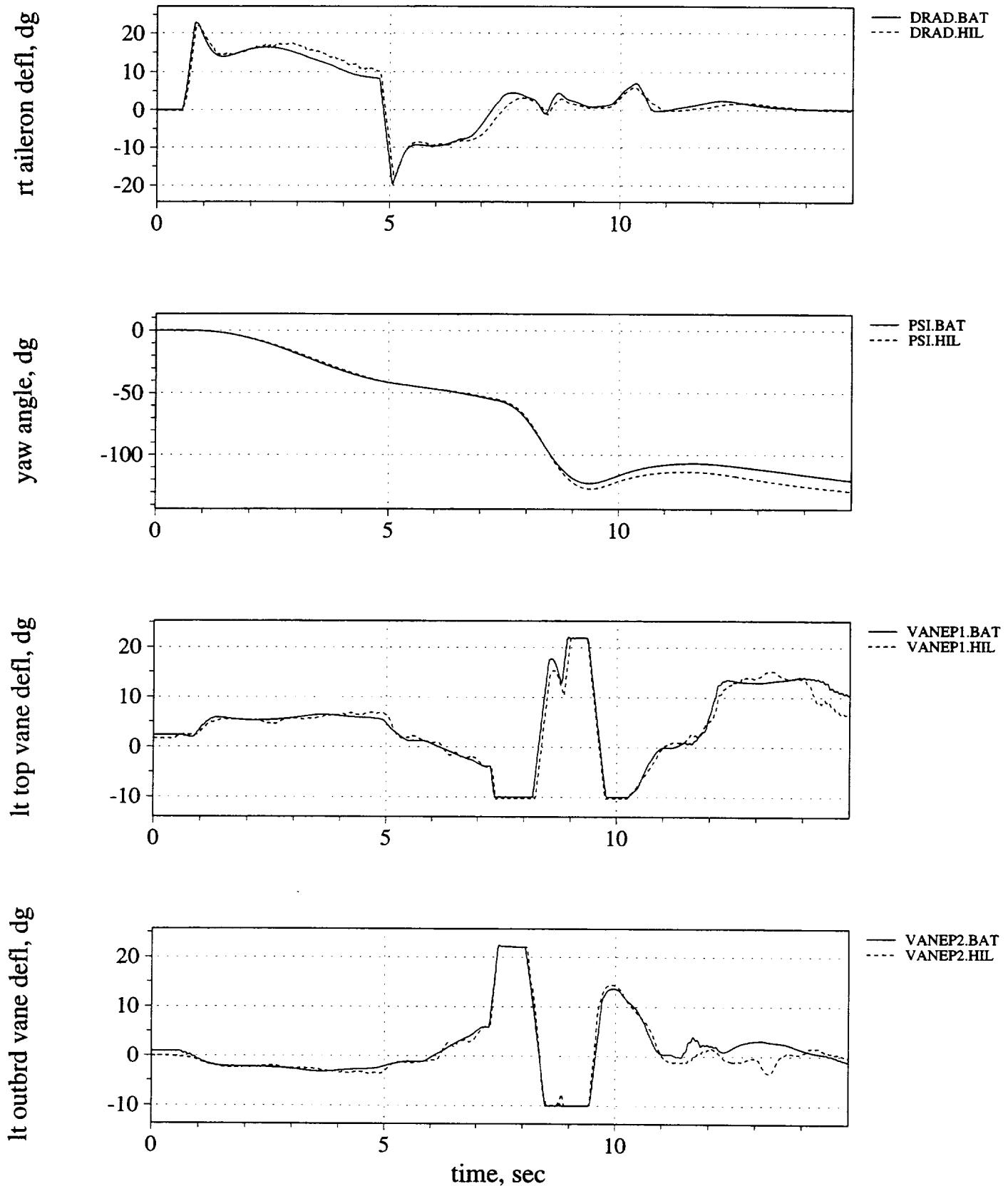


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 8/12



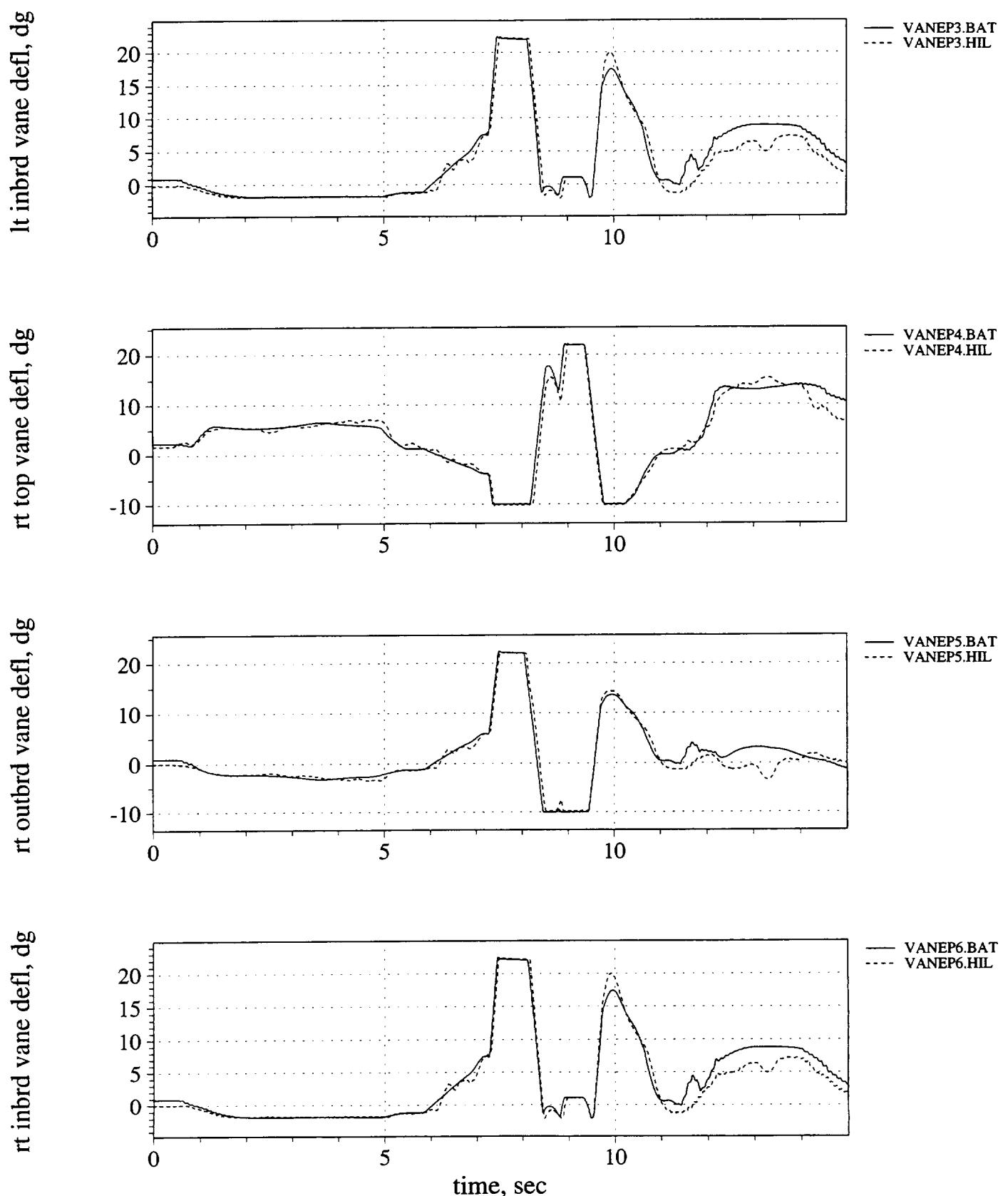
Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 9/12



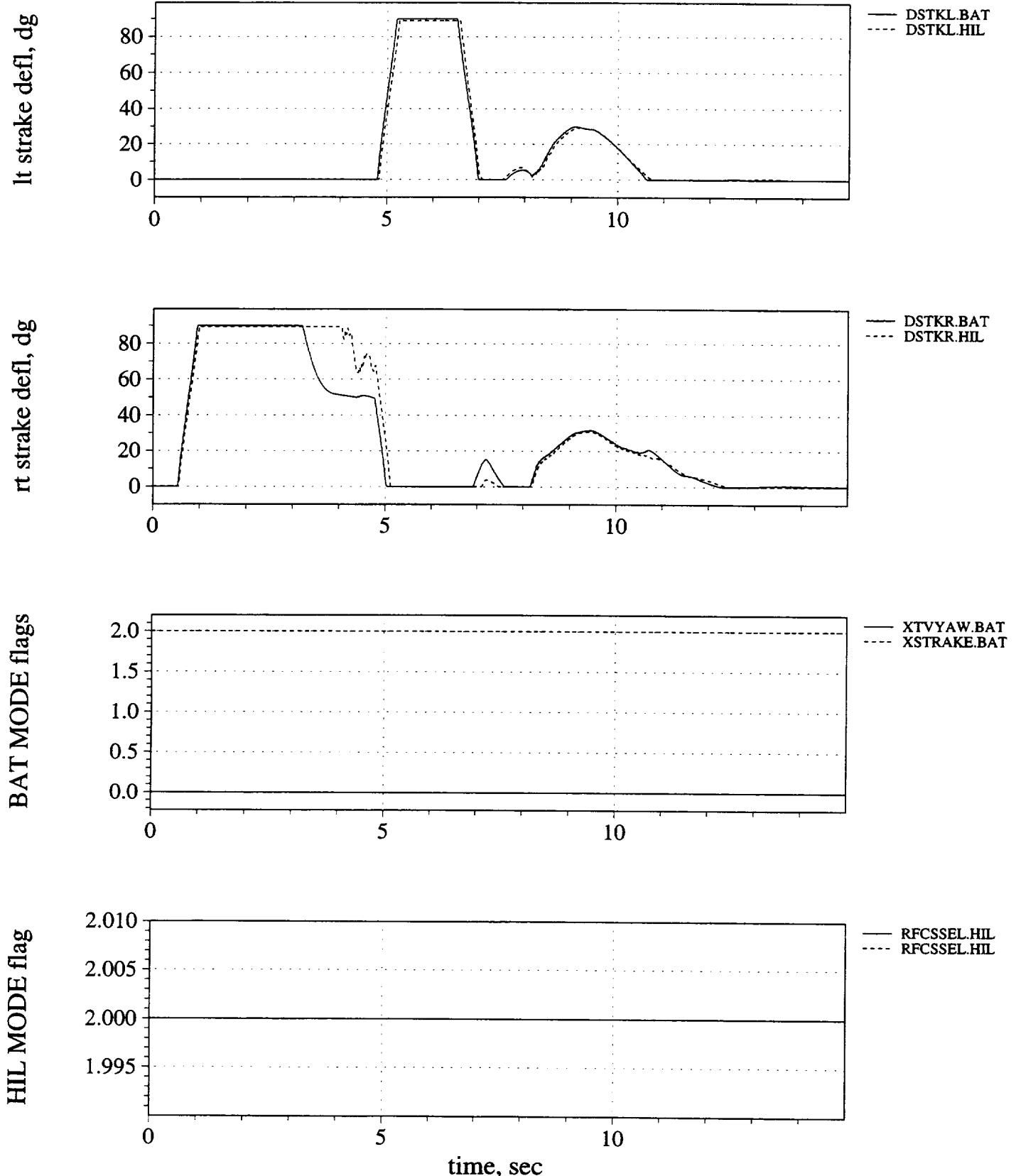
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

CASE_33_S_Mode_.35-25k_trim--3_axis_input page 10/12



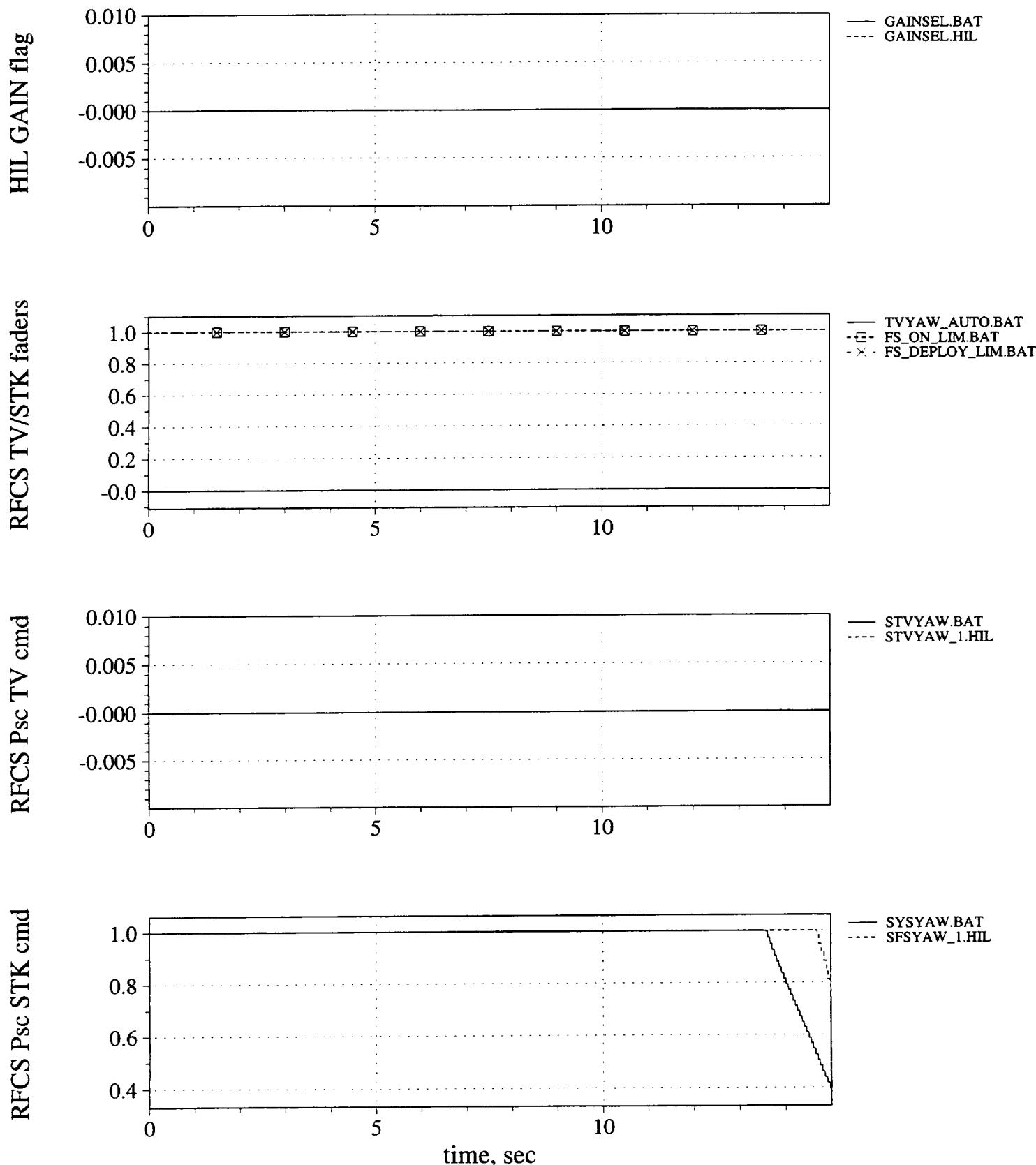
Appendix B - Batch versus HIL Version 154.0
CASE_33_S_Mode_.35-25k_trim--3_axis_input page 11/12



Performance Validation Checkcases

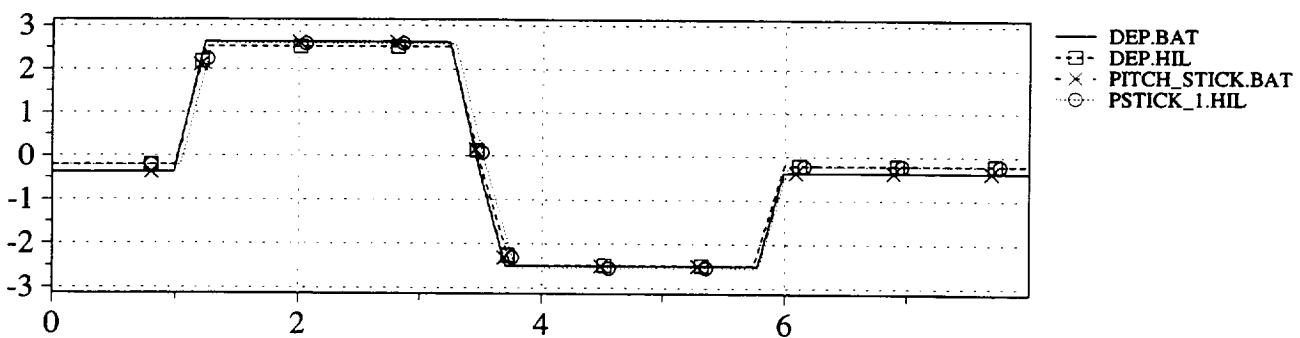
Appendix B - Batch versus HIL Version 154.0

CASE_33_S_Mode_.35-25k_trim--3_axis_input page 12/12

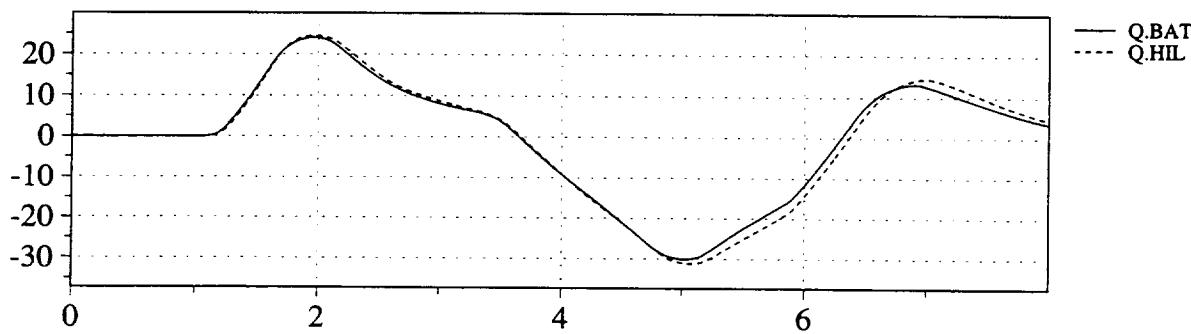


Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 1/12

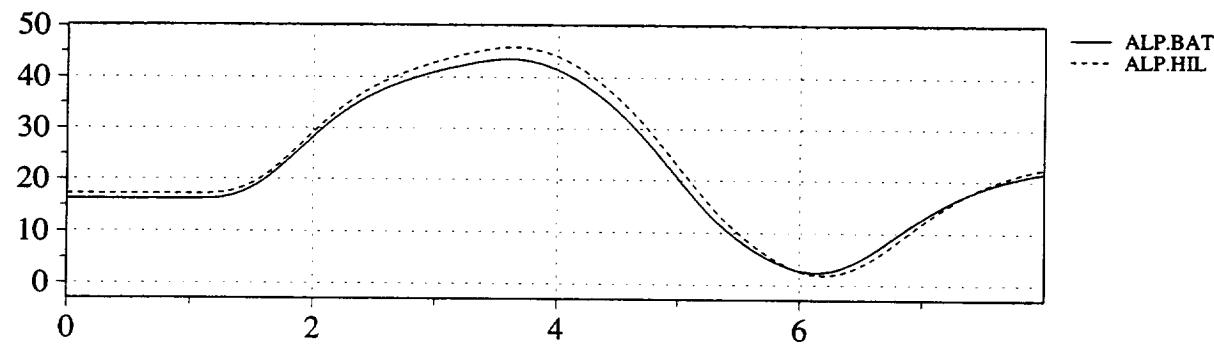
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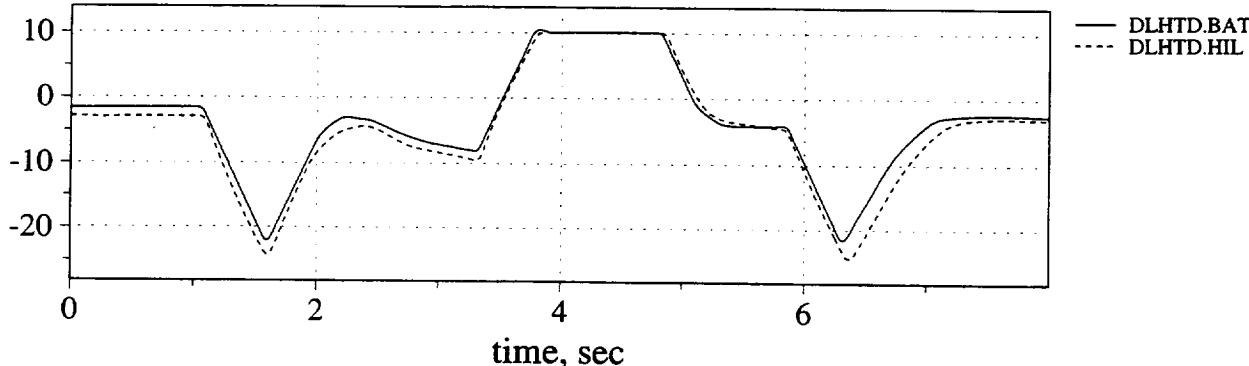
pitch rate, d/s



Angle-of-attack, dg

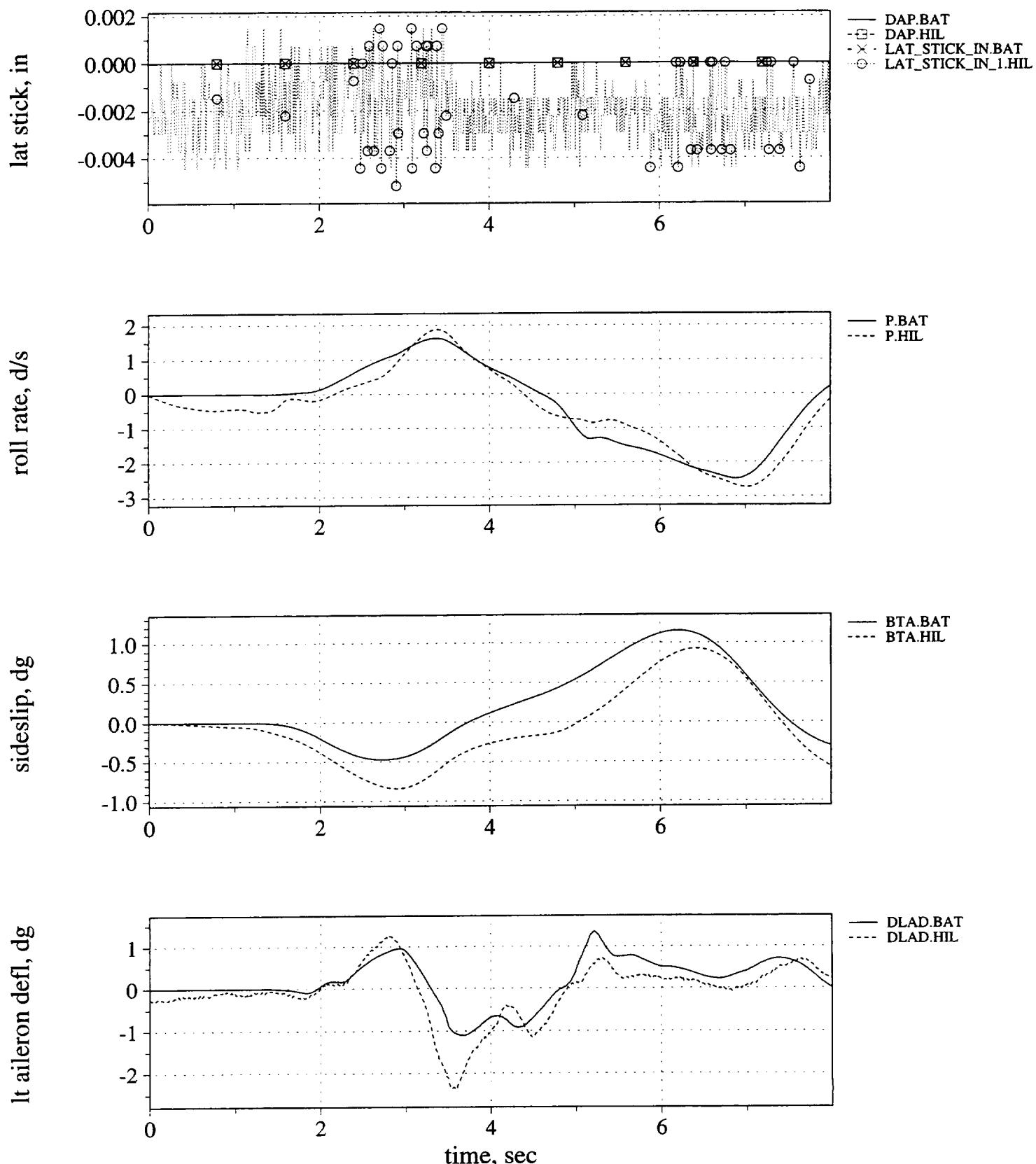


lt stab defl, dg



Performance Validation Checkcases

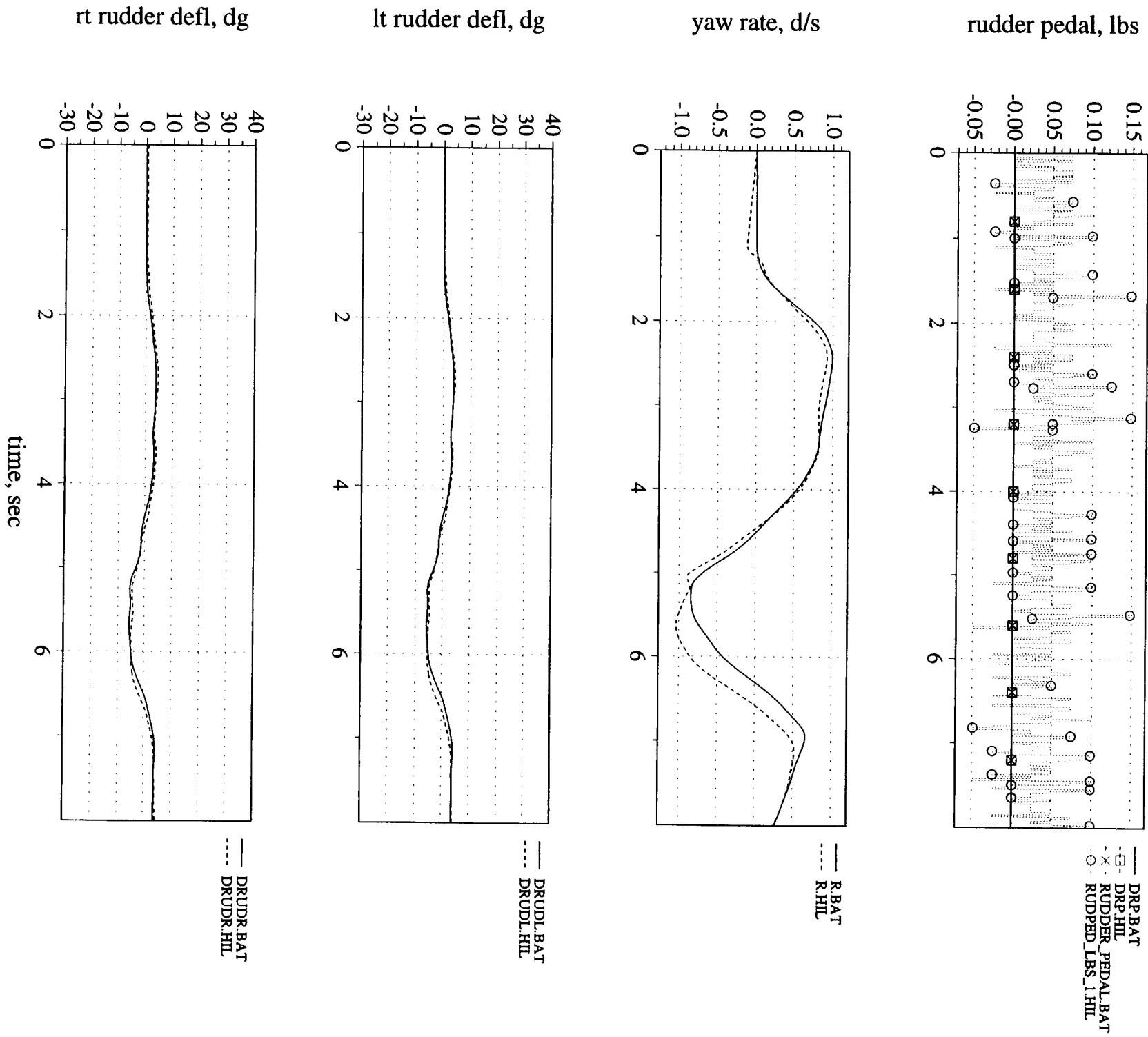
Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 2/12



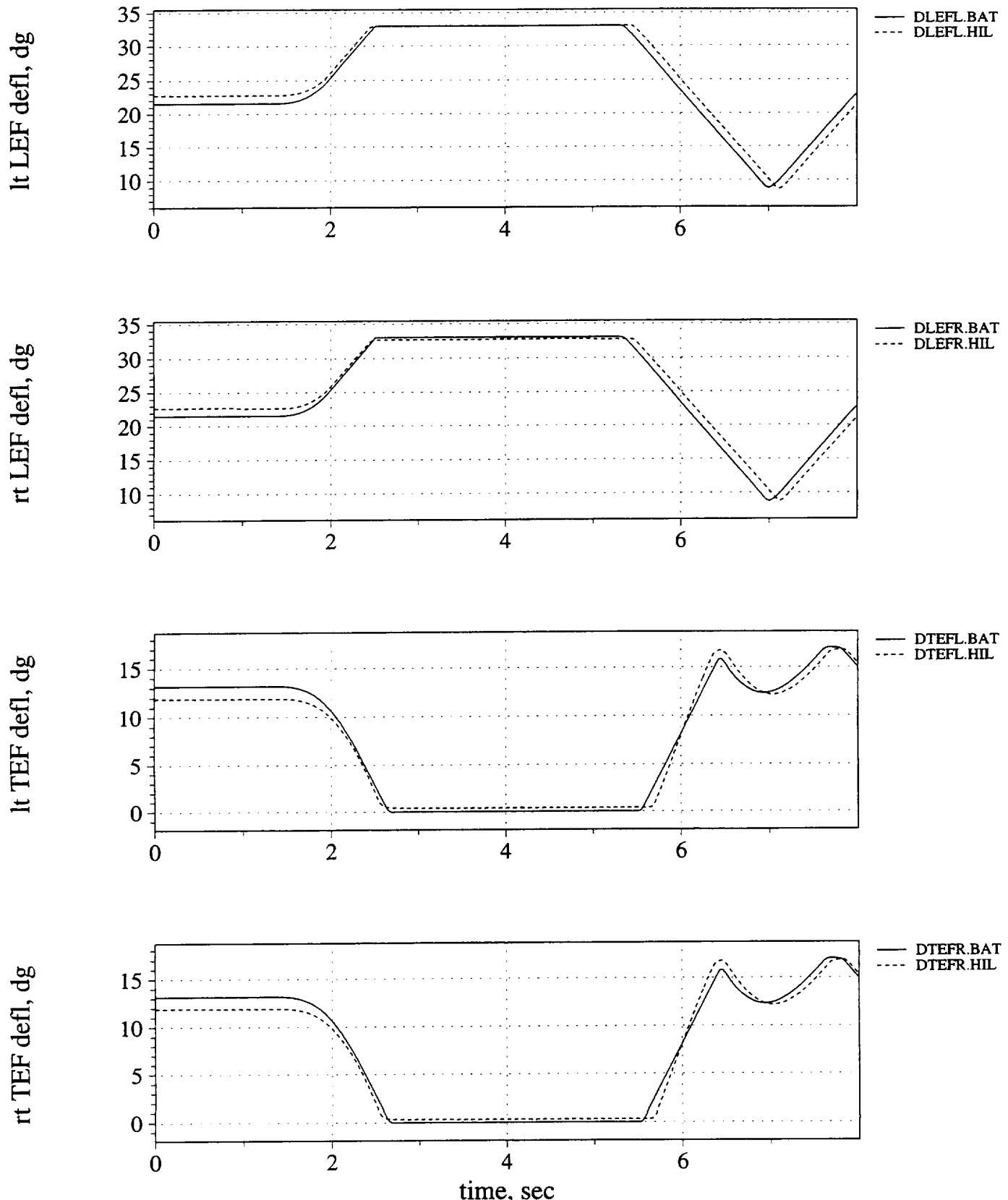
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 3/12



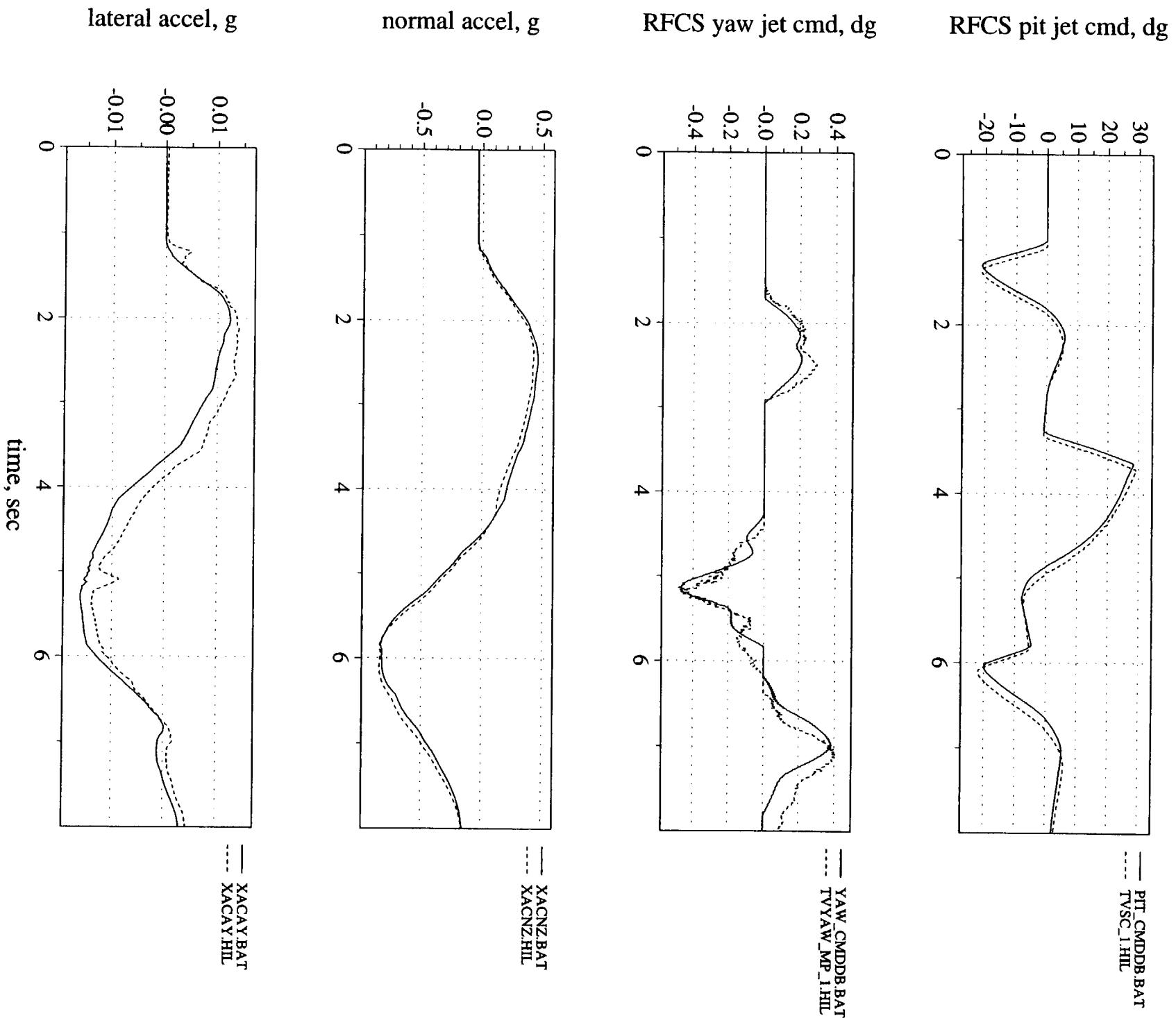
Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 4/12



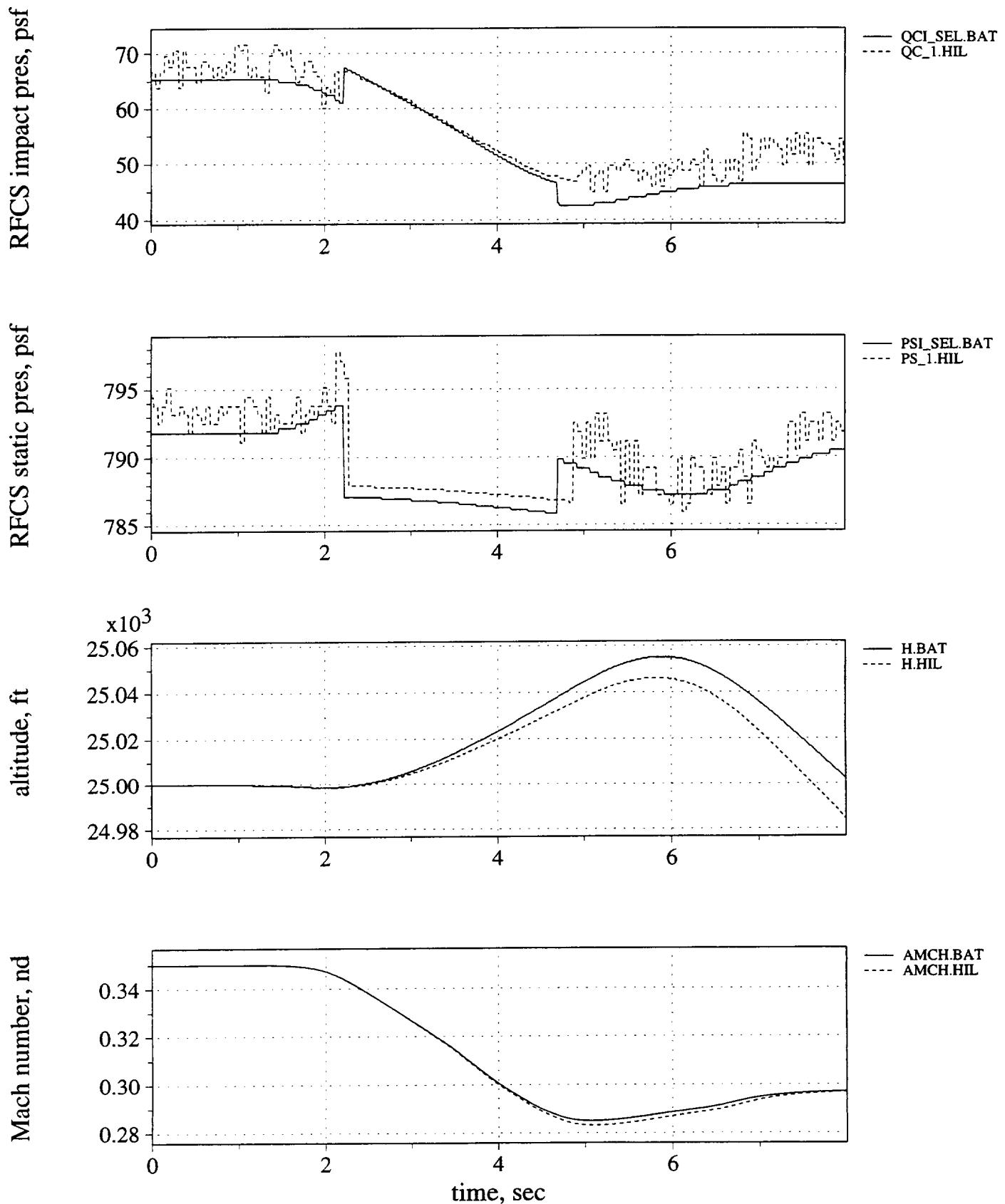
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 5/12

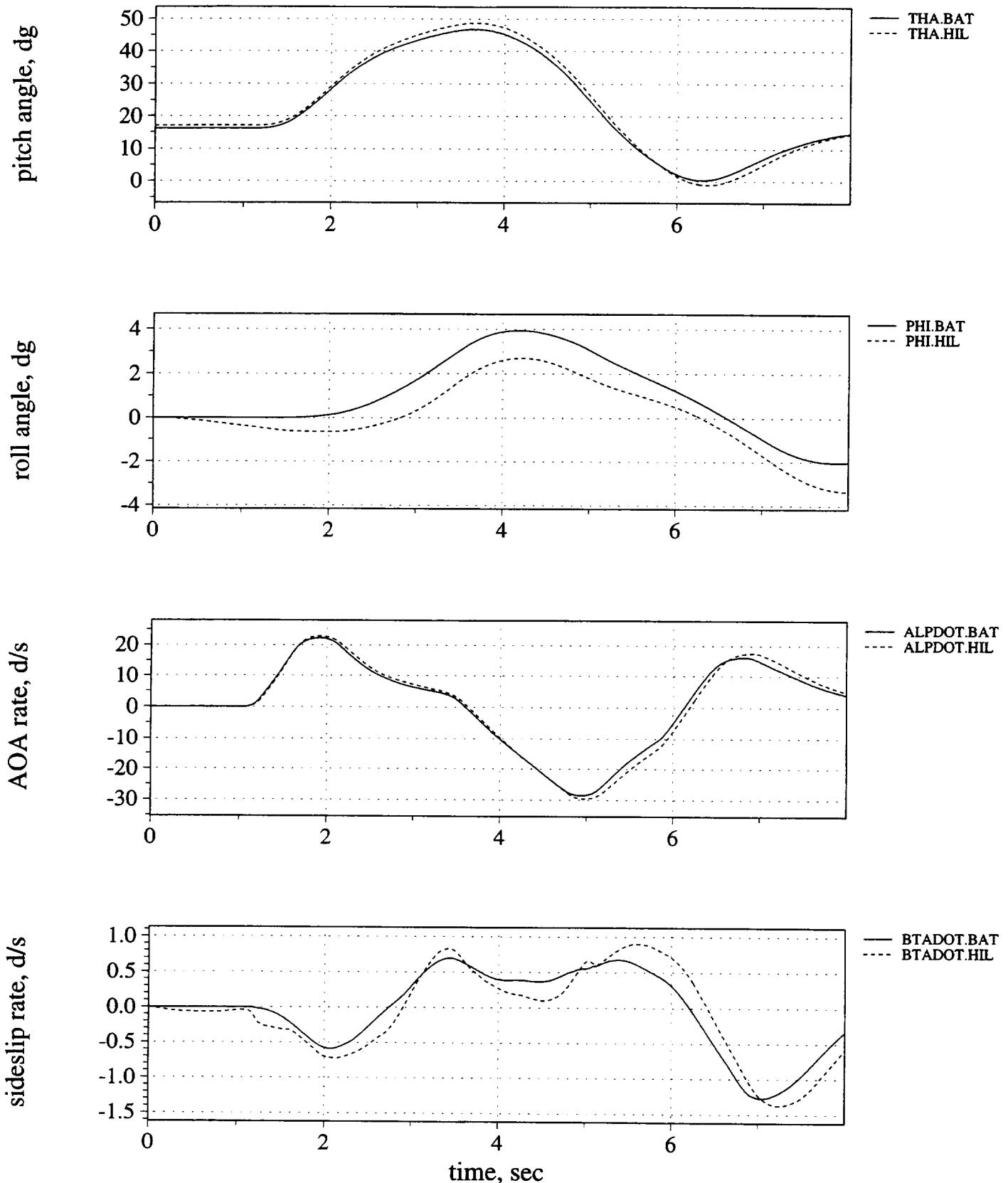


Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 6/12



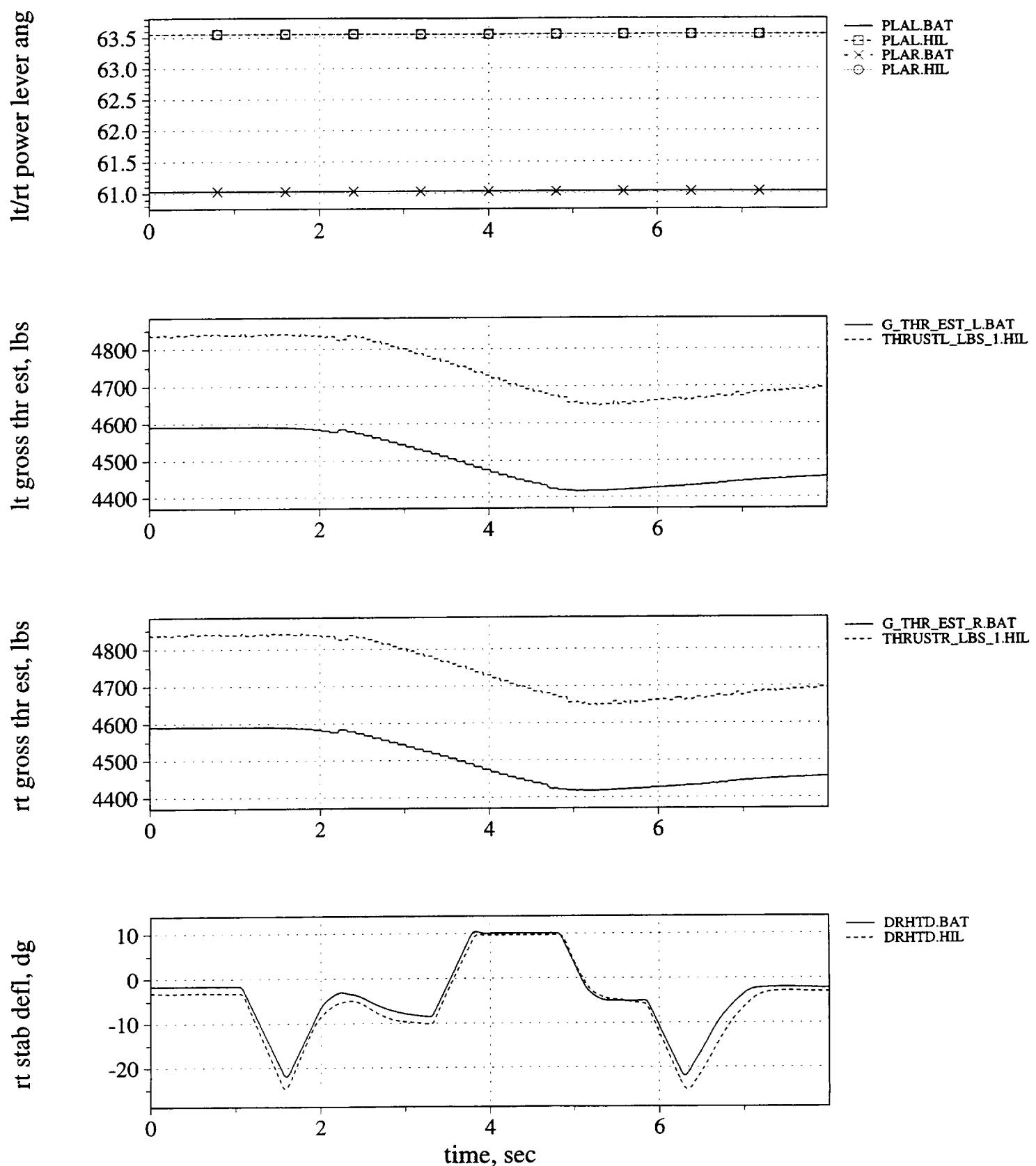
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 7/12

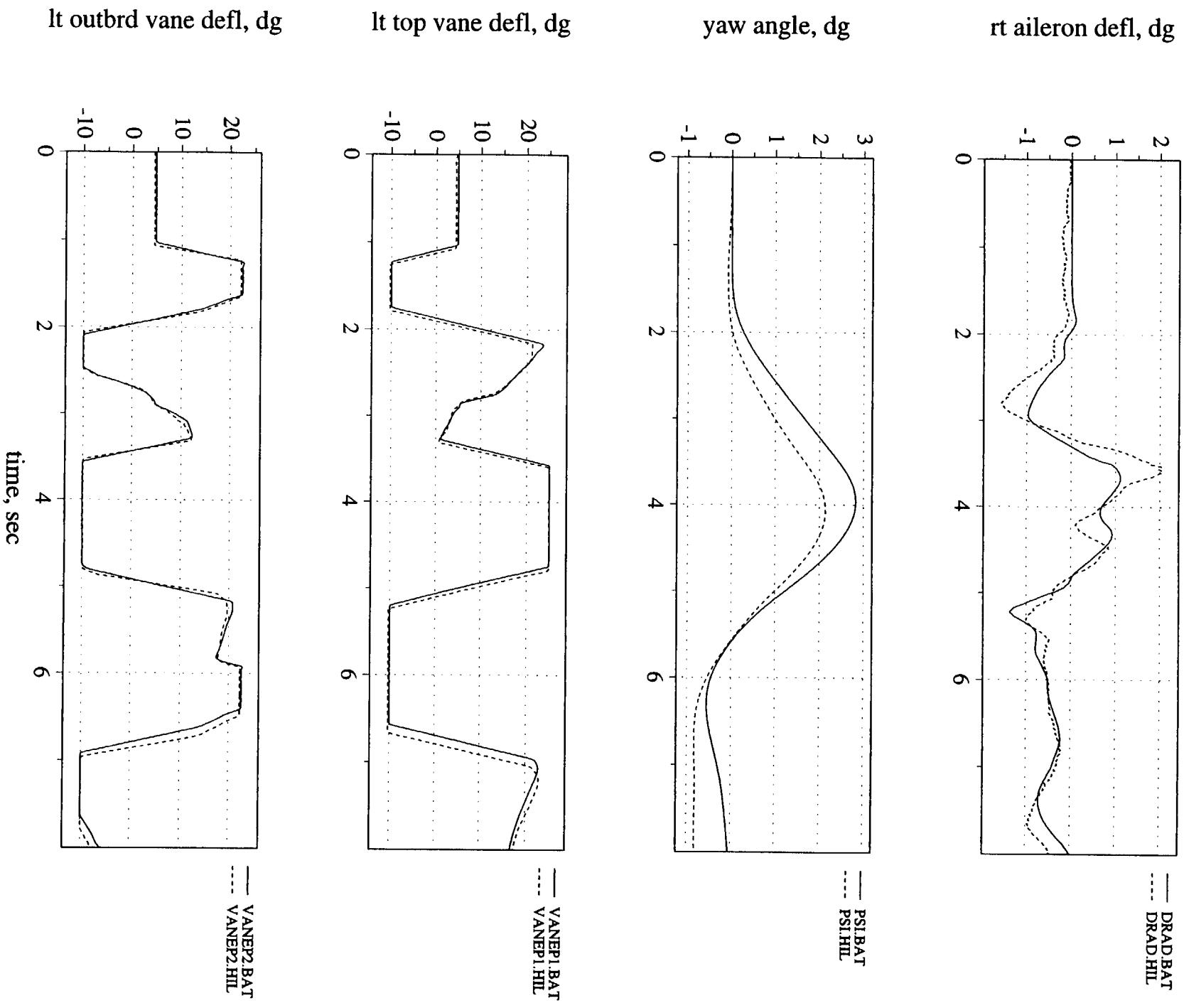


Performance Validation Checkcases

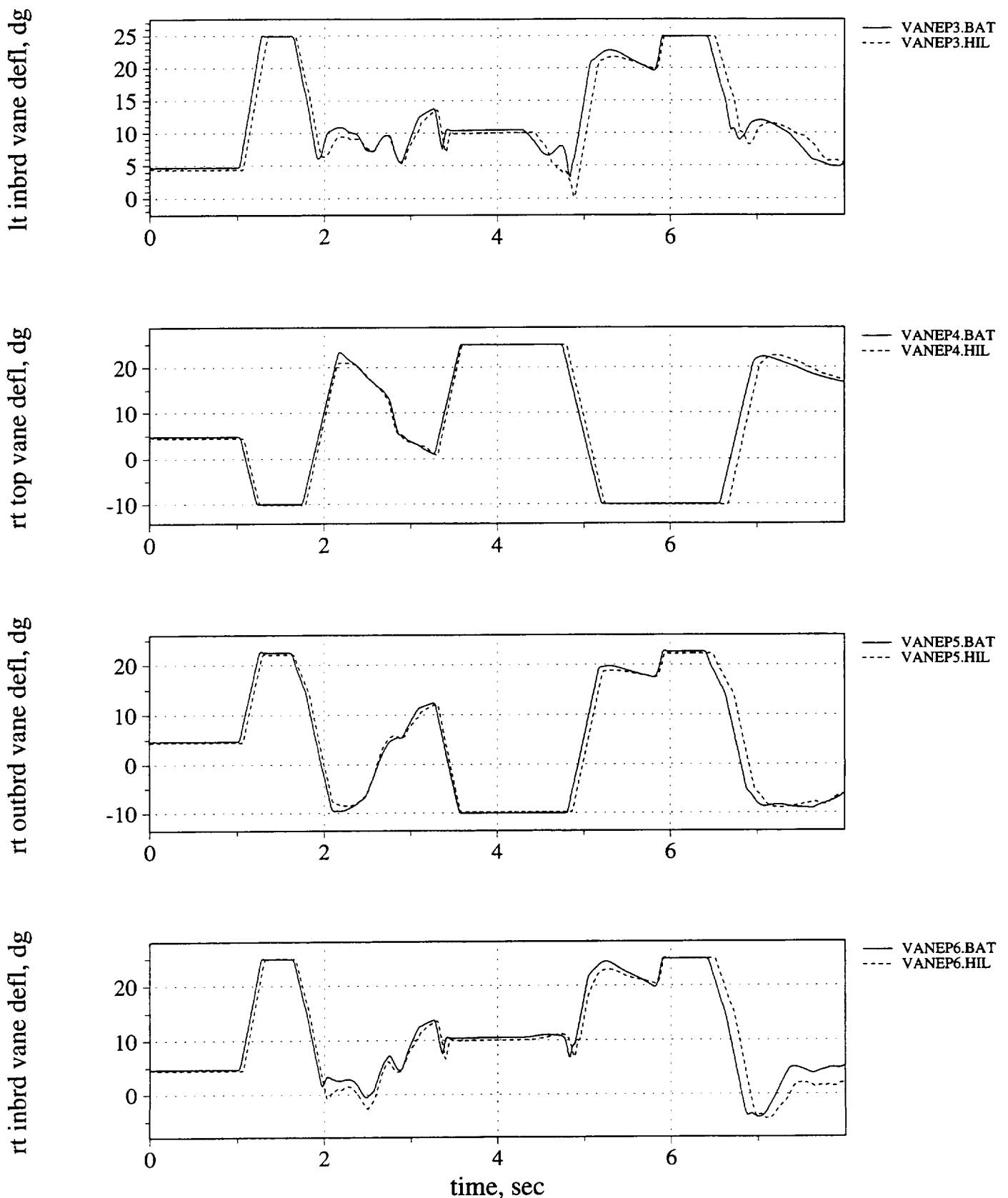
Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 8/12



Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 9/12

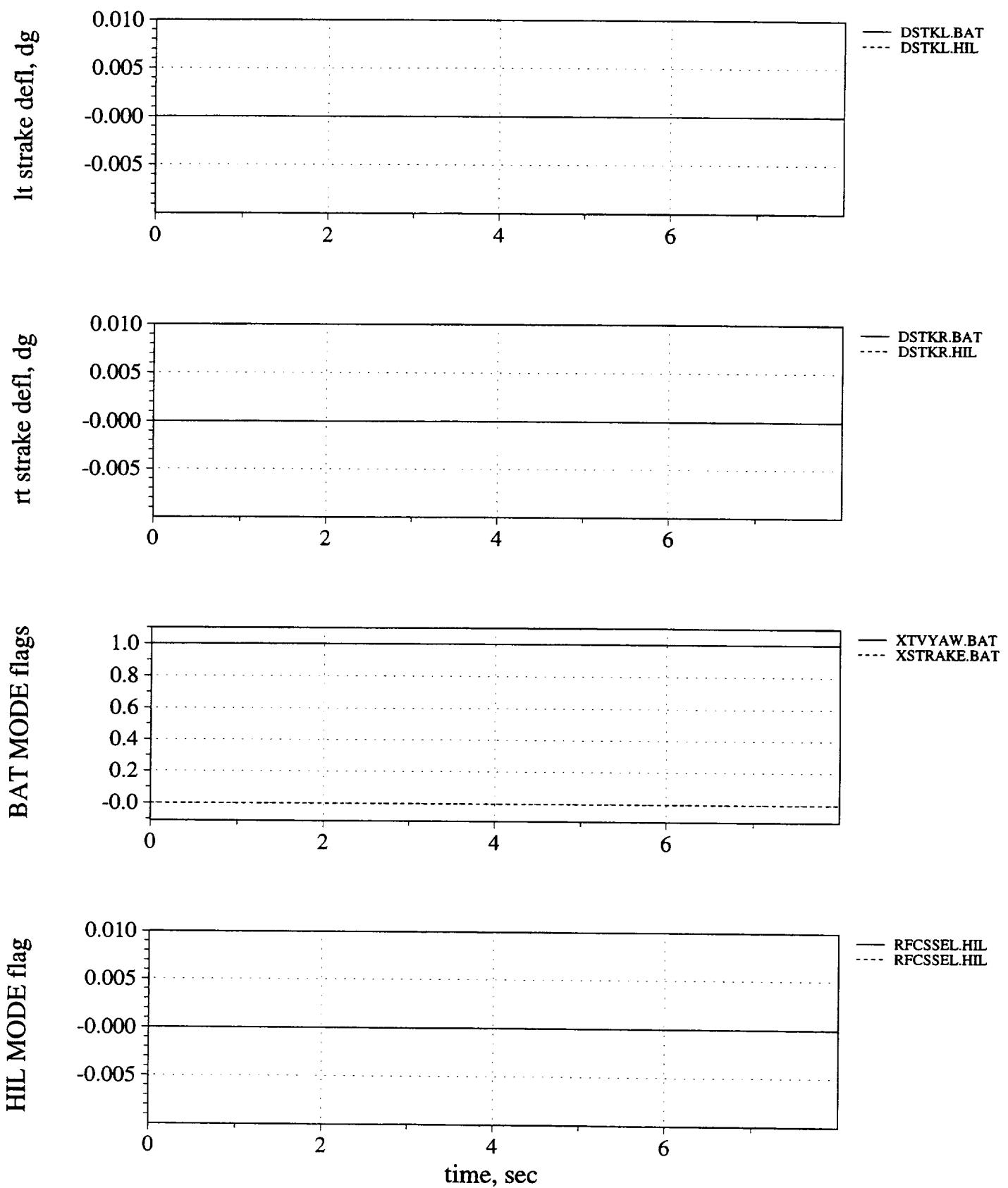


Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 10/12



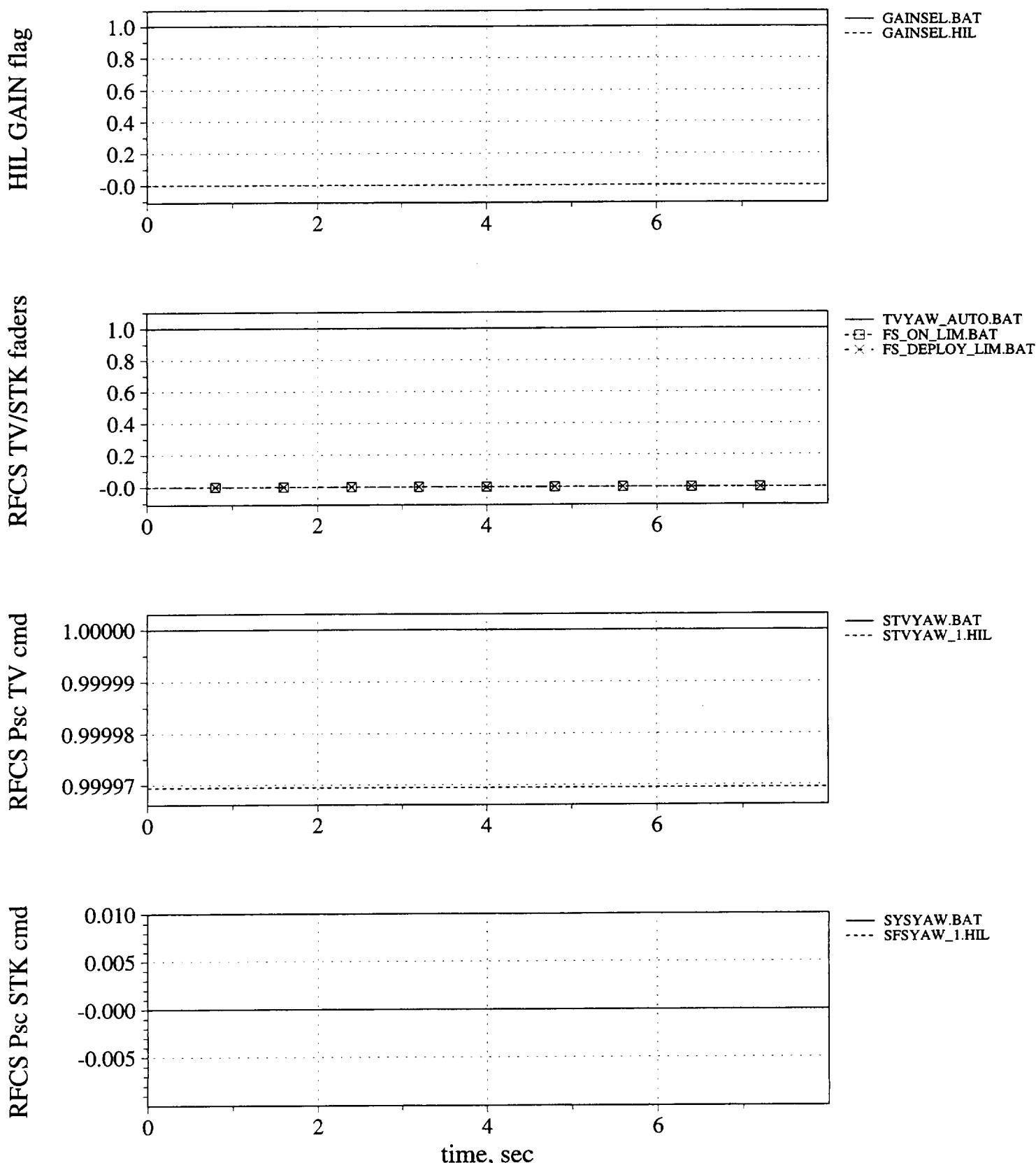
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 11/12



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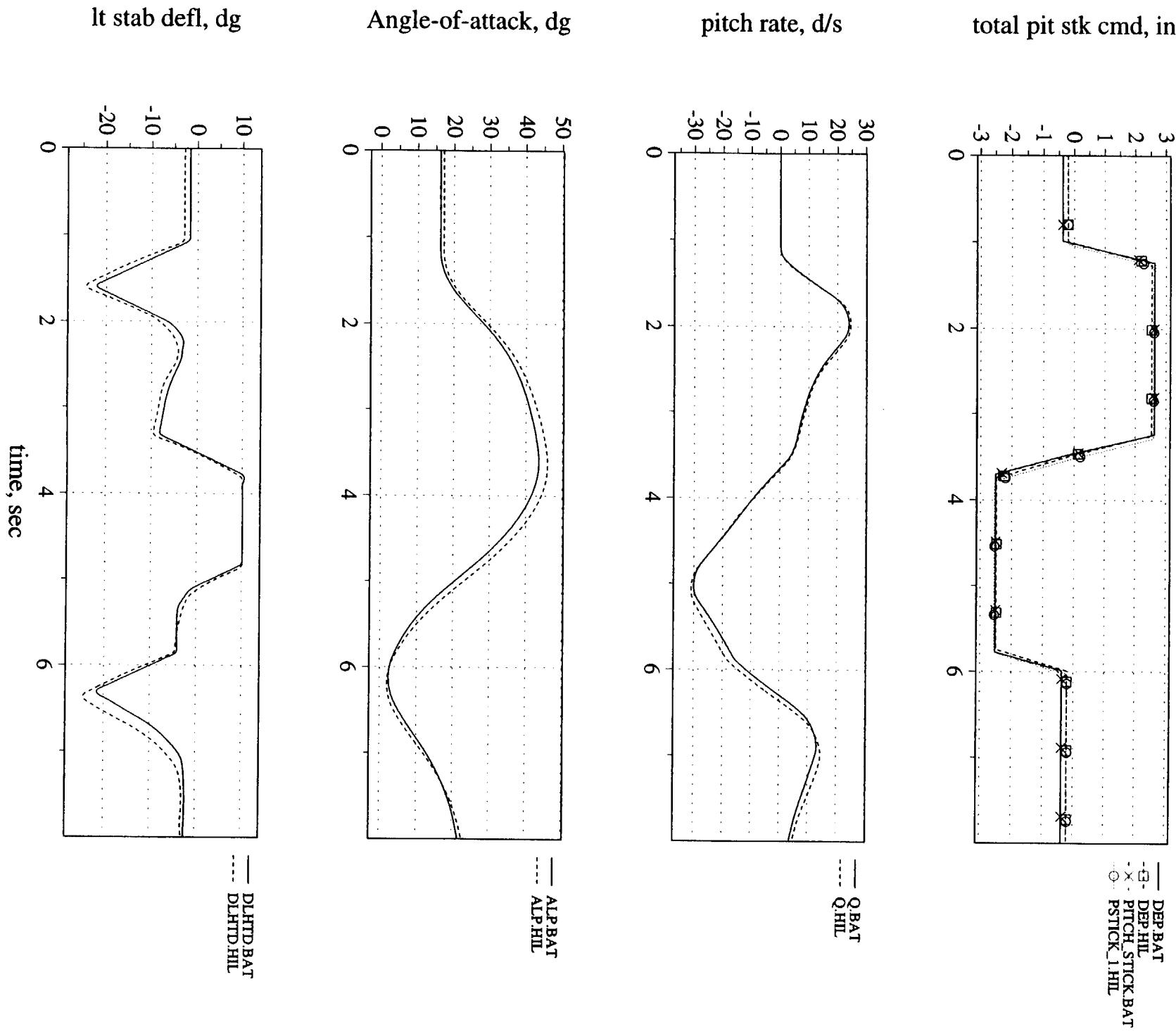
Appendix B - Batch versus HIL Version 154.0
CASE_41_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_1 page 12/12



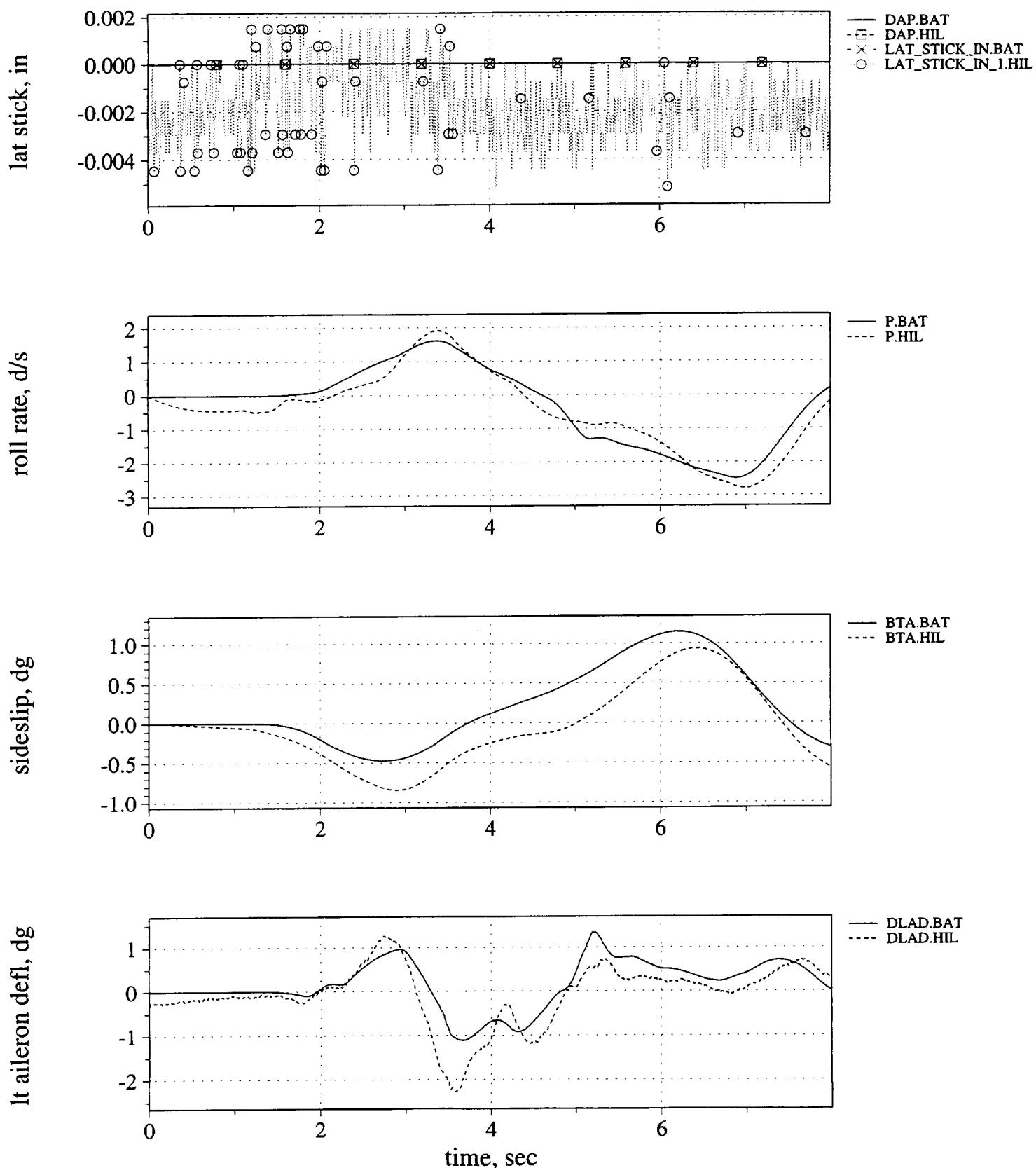
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

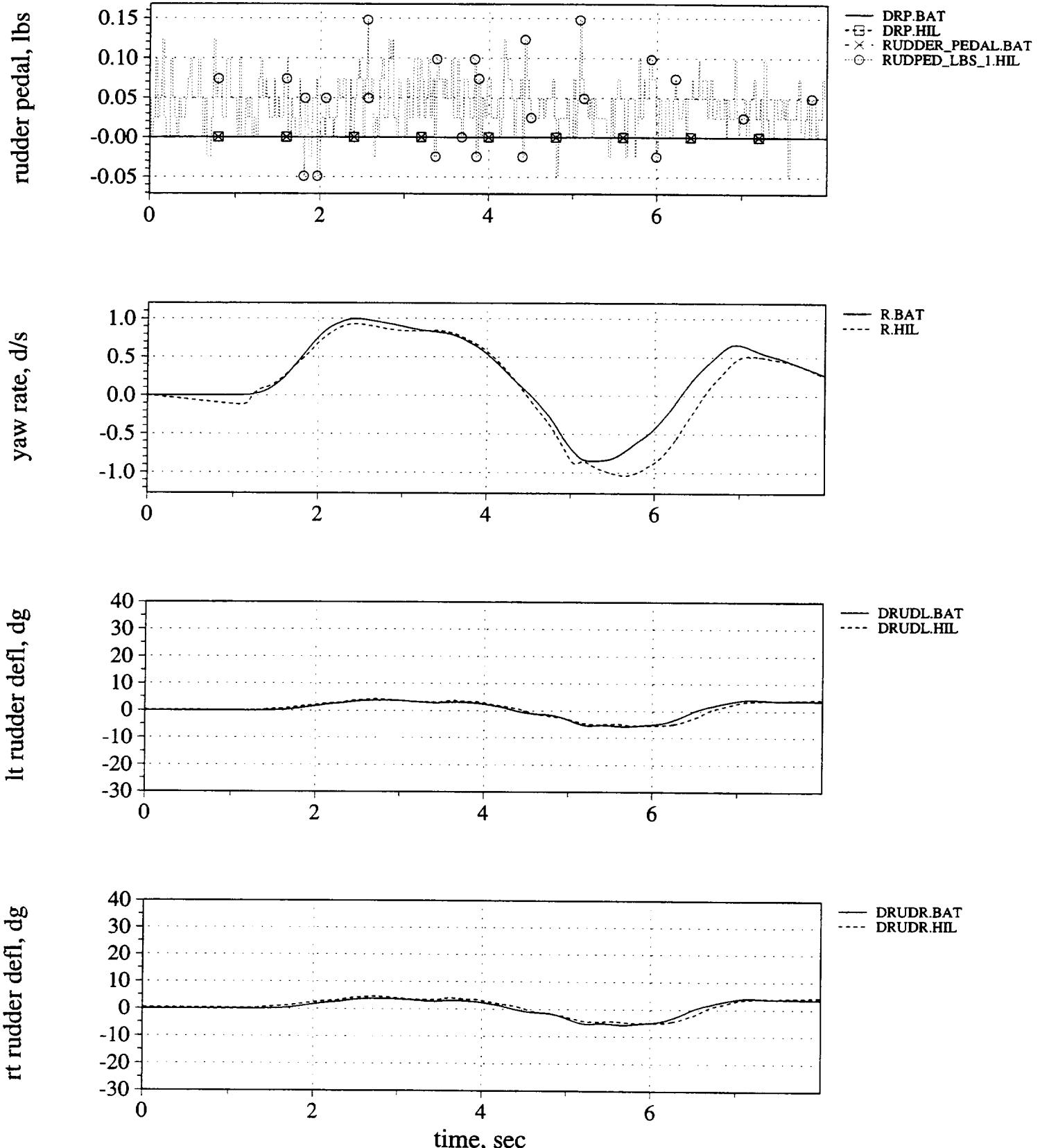
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 1/12



Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 2/12

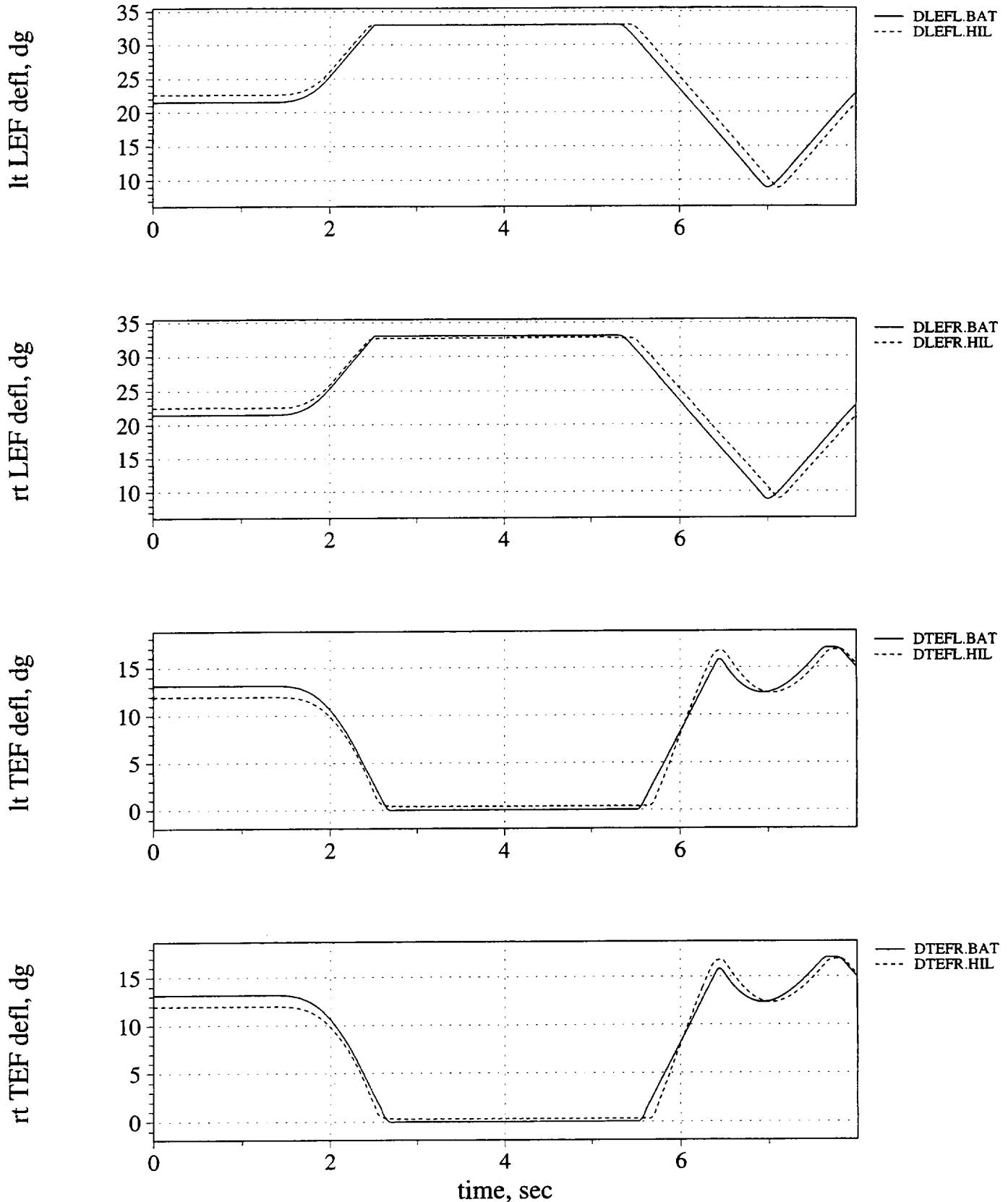


Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 3/12



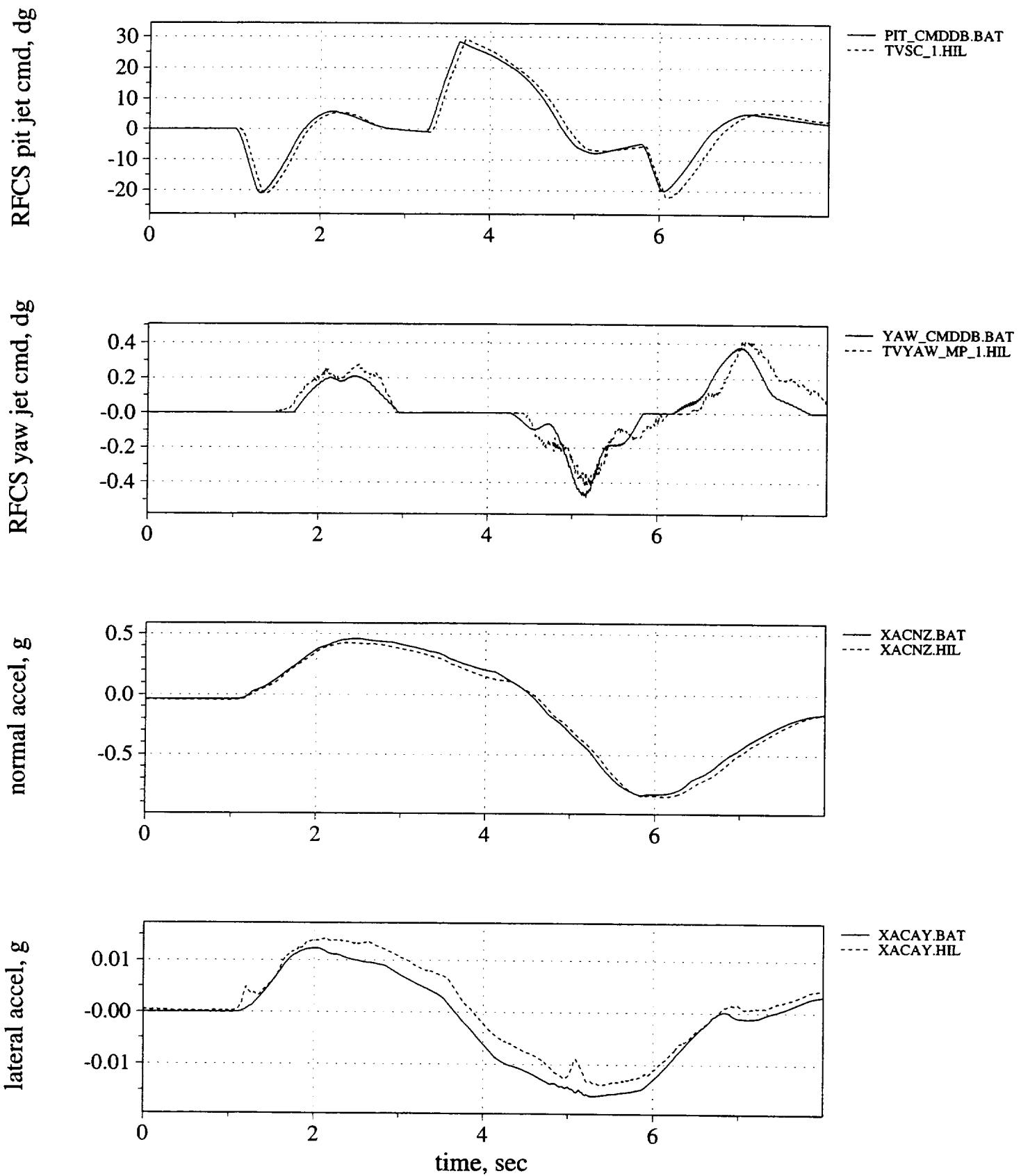
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 4/12



Performance Validation Checkcases

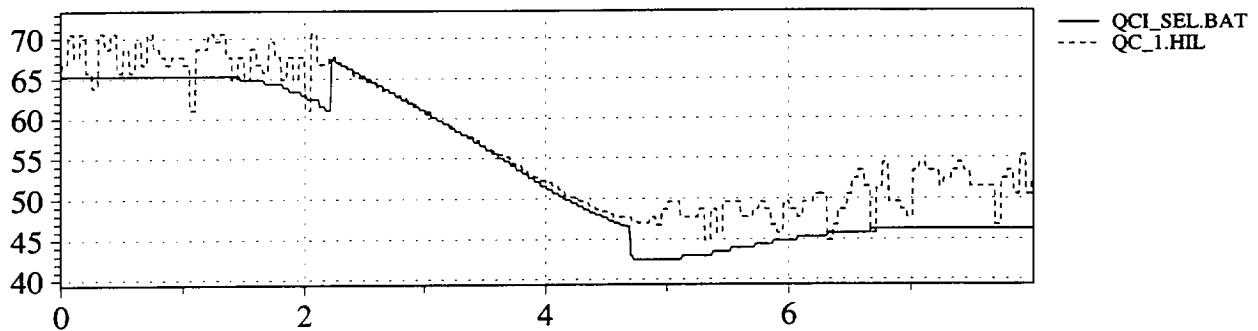
Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 5/12



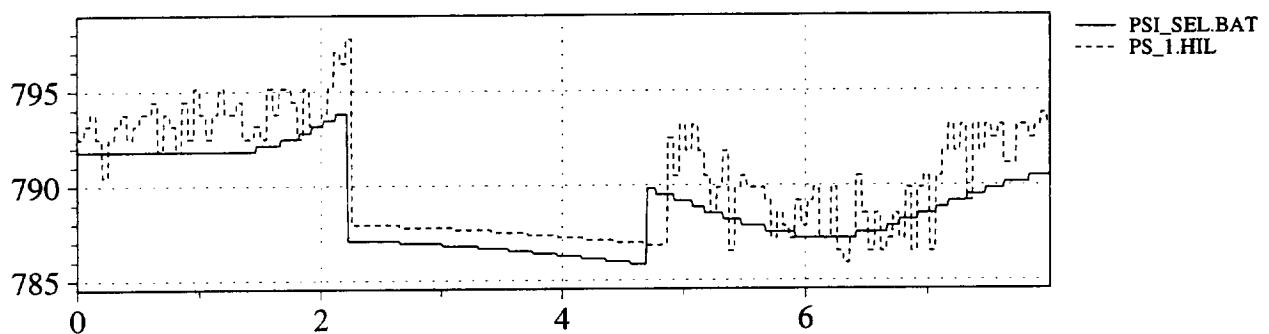
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 6/12

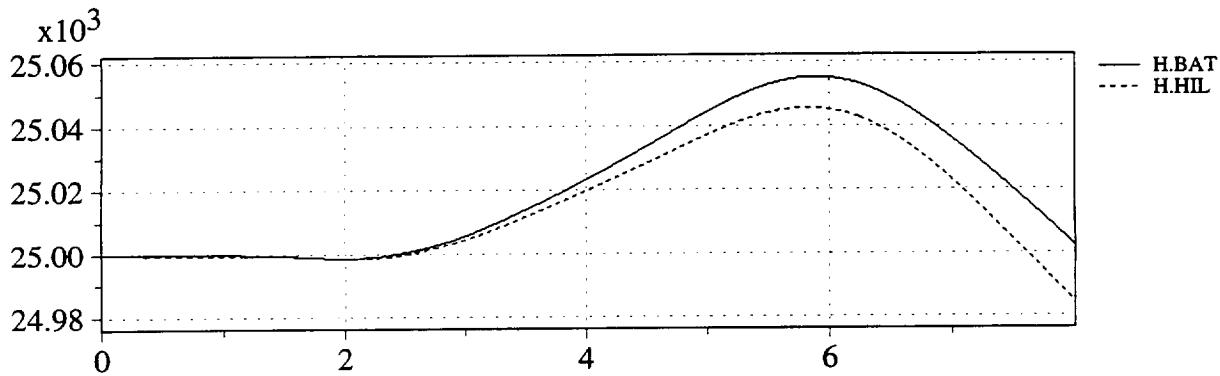
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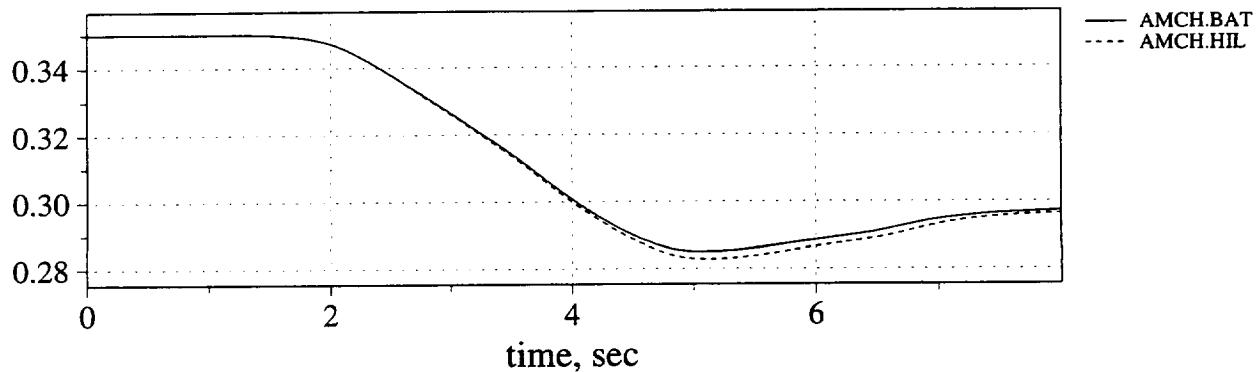
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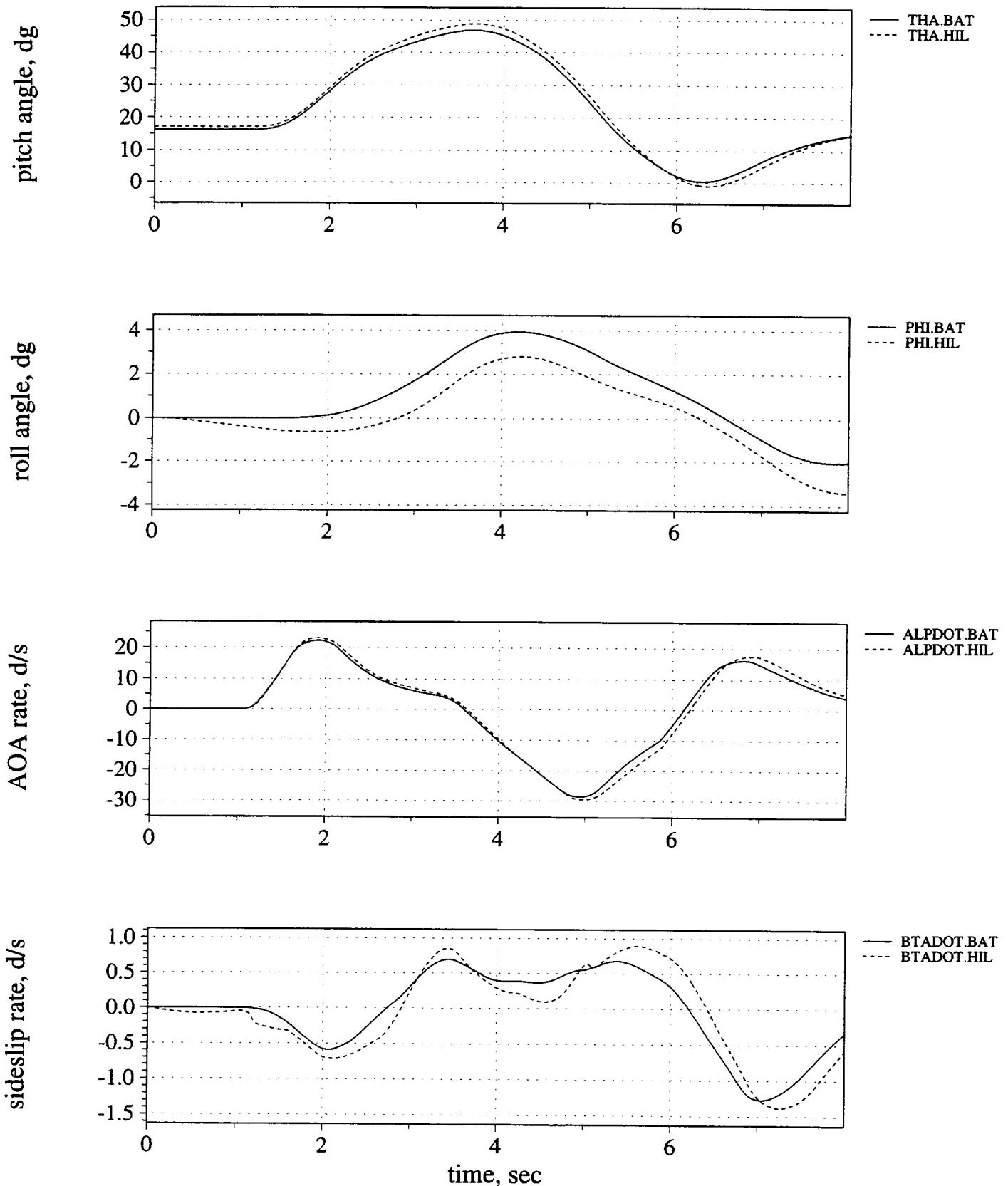
altitude, ft



Mach number, nd

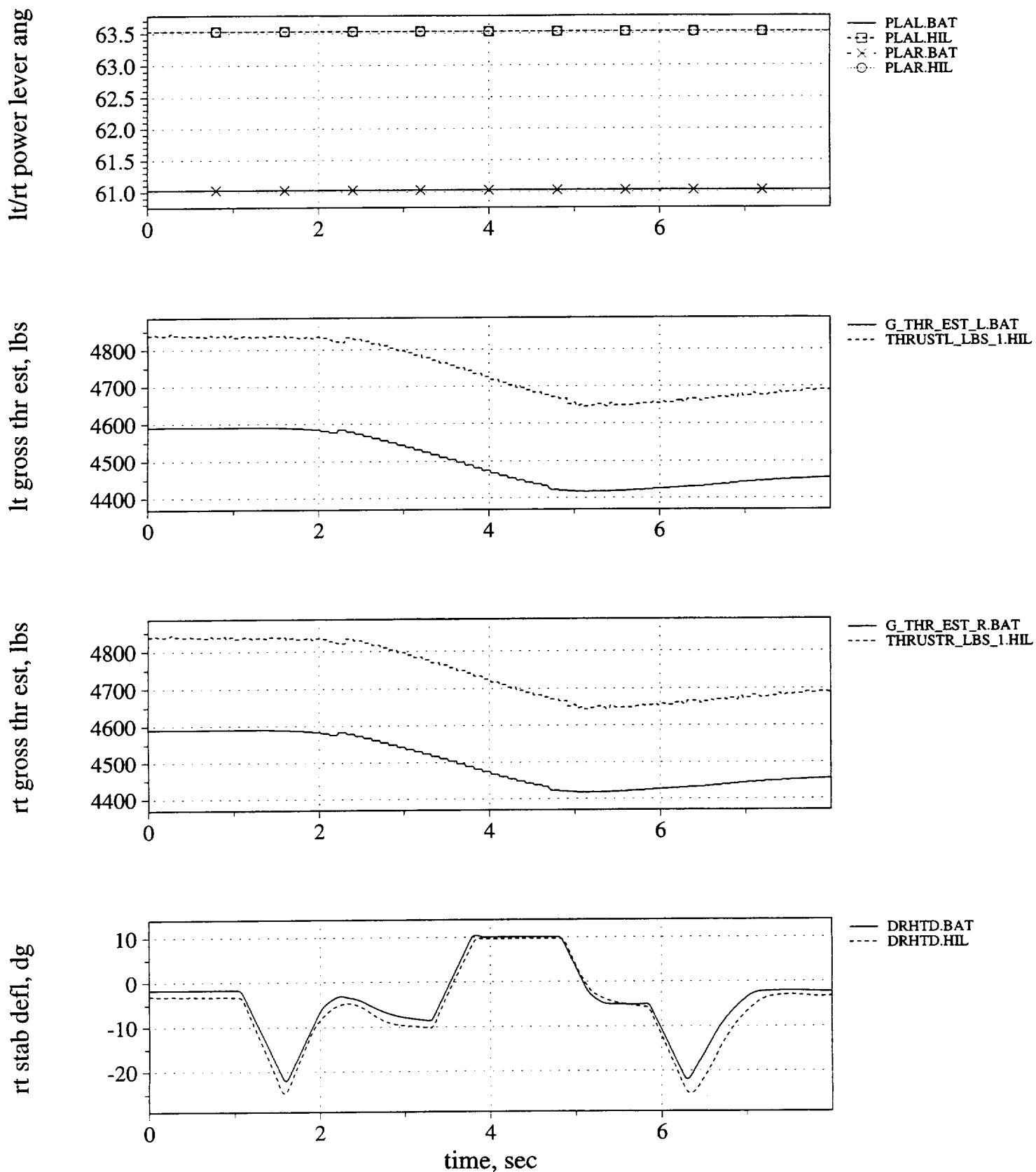


Appendix B - Batch versus HIL Version 154.0
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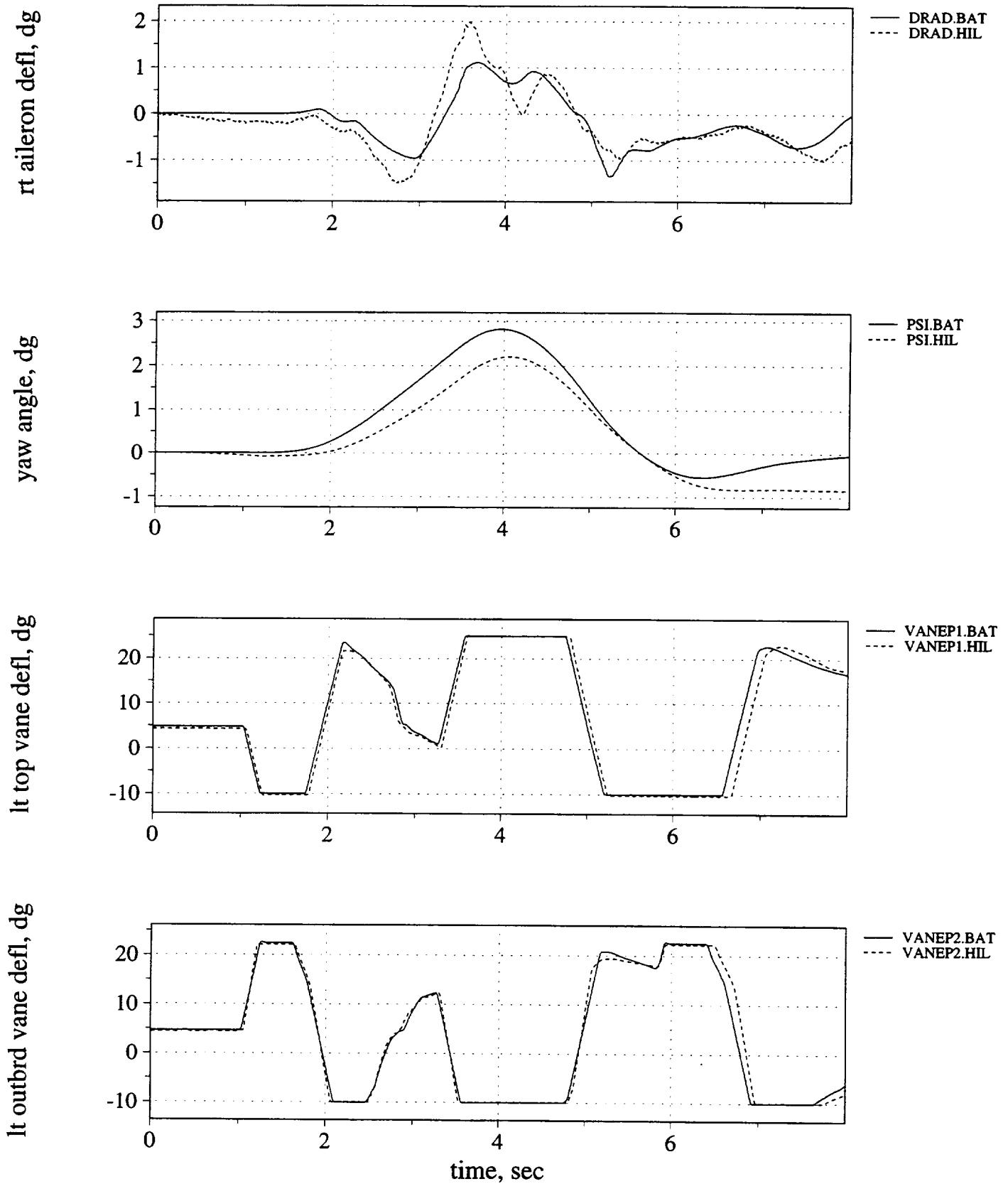


Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 8/12



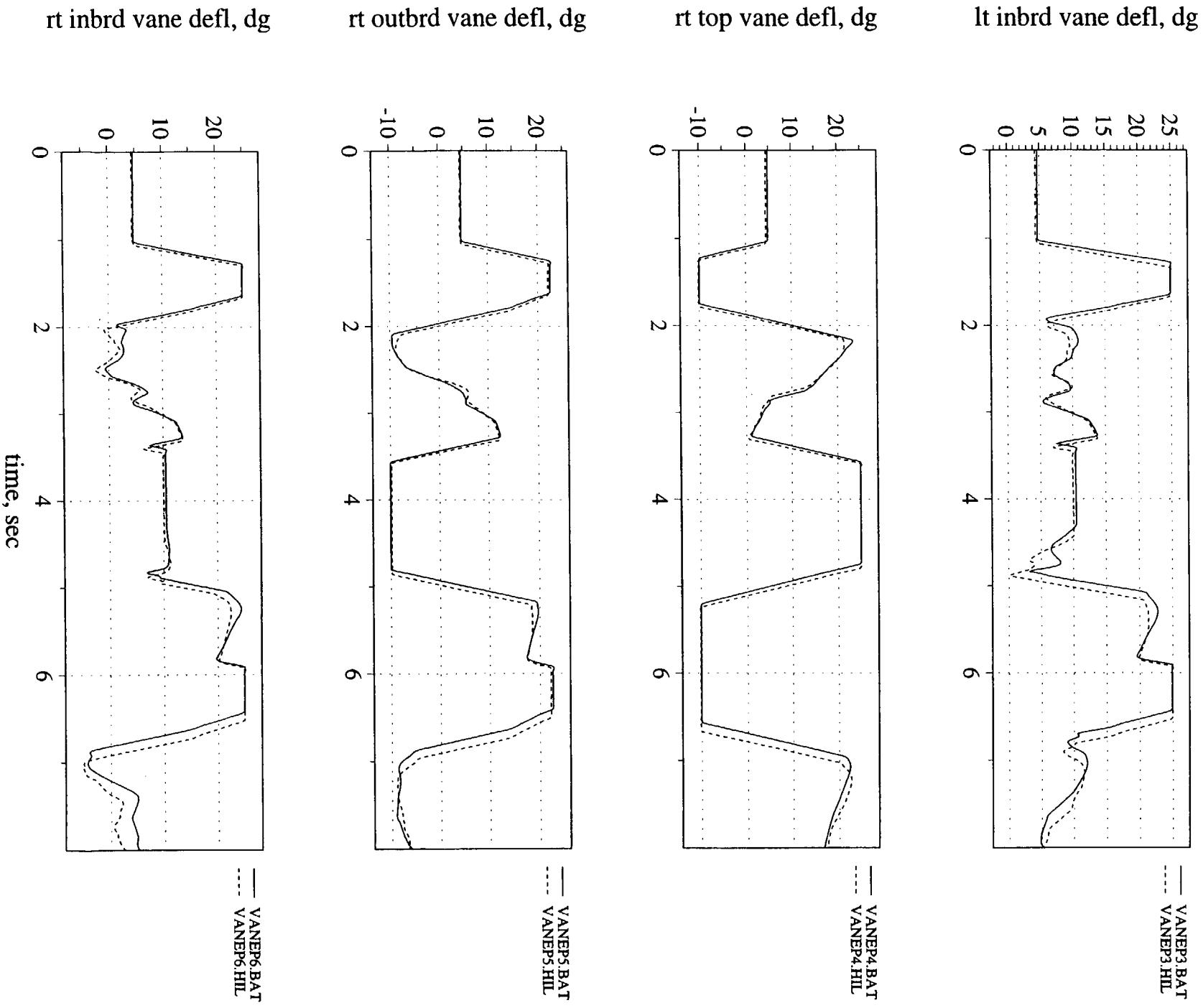
Appendix B - Batch versus HIL Version 154.0
CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 9/12



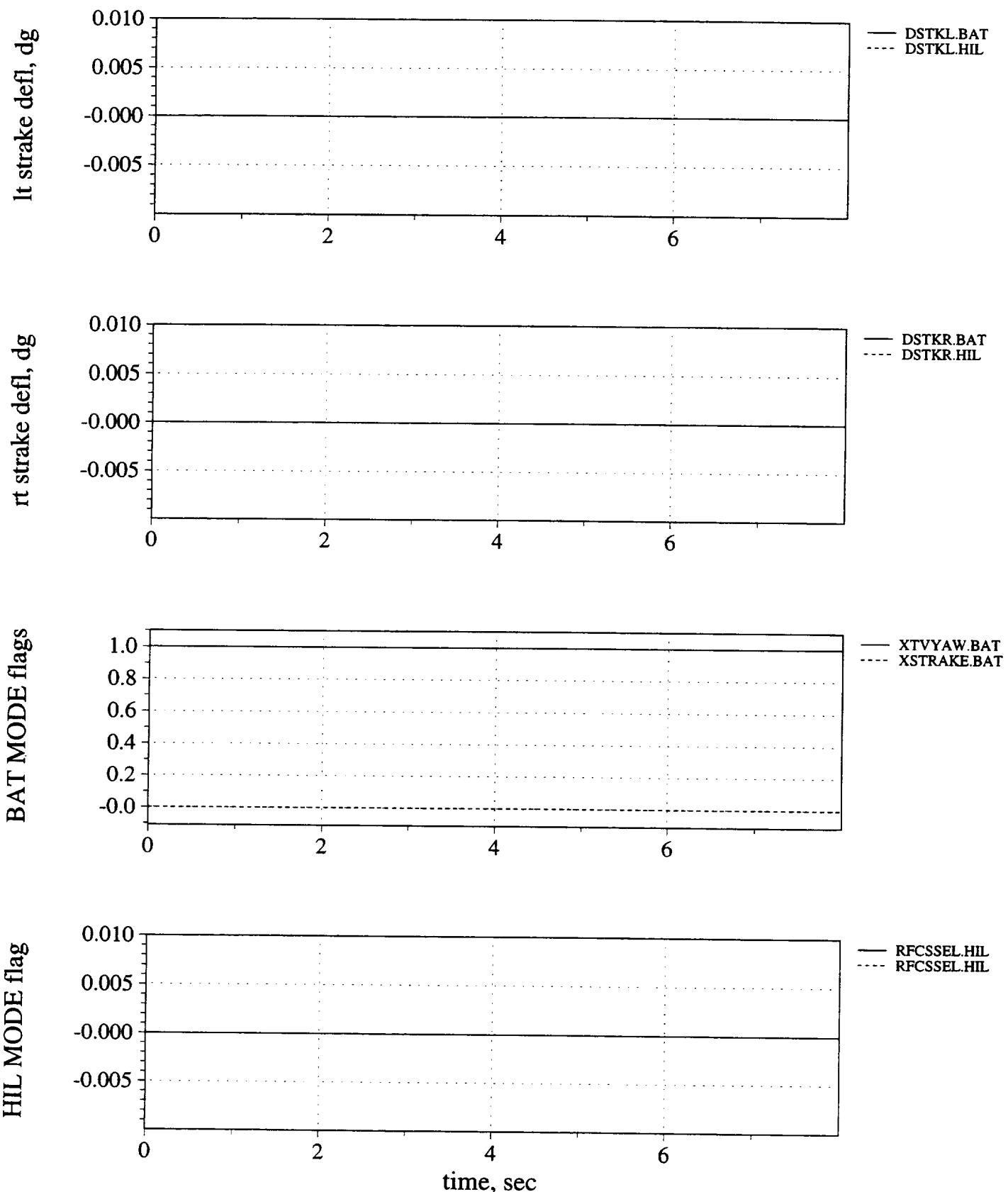
Performance Validation Checkcases

Appendix B - Batch versus HIL Version 154.0

CASE_46_TV_Mode_.35-25k_trim_AOA_3_-2.5_in_ramp_dep_gainset_2 page 10/12

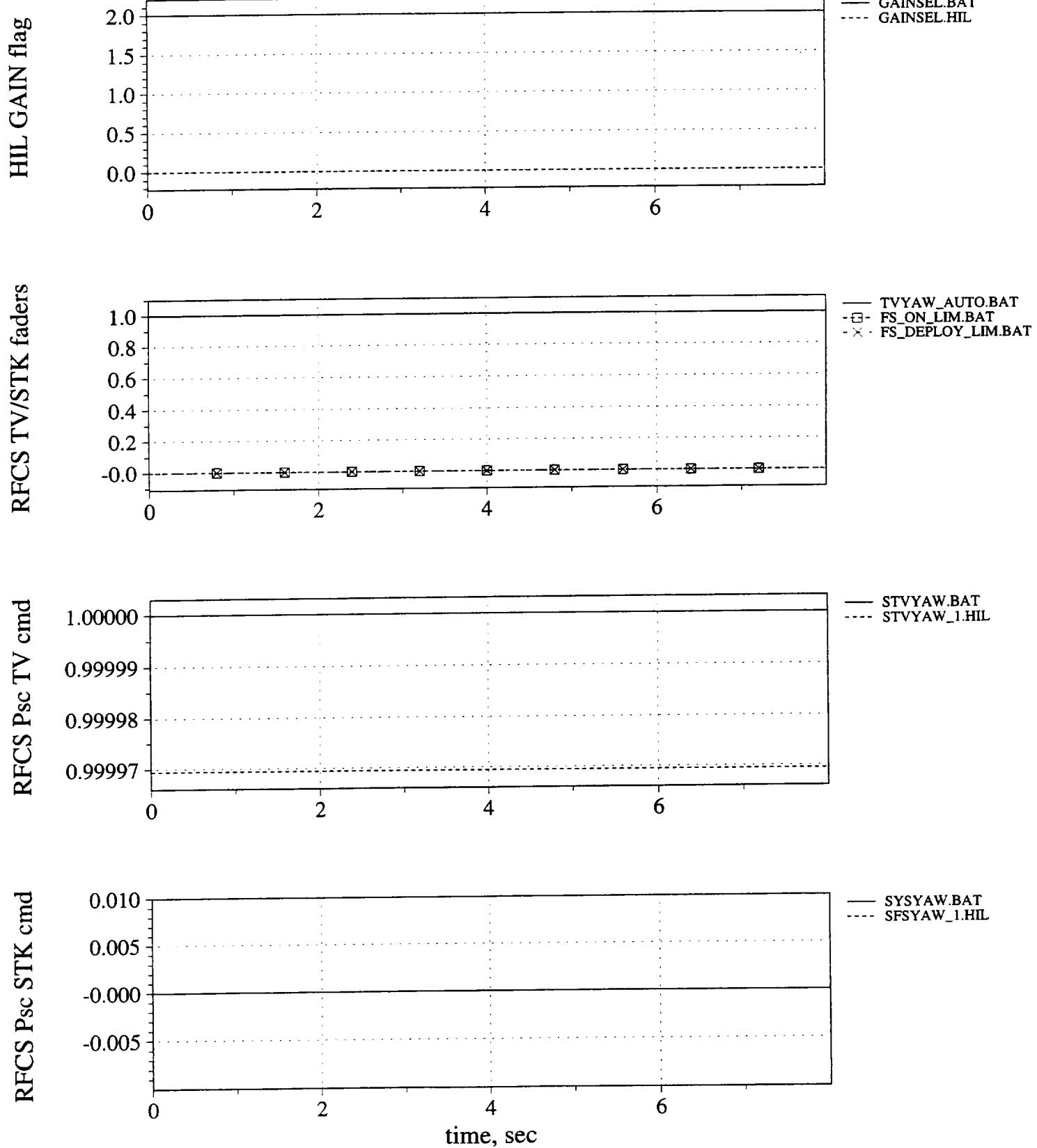


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13. ABSTRACT (Maximum 200 words)			A new software release designated version 154.0 has been developed and tested for use on the F/A-18 High Angle-of-Attack Research Vehicle (HARV). This software release incorporates all software changes to the Actuated Nose Strakes for Enhanced Rolling (ANSER) control laws through version 153.0 with the following exceptions. The first exception is a modification of the On-Board Excitation System (OBES) which replaces the OBES load in version 153.0 with 17 new maneuvers. The second exception is to change the symmetric strake deployment schedule back to the original schedule in ANSER control law version 151.1. The final change replaces two instrumentation parameters. These three modifications make up the new software release for use in the Research Flight Control System (RFCS) flight control computer. This report documents the batch versus Hardware-In-the-Loop Simulation test results for version 154.0.		
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